

Set	Items	Description
S1	42	AU=(ELKO D? OR ELKO, D?)
S2	9	AU=(DIEVENDORFF R? OR DIEVENDORFF, R?)
S3	22	AU=(FLAHERTY D? OR FLAHERTY, D?)
S4	77	AU=(NICK J? OR NICK, J?)
S5	22	AU=(SURMAN D? OR SURMAN, D?)
S6	6	AU=(WARNES J? OR WARNES, J?)
S7	42	AU=(WESTCOTT D? OR WESTCOTT, D?)
S8	0	S1 AND S2 AND S3 AND S4 AND S5 AND S6 AND S7
S9	5	(S1 OR S2 OR S3 OR S4 OR S5 OR S6) AND MESSAGE()QUEUE
S10	10	(S1 OR S2 OR S3 OR S4 OR S5 OR S6) AND IC=(G06F-007? OR G0-6F-017?)
S11	15	S9 OR S10
S12	15	IDPAT (sorted in duplicate/non-duplicate order)
S13	14	IDPAT (primary/non-duplicate records only)
File 344:Chinese Patents Abs Aug 1985-2003/Jan		
(c) 2003 European Patent Office		
File 347:JAPIO Oct 1976-2002/Nov(Updated 030306)		
(c) 2003 JPO & JAPIO		
File 348:EUROPEAN PATENTS 1978-2003/Mar W03		
(c) 2003 European Patent Office		
File 349:PCT FULLTEXT 1979-2002/UB=20030327,UT=20030320		
(c) 2003 WIPO/Univentio		
File 350:Derwent WPIX 1963-2003/UD,UM &UP=200321		
(c) 2003 Thomson Derwent		

Examiner Pannala:

Attached please find the results of your search request re:
Method for implementing a shared message queue...

Please let me know if you would like to try a refocused search with a
different strategy or additional terminology.

David Holloway
308-7794

Set	Items	Description
S1	110905	MESSAGE OR MESSAGING
S2	6908	S1(2N) (MANAG? OR INTERFACE? OR QUEUE? OR MIDDLEWARE?) OR MQ OR MQI
S3	219158	COMMIT? OR UNCOMMIT?
S4	560770	DEFINED() (ORDER? OR RANK? OR ORDER?) OR LIST()ENTRY? OR LI- ST()STRUCTURE? OR KEY? ?
S5	9053725	VALUE? OR HIGHEST? OR LOWEST? OR EXTREME? OR FIRST? OR LA- ST? OR GREATEST? OR LEAST? OR MOST?
S6	0	S2 AND S3 AND S4 AND S5
S7	37	S2 AND S3
S8	209	S2 AND S4
S9	46	S8 AND S5
S10	114	S2(5N) (SHARE? ? OR SHARING)
S11	1	S10(S)LIST? ?
S12	3	S10 AND S3
S13	1	S10 AND LIST? ?
S14	2	S10 AND (GET? ? OR PUT? ?)
S15	74	S2 AND (LIST? ? OR PUT? ? OR GET? ?) AND (S3 OR S4 OR S5)
S16	7	(SHARE? OR SHARING) AND S15
S17	10	S7 AND (S4 OR S5)
S18	19	S11 OR S12 OR S13 OR S14 OR S16 OR S17
S19	15	RD (unique items)
S20	4	S7 AND (S4 OR S5) AND (SHARE? OR SHARING OR LIST? ?)
S21	17	S19 OR S20
S22	15	RD (unique items)
S23	14	S22 NOT PY>2000
S24	13	S23 NOT PD>20001002
File	8: Ei Compendex(R) 1970-2003/Mar W4	(c) 2003 Elsevier Eng. Info. Inc.
File	35: Dissertation Abs Online 1861-2003/Mar	(c) 2003 ProQuest Info&Learning
File	65: Inside Conferences 1993-2003/Mar W5	(c) 2003 BLDSC all rts. reserv.
File	2: INSPEC 1969-2003/Mar W4	(c) 2003 Institution of Electrical Engineers
File	94: JICST-EPlus 1985-2003/Mar W5	(c) 2003 Japan Science and Tech Corp(JST)
File	111: TGG Natl. Newspaper Index(SM) 1979-2003/Mar 28	(c) 2003 The Gale Group
File	233: Internet & Personal Comp. Abs. 1981-2003/Feb	(c) 2003 Info. Today Inc.
File	144: Pascal 1973-2003/Mar W4	(c) 2003 INIST/CNRS
File	434: SciSearch(R) Cited Ref Sci 1974-1989/Dec	(c) 1998 Inst for Sci Info
File	34: SciSearch(R) Cited Ref Sci 1990-2003/Mar W4	(c) 2003 Inst for Sci Info
File	99: Wilson Appl. Sci & Tech Abs 1983-2003/Feb	(c) 2003 The HW Wilson Co

24/5/1 (Item 1 from file: 8)

DIALOG(R)File 8: Ei Compendex(R)

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04071024 E.I. No: EIP95022564530

Title: GTW: a time warp system for shared memory multiprocessors

Author: Das, Samir; Fujimoto, Richard; Panesar, Kiran; Allison, Don; Hybinette, Maria

Corporate Source: Georgia Inst of Technology, Atlanta, GA, USA

Conference Title: Proceedings of the 1994 Winter Simulation Conference

Conference Location: Buena Vista, FL, USA Conference Date: 19941211-19941214

E.I. Conference No.: 42449

Source: Winter Simulation Conference Proceedings 1994. IEEE, Piscataway, NJ, USA. p 1332-1339

Publication Year: 1994

CODEN: WSCPKD ISSN: 0275-0708

Language: English

Document Type: CA; (Conference Article) Treatment: A; (Applications)

Journal Announcement: 9504W3

Abstract: The design of the Georgia Tech Time Warp (GTW, version 2.0) executive for cache-coherent **shared** -memory multiprocessors is described. The programmer's interface is presented. Several optimizations used to efficiently realize **key** functions such as event **list** manipulation, memory and buffer **management**, and **message** passing are discussed. An efficient algorithm for computing GVT on **shared** -memory multi-processors is described. Measurements of a wireless personal communication services (PCS) network simulation indicate the GTW simulator is able to sustain performance as high as 335,000 **committed** events per second for this application on a 42-processor KSR-2 machine. (Author abstract) 12 Refs.

Descriptors: *Program processors; Multiprocessing systems; Optimization; Computer program listings; Data storage equipment; Buffer storage; Data transfer; Algorithms; Computer simulation

Identifiers: Time warp system; **Shared** memory multiprocessors; Programmers interface; **List** manipulation; Buffer **management**; **Message** passing; Network simulation

Classification Codes:

723.1 (Computer Programming); 723.2 (Data Processing); 723.5 (Computer Applications)

723 (Computer Software)

72 (COMPUTERS & DATA PROCESSING)

24/5/2 (Item 2 from file: 8)

DIALOG(R)File 8: Ei Compendex(R)

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02809220 E.I. Monthly No: EIM8910-038440

Title: Implementation and measurements of efficient communication facilities for distributed database systems.

Author: Bhargava, Bharat; Mafla, Enrique; Riedl, John; Sauder, Bradley

Corporate Source: Purdue Univ, Dep of Computer Science, West Lafayette, IN, USA

Conference Title: Proceedings: Fifth International Conference on Data Engineering

Conference Location: Los Angeles, CA, USA Conference Date: 19890206

E.I. Conference No.: 12353

Source: Proc Fifth Int Conf Data Eng. Publ by IEEE, IEEE Service Center, Piscataway, NJ, USA. Available from IEEE Service Cent (cat n 89CH2695-5), Piscataway, NJ, USA. p 200-207

Publication Year: 1989

ISBN: 0-8186-1915-5

Language: English

Document Type: PA; (Conference Paper) Treatment: X; (Experimental)

Journal Announcement: 8910

Abstract: Experimentation with several methods of providing efficient communication facilities for distributed database systems is described. These studies give insight into the delays incurred by applications running

on distributed systems. Five different mechanisms for local interprocess communication (two variations with **message queues**, named pipes, **shared memory**, and UDP sockets) have been implemented, compared, and analyzed. The **most** efficient of these is three times as fast as UDP for 1000-byte messages. Kernel-level software multicast and hardware multicast have also been implemented and their performance analyzed. The results show the significant advantage of using these techniques instead of using multiple sends and receives at the user level. The design of a facility that allows the dynamic addition of user-level protocols such as two-phase **commit**, clock synchronization, etc. to an operating system kernel is presented. The facility is based on a simple stack-based language that provides the functionality and security required. 12 refs.

Descriptors: *DATABASE SYSTEMS--*Distributed; COMPUTER OPERATING SYSTEMS; DATA TRANSMISSION

Identifiers: LOCAL INTERPROCESS COMMUNICATION; MULTICASTING; COMMUNICATION PROTOCOLS

Classification Codes:

723 (Computer Software)

72 (COMPUTERS & DATA PROCESSING)

24/5/3 (Item 1 from file: 35)

DIALOG(R)File 35:Dissertation Abs Online

(c) 2003 ProQuest Info&Learning. All rts. reserv.

01773415 ORDER NO: AADAA-I9984742

Galatea in business organizations: The impact of a non-fictitious intervention on accountants' self-efficacy, work attitudes, and performance

Author: McNatt, Donald Brian

Degree: Ph.D.

Year: 2000

Corporate Source/Institution: The University of Iowa (0096)

Supervisor: Timothy A. Judge

Source: VOLUME 61/08-A OF DISSERTATION ABSTRACTS INTERNATIONAL.

PAGE 3253. 229 PAGES

Descriptors: BUSINESS ADMINISTRATION, MANAGEMENT ; PSYCHOLOGY, INDUSTRIAL ; PSYCHOLOGY, SOCIAL ; BUSINESS ADMINISTRATION, ACCOUNTING

Descriptor Codes: 0454; 0624; 0451; 0272

ISBN: 0-599-90791-6

The Galatea Effect is a special case of self-fulfilling prophecies whereby performance gains are achieved by raising persons' expectations about their own performance capability. This dissertation provides the **first** empirical support for the effectiveness of using non-fictional psychological interventions with new and established adult professionals in a business organization to raise self-efficacy, on-going work performance, and work adjustment. The longitudinal, repeated-measures design of the study detected that, depending upon the outcomes specified, that effects can **last** from several days to several months.

Seventy-one staff-level financial auditors of a Big-5 accounting firm were randomly assigned to experimental and control conditions. The experimental interventions were based on true information obtained from the Firm and consisted of vicarious experience and verbal persuasion communicated to treatment-group employees via an eight-minute interview with me, followed by two letters and one email **message** from Firm **managers** and partners at three-week intervals. This was the **first** field-setting test of a psychological intervention without the use of fictitious information. Performance was measured by uninformed supervisors on a weekly basis for three months. In addition, self-report variables were measured one-week before the initial intervention, the same day as the intervention, and three months later.

Analyses verified that the intervention raised the self-efficacy and motivation of treatment employees through the end of the study. As well, treatment-group employees had higher average work performance over the **first** month. The findings also supported some moderator effects for general self-efficacy (GSE) and amount of time at the Firm. For example, GSE moderated the intervention to performance relationship showing that the

treatment employees higher in GSE had greater performance averaged over the three months. Next, the interventions temporarily increased established employees' job satisfaction, perceived ability to cope, and organizational and professional **commitment**, and decreased their intentions to quit the firm or the profession. However, these effects dwindled sometime before three months. The dissertation expands what has been known regarding the generalizability and duration of self-efficacy interventions to ongoing work settings within business organizations.

24/5/4 (Item 2 from file: 35)
DIALOG(R) File 35:Dissertation Abs Online
(c) 2003 ProQuest Info&Learning. All rts. reserv.

01334875 ORDER NO: AAD94-05653
FAULT-TOLERANT DISTRIBUTED SHARED MEMORIES (SHARED MEMORY, SNOOPER)
Author: BROWN, LARRY
Degree: PH.D.
Year: 1993
Corporate Source/Institution: FLORIDA ATLANTIC UNIVERSITY (0119)
ADVISER: JIE WU
Source: VOLUME 54/09-B OF DISSERTATION ABSTRACTS INTERNATIONAL.
PAGE 4770. 251 PAGES
Descriptors: COMPUTER SCIENCE
Descriptor Codes: 0984

Distributed shared memory (DSM) implements a **shared** -memory programming **interface** on **message** -passing hardware. The **shared** -memory programming paradigm offers several advantages over the message-passing paradigm. DSM is recognized as an important technology for massively parallel computing. However, as the number of processors in a system increases, the probability of a failure increases. To be widely useful, the DSM must be able to tolerate failures. This dissertation presents a method of implementing fault-tolerant DSM (FTDSM) that is based on the idea of a snoopers. The snoopers monitors DSM protocol messages and keeps a backup of the current state of the DSM. The snoopers can respond on behalf of failed processors. The snoopers-based FTDSM is an improvement over existing FTDSMs because it is based on the efficient dynamic distributed manager DSM algorithm, does not require the repair of a failed processor in access the DSM, and does not query all nodes to rebuild the state of the DSM. Three snoopers-based FTDSM systems are developed. The single-snoopers (SS) FTDSM has one snoopers and is restricted to a broadcast network. Additional snoopers are added in the multiple-snoopers (MS) FTDSM to improve performance. Two-phase **commit** (2PC) protocols are developed to coordinate the activities of the snoopers, and a special data structure is used to store causality information to reduce the amount of snoopers activity. Snooping is integrated with each processor in the integrated snoopers (IS) FTDSM. The IS FTDSM is scalable because it is not restricted to a broadcast network. The concept of dynamic snooping is introduced for the IS FTDSM and several snoopers migration algorithms are studied. Several recovery algorithms are developed to allow failed processors to rejoin the system. The properties of data structures used to locate owners and snoopers are studied and used to prove that the system can tolerate any single fault. A flexible method of integrating application-level recovery with the FTDSM is presented, and a reliability analysis is conducted using a Markov-chain modeling tool to show that the snoopers-based FTDSM is a cost effective way to improve the reliability of DSM.

24/5/5 (Item 1 from file: 2)
DIALOG(R) File 2:INSPEC
(c) 2003 Institution of Electrical Engineers. All rts. reserv.

5573443 INSPEC Abstract Number: C9706-6150N-039
Title: Emulating shared memory to simplify distributed-memory programming
Author(s): Clarke, J.A.
Author Affiliation: US Army Res. Lab., Aberdeen Proving Ground, MD, USA
Journal: IEEE Computational Science and Engineering vol.4, no.1 p.

55-62

Publisher: IEEE,

Publication Date: Jan.-March 1997 Country of Publication: USA

CODEN: ISCEE4 ISSN: 1070-9924

SICI: 1070-9924(199701/03)4:1L:55:ESMS;1-1

Material Identity Number: B482-97002

U.S. Copyright Clearance Center Code: 1070-9924/97/\$10.00

Language: English Document Type: Journal Paper (JP)

Treatment: Practical (P)

Abstract: A system called Network Distributed Global Memory simplifies parallel computing in a distributed memory environment by allowing processors to be programmed as though they had **shared** memory. NDGM **manages** **message** passing; applications perform **puts** and **gets** to a virtual buffer. Porting a 3D fluid dynamics code to NDGM was much easier than writing an explicit message passing version. (9 Refs)

Subfile: C

Descriptors: distributed memory systems; fluid dynamics; message passing; parallel programming; physics computing; software portability; virtual storage

Identifiers: shared memory emulation; distributed memory programming; Network Distributed Global Memory; parallel computing; distributed memory environment; NDGM; message passing management; virtual buffer; 3D fluid dynamics code; virtual storage; VS

Class Codes: C6150N (Distributed systems software); C5440 (Multiprocessing systems); C6120 (File organisation); C7320 (Physics and chemistry computing); C6110B (Software engineering techniques)

Copyright 1997, IEE

24/5/6 (Item 2 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2003 Institution of Electrical Engineers. All rts. reserv.

5481972 INSPEC Abstract Number: C9703-6150E-001

Title: OLE-enabled middleware

Author(s): Linthicum, D.S.

Journal: DBMS vol.10, no.1 p.26, 28, 30

Publisher: Miller Freeman,

Publication Date: Jan. 1997 Country of Publication: USA

ISSN: 1041-5173

SICI: 1041-5173(199701)10:1L:26:EM;1-6

Material Identity Number: M772-97001

Language: English Document Type: Journal Paper (JP)

Treatment: Practical (P)

Abstract: I examine three of the latest component object model (COM)-enabled middleware producers: OLE DB, Microsoft's new TP monitor (code name: Viper), and Microsoft's MOM (code name: Falcon). I answer the question: Can Microsoft be successful in the high-end middleware market? I see each middleware product serving a specific need. Falcon will provide just another **messaging** **middleware** solution, offering an information bus of sorts for disbursed applications that need to **share** information. Using the COM interface, Falcon already has many tools that can make use of Falcon without new libraries, APIs, or DLLs. Viper will provide low-end TP monitor services for existing Windows NT shops, but the jury is still out as to how Viper will handle larger user loads, which is the real **value** of TP monitors. OLE DB will provide another layer of abstraction for data access, but **most** client/server developers won't exploit the power of OLE DB - they really just need to **get** at the data and can take or leave OLE. (0 Refs)

Subfile: C

Descriptors: application program interfaces; client-server systems; object-oriented databases; software packages; utility programs

Identifiers: OLE-enabled middleware; component object model; OLE DB; Microsoft; TP monitor; Viper; MOM; Falcon; high-end middleware; **messaging** **middleware**; information bus; disbursed applications; Windows NT; data access; client/server

Class Codes: C6150E (General utility programs); C6150N (Distributed systems software); C6160J (Object-oriented databases)

24/5/7 (Item 3 from file: 2)

DIALOG(R) File 2:INSPEC

(c) 2003 Institution of Electrical Engineers. All rts. reserv.

4685862 INSPEC Abstract Number: B9407-6210G-007, C9407-7104-029

Title: Structured design of a knowledge-based message dissemination system

Author(s): Higa, K.; Aiken, M.W.; Liu Sheng, O.R.

Author Affiliation: Dept. of Bus. Inf. Syst., Hong Kong Univ. of Sci. and Technol., Kowloon, Hong Kong

Journal: International Journal of Software Engineering and Knowledge Engineering vol.4, no.1 p.61-80

Publication Date: March 1994 Country of Publication: Singapore

CODEN: ISEKEW

Language: English Document Type: Journal Paper (JP)

Treatment: Practical (P)

Abstract: The paper **first** provides a brief survey of several existing intelligent EMSs which seek to address the conundrum of mail management and then proposes an alternative solution which takes advantage of the unique characteristics of a knowledge base/database coupling to facilitate effective information **sharing** among members of an organization. This approach improves on prior Knowledge-Based Mail System (KMS) designs by (1) focusing on **message dissemination management** rather than on **message receipt management**, (2) separating the relatively static knowledge of message routing from the relatively dynamic knowledge of the organizational membership, and (3) incorporating a more comprehensive view of the semantic constraints involved in configuring distribution **lists** of message receivers through the use of the Structured Object Model (SOM) Methodology. The design concepts are illustrated by a prototype KMS, the Message Dissemination System (MDS). (34 Refs)

Subfile: B C

Descriptors: electronic mail; knowledge based systems; message passing

Identifiers: knowledge-based; message dissemination system; mail management; EMS; knowledge-based mail system; message routing; semantic constraints; electronic mail; knowledge base/database coupling; structured design

Class Codes: B6210G (Electronic mail); C7104 (Office automation); C6170 (Expert systems)

24/5/8 (Item 4 from file: 2)

DIALOG(R) File 2:INSPEC

(c) 2003 Institution of Electrical Engineers. All rts. reserv.

03246546 INSPEC Abstract Number: B88067021, C88063613

Title: Management of messages and CCITT X.400 directive

Author(s): Schutt, T.E.; Staton, J.B., III; Racke, W.F.

Journal: Sistemi e Automazione vol.34, no.291 p.360-75

Publication Date: April 1988 Country of Publication: Italy

CODEN: SSAUBD ISSN: 0037-5896

Language: Italian Document Type: Journal Paper (JP)

Treatment: Practical (P)

Abstract: This article gives a panoramic view on directive CCITT X.400 and includes a description of two prototypes. The science of **message management** commenced in the 1970s, and the article gives the history from then to the present day. The main concepts and terminology of X.400 are given, followed by an explanation of relationships with ISO standards. The particular attractions of X.400 are given at some length as well as descriptions of particular functions such as the transfer of messages and similar activities. The **last** sections describe a prototype X.400 for the VM/SP operating system and include notes on future standardisation **commitments**. (26 Refs)

Subfile: B C

Descriptors: operating systems (computers); protocols; standardisation; standards

Identifiers: protocols; CCITT X.400; **message management**; ISO

standards; VM/SP; operating system; standardisation

Class Codes: B6210L (Computer communications); C5620 (Computer networks and techniques); C6150J (Operating systems)

24/5/9 (Item 1 from file: 111)

DIALOG(R)File 111:TGG Natl.Newspaper Index(SM)

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05243965 Supplier Number: 19731521

Boole & Babbage First to Provide End-to-End Availability Management for Microsoft Message Queue Server, Code-Named Falcon; Extensions to Command MQ Management Solution Underscore Boole & Babbage Commitment to Rapid-Growth Middleware Market.

Business Wire, p9090005

Sep 9, 1997

LANGUAGE: English RECORD TYPE: Citation

COMPANY NAMES: Boole and Babbage Inc.--Product introduction

DESCRIPTORS: Computer software industry--Product introduction

PRODUCT NAMES: 7372605 (Electronic Mail Software)

SIC CODES: 7372 Prepackaged software

TICKER SYMBOLS: BOOL

FILE SEGMENT: NW File 649

24/5/10 (Item 1 from file: 233)

DIALOG(R)File 233:Internet & Personal Comp. Abs.

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00522649 99SD01-003

Exploring Java for the enterprise -- Java's broad scope should eventually bridge many of the gaps across the enterprise. In the meantime, your development plan should let...

Kelly, Chip

Software Development , January 1, 1999 , v7 n1 p43-47, 5 Page(s)

ISSN: 0749-2839

Languages: English

Document Type: Articles, News & Columns

Geographic Location: United States

Focuses on using Java as an enterprise-wide development environment. Says that the Java Futures **Committee** of the SAS Institute sought to determine how Java would function in the areas of intelligent clients, intelligent servers, and intelligent storage. Says that an important consideration when planning intelligent Java clients is to take advantage of Java's object orientation. Advises creating one's own components and using vendor-supplied components. Explains that the intelligent server is the central point of communications for enterprise-scale applications, the powerhouse of the enterprise application architecture. Notes that platform independence is one of Java's **key** uses in the construction of intelligent servers. Says Java is one of the only platform-neutral solutions to incorporate multithreaded kernels, persistent state maintenance, **message** queuing **interfaces** , and exploitation of open connectivity standards. (jo)

Descriptors: Java; Enterprise Computing; Client-Server Computing; Architecture; Application Development

24/5/11 (Item 2 from file: 233)

DIALOG(R)File 233:Internet & Personal Comp. Abs.

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00385271 95DD05-002

Shared memory and message queues : C++ classes for OS/2, AIX, and Windows NT

Lam, Richard B

Dr. Dobb's Journal , May 1, 1995 , v20 n5 p28, 30, 97, 3 Page(s)

ISSN: 1044-789X

Languages: English

Document Type: Feature Articles and News

Geographic Location: United States

Discusses cross-platform coding of named **shared** memory and **message queues**. Says named **shared** memory allows any process with knows the name of the block to gain access to memory and may be the fastest interprocess communication (IPC) mechanism, especially for transferring large structures between processes. Notes that the use of share-memory blacks require careful synchronization in order to prevent the occurrence of subtle bugs in complex programs or systems. Also says message queues are useful for one-way communication between a server and a number of clients but it practical size is limited to small chunks of information. Includes source code for the interface to the shared memory class. Includes a photo and a source code **list**. (dpm)

Descriptors: Memory; Utility Program; Data Communication; Programming Design; Interoperability

24/5/12 (Item 1 from file: 144)

DIALOG(R)File 144:Pascal

(c) 2003 INIST/CNRS. All rts. reserv.

14351035 PASCAL No.: 00-0002095

Automatic profiling of MPI applications with hardware performance counters

Recent advances in parallel virtual machine and message passing interface : Barcelona, 26-29 September 1999

RABENSEIFNER R

DONGARRA Jack, ed; LUQUE Emilio, ed; MARGALEF Tomas, ed

Center for High Performance Computing (ZHR), Dresden University of Technology, Zellescher Weg 12, Willers-Bau A 117, 01062 Dresden, Germany

European PVM/MPI Users' Group meeting, 6 (Barcelona ESP) 1999-09-26

Journal: Lecture notes in computer science, 1999, 1697 35-42

ISBN: 3-540-66549-8 ISSN: 0302-9743 Availability: INIST-16343;
354000080038090050

No. of Refs.: 11 ref.

Document Type: P (Serial); C (Conference Proceedings) ; A (Analytic)

Country of Publication: Germany

Language: English

This paper presents an automatic counter instrumentation and profiling module added to the MPI library on Cray T3E and SGI Origin2000 systems. A detailed summary of the hardware performance counters and the MPI calls of any MPI production program is gathered during execution and written in MPIFinalize on a special syslog file. The user can **get** the same information in a different file. Statistical summaries are computed weekly and monthly. The paper describes experiences with this library on the Cray T3E systems at HLRS Stuttgart and TU Dresden. It focuses on the problems integrating the hardware performance counters into MPI counter profiling and presents **first** results with these counters. Also, a second software design is described that allows the integration of the profiling layer into a dynamic **shared** object MPI library without consuming the user's PMPI profiling interface.

English Descriptors: Computer system; Distributed memory systems;

Application program interfaces; System architecture; Parallel architectures; System performance

French Descriptors: Systeme informatique; Systeme memoire repartie;

Interface programme application; Architecture systeme; Architecture parallele; Performance systeme

Classification Codes: 001D02B10; 001D03J11

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24/5/13 (Item 1 from file: 34)

DIALOG(R)File 34:SciSearch(R) Cited Ref Sci
(c) 2003 Inst for Sci Info. All rts. reserv.

03585435 Genuine Article#: PP372 Number of References: 22

Title: REVISION OF SOREX-ARANEUS L CHROMOSOME NOMENCLATURE, AND RACE N NEW
TO FINLAND

Author(s): HALKKA L; KAIKUSALO A; VAKULA N

Corporate Source: UNIV HELSINKI, DEPT GENET, POB 17, ARKADIAKATU 7/SF-00014
HELSINKI//FINLAND/; STATE FOREST RES STN OJAJOKI/SF-12700
LOPPI//FINLAND/

Journal: ANNALES ZOOLOGICI FENNICI, 1994, V31, N3, P283-288

ISSN: 0003-455X

Language: ENGLISH Document Type: REVIEW

Geographic Location: FINLAND

Subfile: SciSearch; CC AGRI--Current Contents, Agriculture, Biology &
Environmental Sciences

Journal Subject Category: ZOOLOGY

Abstract: The original chromosome arm nomenclature of the chromosomal races of *Sorex araneus* L. is based on the Kuhmo (Kuusijoki) population, of Northeastern Finland (Halkka et al. 1974). The North-Finnish and North-Swedish chromosome races have prior to this study been thought to be the same chromosomal race, with racial arm combinations gm, lj, hn, ip, kq and or. We have earlier (1987) cautioned of possible differences in the use of arm symbols m and o in Finland and the rest of Europe. The International *Sorex araneus* Cytogenetics Committee (ISACC, 1991) has decided to adopt the nomenclature most widespread in Europe, based on the figures of Fredga & Nawrin (1977) from Sweden. Thus, the original Finnish racial nomenclature has to be converted to that suggested by the ISACC. Now, we have found a new chromosome race in Finland from Kilpisjärvi, in the Finnish Lapland. This finding corroborates the racial difference between Northern Finland and Northern Sweden. This race is apparently the same as the race N of Swedish Lapland (S-race N = FIN-race N), with racial arm combinations gm, jl, hn, ip, kq and or (ISACC). The other Finnish races have been converted to the following (ISACC): FIN-race I (Northern Finland): go, jl, hn, ip, kq and mr; FIN-race II (Eastern tip of Finland): og, jl, hn, ip, kq and mr; FIN-race III (Southeastern Finland): gq, jl, hk, ip, mo and nr; FIN-race IV (Central and Southern Finland): gq, jl, hn, ip, ko and mr; FIN-race V (Western coast of Finland): g, k, jl, ip, mq and or; FIN-race VI: (angstrom-land): Islands): jl, ip, g, h, k, m, n, o, q, r. A revised hypothesis on the evolution cascade in the eastern racial group of *Sorex araneus* L. is presented and discussed.

Identifiers--KeyWords Plus: KARYOTYPIC RACES; POLYMORPHISM; EVOLUTION;
SWEDEN

Cited References:

BENGTSSON BO, 1990, V3, P85, J EVOLUTION BIOL
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HALKKA L, 1994, V31, P289, ANN ZOOL FENN
HALKKA L, 1974, V76, P305, HEREDITAS

Set	Items	Description
S1	89135	MESSAGE OR MESSAGING
S2	2945	S1(2N) (MANAG? OR INTERFACE? OR QUEUE? OR MIDDLEWARE?) OR MQ OR MQI
S3	2322	COMMIT? OR UNCOMMIT?
S4	202062	DEFINED() (ORDER? OR RANK? OR ORDER?) OR LIST()ENTRY? OR LI- ST()STRUCTURE? OR KEY? ?
S5	4422613	VALUE? OR HIGHEST? OR LOWEST? OR EXTREME? OR FIRST? OR LA- ST? OR GREATEST? OR LEAST? OR MOST?
S6	1	S2 AND S3 AND S4
S7	10	S2 AND S3
S8	106	S2 AND S4
S9	26	S8 AND S5
S10	35	S6 OR S7 OR S9
S11	2	S8 AND MC=(T01-F02C2 OR T01-N01C OR T01-J05B3 OR T01-G03)
S12	1	S10 AND IC=G06F-007?
S13	844	S2 AND (S3 OR S4 OR S5)
S14	7	S13 AND IC=G06F-007?
S15	302	S13 AND IC=G06F?
S16	16	S10 AND IC=G06F?
S17	24	S6 OR S7 OR S11 OR S12 OR S14 OR S16
S18	24	IDPAT (sorted in duplicate/non-duplicate order)
S19	24	IDPAT (primary/non-duplicate records only)
S20	11	SHARE?(3N)S2
S21	11	S20 NOT S17
S22	9	S21 AND IC=G06F?
S23	9	IDPAT (sorted in duplicate/non-duplicate order)
S24	9	IDPAT (primary/non-duplicate records only)

File 344:Chinese Patents Abs Aug 1985-2003/Jan

(c) 2003 European Patent Office

File 347:JAPIO Oct 1976-2002/Nov(Updated 030306)

(c) 2003 JPO & JAPIO

File 350:Derwent WPIX 1963-2003/UD,UM &UP=200321

(c) 2003 Thomson Derwent

24/5/1 (Item 1 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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014343408

WPI Acc No: 2002-164111/200221

XRPX Acc No: N02-125324

**Partitioned applications communication running on CPU at timed intervals
in Integrated Modular Avionics system by placing messages from
applications in circular queues in shared memory**

Patent Assignee: HONEYWELL INT INC (HONE)

Inventor: ABOUTABL M S; YOUNIS M

Number of Countries: 091 Number of Patents: 003

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
WO 200186442	A2	20011115	WO 2001US14895	A	20010509	200221 B
AU 200174823	A	20011120	AU 200174823	A	20010509	200224
US 20020144010	A1	20021003	US 2000202984	A	20000509	200267
			US 2001821601	A	20010329	

Priority Applications (No Type Date): US 2001821601 A 20010329; US
2000202984 P 20000509

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
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WO 200186442	A2	E	45	G06F-009/54
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Designated States (National): AE AL AM AT AU AZ BA BB BG BR BY CA CH CN
CR CU CZ DE DK DM EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP
KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX NO NZ PL PT RO RU SD SE
SG SI SK SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW

Designated States (Regional): AT BE CH CY DE DK EA ES FI FR GB GH GM GR
IE IT KE LS LU MC MW MZ NL OA PT SD SE SL SZ TR TZ UG ZW

AU 200174823	A	G06F-009/54	Based on patent WO 200186442
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US 20020144010	A1	G06F-009/46	Provisional application US 2000202984
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Abstract (Basic): WO 200186442 A2

NOVELTY - A circular message queue is registered in a central channel registry table maintained by the system executive and listing the partitioned applications which are allowed to read each outgoing message. A library routine verifies that the outgoing messages are properly addressed and are not corrupted.

DETAILED DESCRIPTION - The messages are then read directly by those partitioned applications which are authorized to read the messages in a read only access from the shared memory. The partitioned applications run at a lower priority than the system executive and access the CPU (Central Processing Unit) at timed intervals using protected memory space. Outgoing messages are arranged in circular **message queues** in **shared** memory allocated by the system executive. Each partitioned application stores its outgoing messages in its own allocated shared memory space.

INDEPENDENT CLAIMS are included for:-

- (a) an aircraft avionics system,
- (b) a method for an aircraft avionics system.

USE - In avionics systems.

ADVANTAGE - Reduces the support needed from the system executive for partitioned applications to send messages.

pp; 45 DwgNo 0/9

Title Terms: PARTITION; APPLY; COMMUNICATE; RUN; CPU; TIME; INTERVAL;
INTEGRATE; MODULE; AVIATION; SYSTEM; PLACE; MESSAGE; APPLY; CIRCULAR;
QUEUE; SHARE; MEMORY

Derwent Class: T01; W06

International Patent Class (Main): G06F-009/46 ; G06F-009/54

International Patent Class (Additional): G06F-009/00

File Segment: EPI

24/5/2 (Item 2 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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013895872 **Image available**

WPI Acc No: 2001-380085/200140

XRPX Acc No: N01-278578

Communicating conference status information from a host computer to one or more guest computers, involves opening shared application window on each guest computer to communicate conference status data

Patent Assignee: INTEL CORP (ITLC)

Inventor: DELEEUW W C

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 6173315	B1	20010109	US 96623973	A	19960329	200140 B

Priority Applications (No Type Date): US 96623973 A 19960329

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 6173315	B1	16	G06F-015/16	

Abstract (Basic): US 6173315 B1

NOVELTY - A host application sharing engine (HASE) automatically posts a unique message to a **message queue** associated with a **shared** application program, then monitors the message queue for retrieving the unique message. Upon detecting the message retrieval, the HASE opens a separate shared application window on each of the guest computer systems for communicating the conference status information.

DETAILED DESCRIPTION - An INDEPENDENT CLAIM is also included for an apparatus for communication conference status information.

USE - Communicating conference status information from a host computer system to one or more guest computer systems.

ADVANTAGE - Improves communication of the computer systems participating in the computer conference since conference notification messages are created within the context of the shared application.

DESCRIPTION OF DRAWING(S) - The figure shows the representation of the interactions between two shared applications, the host application sharing engine, and the operating system when a notification-triggering event occurs.

pp; 16 DwgNo 5/10

Title Terms: COMMUNICATE; CONFER; STATUS; INFORMATION; HOST; COMPUTER; ONE; MORE; GUEST; COMPUTER; OPEN; SHARE; APPLY; WINDOW; GUEST; COMPUTER; COMMUNICATE; CONFER; STATUS; DATA

Derwent Class: T01; W01; W02

International Patent Class (Main): G06F-015/16

File Segment: EPI

24/5/3 (Item 3 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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011300246 **Image available**

WPI Acc No: 1997-278151/199725

XRPX Acc No: N97-230429

Message communication system - Performing transmission and reception of message between processes through signal states shown by Mutex

Patent Assignee: NTT DATA TSUSHIN KK (NITE)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 9101901	A	19970415	JP 95286570	A	19951006	199725 B

Priority Applications (No Type Date): JP 95286570 A 19951006

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
JP 9101901	A	9	G06F-009/46	

Abstract (Basic): JP 9101901 A

The system involves a personal computer which operates in

multiprocess operating system. Multiple processes (2,4) share the memory area of the computer. A message queue mechanism (6) is formed by the share memory area and transmits and receives message between processes formed on the **share** memory area. A **message queue** (8) is provided at the common memory area of the processes. When there is no process which has accessed the common memory area, a mutex (10) shows a signal state and an empty semaphore (12) shows a signal state when there is a vacancy in the queue, a waiting semaphore (14) shows a signal state.

A transmitting process writes a new message in the queue when the mutex and the empty semaphore are in the signal state and the receiving process takes out the message from the queue.

ADVANTAGE - Realizes bidirection communication between processes.

Dwg.1/3

Title Terms: MESSAGE; COMMUNICATE; SYSTEM; PERFORMANCE; TRANSMISSION;

RECEPTION; MESSAGE; PROCESS; THROUGH; SIGNAL; STATE

Derwent Class: T01

International Patent Class (Main): G06F-009/46

File Segment: EPI

24/5/4 (Item 4 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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011154215 **Image available**

WPI Acc No: 1997-132139/199712

XRPX Acc No: N97-109163

Instruction dequeuing method for improved message passing - involves obtaining new queue bank descriptor for queue entry to be dequeued and updating queue links to point to the second queue entry in selected queue

Patent Assignee: UNISYS CORP (BURS)

Inventor: ALFERNES M H; CALDARALE C R; JOHNSON D C; JOHNSON D R; MCBREEN J R; WARD W D

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5602998	A	19970211	US 94362638	A	19941222	199712 B

Priority Applications (No Type Date): US 94362638 A 19941222

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 5602998	A		19	G06F-009/46	

Abstract (Basic): US 5602998 A

The method for dequeuing instructions involves calculating the address of the queue bank descriptor of the queue header of a queue selected by the operands of the instruction. The queue header and the queue entries of the selected queue are selected by referencing the queue bank descriptor of the queue header of the selected queue.

A new queue bank descriptor is obtained for a queue entry to be dequeued from the front of the selected queue. The queue links in the control area of the queue header are updated to point to the second queue entry in the selected queue. The queue entry is mapped to the new queue bank descriptor to provide visibility to the contents of the queue entry for the receiving process.

USE/ADVANTAGE - Removes message data to be transferred from sending process to receiving process from **shared queue** without copying **message** data. Communicates processes in one instruction. Has instruction set support for improved message passing and process synchronisation capabilities.

Dwg.1/11

Title Terms: INSTRUCTION; METHOD; IMPROVE; MESSAGE; PASS; OBTAIN; NEW;

QUEUE; BANK; DESCRIBE; QUEUE; ENTER; UPDATE; QUEUE; LINK; POINT; SECOND;

QUEUE; ENTER; SELECT; QUEUE

Derwent Class: T01

International Patent Class (Main): G06F-009/46

File Segment: EPI

24/5/5 (Item 5 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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010928043 **Image available**
WPI Acc No: 1996-424994/199642

Inter-process communication system for computer system - provides hierarchical queuing which allows sending process to collect multiple message segments as entries in local sub-queue

Patent Assignee: UNISYS CORP (BURS)

Inventor: ALFERNESS M H; CALDARALE C R; JOHNSON D R; KERZMAN J P; MCBREEN J R

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5555396	A	19960910	US 94362034	A	19941222	199642 B

Priority Applications (No Type Date): US 94362034 A 19941222

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 5555396	A	8	G06F-012/00	

Abstract (Basic): US 5555396 A

The inter-process communication system uses a shared memory queue as a mechanism for message passing and process synchronisation. Data to be transferred from a sending process to a receiving process is stored in a queue entry on the shared memory queue. Hierarchical queuing allows a sending process to collect multiple message segments as entries in a local sub-queue, which is enqueued as a single entity to the **shared memory queue** when all **message** segments are present.

The receiving process dequeues the sub-queue in one operation, thereby increasing the efficiency of message transfer while preventing the erroneous dequeuing of message segments when multiple receiving processes are waiting on the same shared memory queue. In this manner, the logical maximum size of a message being passed between processes is expanded.

ADVANTAGE- Expands logical size of queue entry because it allows single entry to actually consist of nested queue

Dwg.3/3

Title Terms: INTER; PROCESS; COMMUNICATE; SYSTEM; COMPUTER; SYSTEM; HIERARCHY; QUEUE; ALLOW; SEND; PROCESS; COLLECT; MULTIPLE; MESSAGE; SEGMENT; ENTER; LOCAL; SUB; QUEUE

Derwent Class: T01

International Patent Class (Main): G06F-012/00

International Patent Class (Additional): G06F-012/08

File Segment: EPI

24/5/6 (Item 6 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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010241970 **Image available**
WPI Acc No: 1995-143225/199519
XRPX Acc No: N95-112879

E-mail system - employs grant mail processor for updating mail information in share mail management unit

Patent Assignee: MATSUSHITA DENKI SANGYO KK (MATU)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 7066831	A	19950310	JP 93214249	A	19930830	199519 B

Priority Applications (No Type Date): JP 93214249 A 19930830

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
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Abstract (Basic): JP 7066831 A

The E-mail system uses multiple terminals (1a-1d) connected to a mail server (3) through a network (4). Several universal interface processors (2a-2d) provide user interface for mail creation and indication regarding transmission/reception. A share mail memory (9) stores the E-mail sent from the terminals for simultaneous transmission. A share mail management unit (10) manages the mail information such as storing position and size of each mail stored. Mail processing and transmission/reception control are performed by the processors (6,7,5) in the mail server.

When a grant mail to share the mail is received from the terminal, a grant mail processor (13) processes its contents and updates the mail information in the **share mail manager**. The **message** from each user in a simultaneous transmission group is added sequentially.

ADVANTAGE - Allows updated contents to be seen. Enables user to perform renewal of mail.

Dwg.1/10

Title Terms: SYSTEM; EMPLOY; MAIL; PROCESSOR; UPDATE; MAIL; INFORMATION; SHARE; MAIL; MANAGEMENT; UNIT

Index Terms/Additional Words: SYSTEM; EMPLOY; MAIL

Derwent Class: T01; W01

International Patent Class (Main): H04L-012/54

International Patent Class (Additional): **G06F-013/00** ; H04L-012/58

File Segment: EPI

24/5/7 (Item 7 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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009840383 **Image available**

WPI Acc No: 1994-120239/199415

XRPX Acc No: N94-094188

Asynchronous computer inter process communications arrangement - has pair of processes in which synchronous and asynchronous communications path segments respectively are used for communication

Patent Assignee: AMERICAN TELEPHONE & TELEGRAPH CO (AMTT); AT & T CORP (AMTT)

Inventor: ALLEN J R

Number of Countries: 006 Number of Patents: 003

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 592117	A2	19940413	EP 93307297	A	19930916	199415 B
EP 592117	A3	19940420	EP 93307297	A	19930916	199523
US 5434975	A	19950718	US 92951488	A	19920924	199534

Priority Applications (No Type Date): US 92951488 A 19920924

Cited Patents: No-SR.Pub; 3.Jnl.Ref

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
EP 592117	A2	E	12	G06F-009/46	

Designated States (Regional): DE ES FR GB IT

US 5434975	A	12	G06F-013/376
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EP 592117	A3	G06F-009/46
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Abstract (Basic): EP 592117 A

The interprocess communication arrangement can be grafted onto an operating system such as the UNIX operating system for real time and multiprocessor applications. It overcomes the lack of support in time sharing and uniprocessor operating systems.

Communicating processes (100,103) communicate via datagram messages through logical asynchronous inter-process communications links (110) each having a synchronous segment (101) and an asynchronous segment (102). The links include a message-serving hub process (11) that communicates in a synchronous, buffer and semaphore based, manner with processes (100) that are message senders, and communicates in an

asynchronous, queue and signals based, manner with processes (103) that are message destinations.

ADVANTAGE - Supports both real-time and multiprocessor applications without destroying existing characteristics, features or advantages of the existing operating system.

Dwg.2/8

Title Terms: ASYNCHRONOUS; COMPUTER; INTER; PROCESS; COMMUNICATE; ARRANGE; PAIR; PROCESS; SYNCHRONOUS; ASYNCHRONOUS; COMMUNICATE; PATH; SEGMENT; RESPECTIVE; COMMUNICATE

Derwent Class: T01

International Patent Class (Main): G06F-009/46 ; G06F-013/376

International Patent Class (Additional): G06F-003/00 ; G06F-013/14

File Segment: EPI

24/5/8 (Item 8 from file: 347)

DIALOG(R)File 347:JAPIO

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07194743 **Image available**

METHOD FOR GUARANTEEING ATOMIC PROPERTIES OF MESSAGE PROCESSING, AND DEVICE FOR THE SAME

PUB. NO.: 2002-063146 [JP 2002063146 A]

PUBLISHED: February 28, 2002 (20020228)

INVENTOR(s): YOSHIHARA TADASHI
TAKITA WATARU

APPLICANT(s): NIPPON TELEGR & TELEPH CORP (NTT)

APPL. NO.: 2000-246262 [JP 2000246262]

FILED: August 15, 2000 (20000815)

INTL CLASS: G06F-015/16 ; G06F-009/46 ; G06F-012/00 ; G06F-015/177

ABSTRACT

PROBLEM TO BE SOLVED: To efficiently disperse a processing load and information, without damaging the asynchronization of inter-application communication in a communication system, using message-type communication.

SOLUTION: In this device for guaranteeing the atomic properties of message processing, and the atomic properties of processing for a message group are guaranteed in a communication system, in which an application group dispersed on a network achieves requested processing by communicating with each other by using message type communication. This device is provided with a client to be used by a user for preparing a message group and for transmitting the message group to the communication system; messaging middleware for arbitrating the exchange of the message between the client and the application; other application for executing any processing, according to the received message, and for transmitting the processed result through the **messaging middleware**, as necessary, **shared** data belonging to each application to be processed by some operation according to the message processing; and a logical aggregate managing part for managing the execution of the message processing inside a logical aggregate which is constituted of the application and the messaging middleware.

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24/5/9 (Item 9 from file: 347)

DIALOG(R)File 347:JAPIO

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05523976 **Image available**

LOAD DISTRIBUTION SYSTEM FOR TRANSACTION PROCESSING

PUB. NO.: 09-138776 [JP 9138776 A]

PUBLISHED: May 27, 1997 (19970527)

INVENTOR(s): NAKAJIMA MASAYOSHI

APPLICANT(s): NEC CORP [000423] (A Japanese Company or Corporation), JP
(Japan)

APPL. NO.: 07-321025 [JP 95321025]
FILED: November 15, 1995 (19951115)
INTL CLASS: [6] G06F-015/00 ; G06F-012/00 ; G06F-013/00 ; G06F-015/16

JAPIO CLASS: 45.4 (INFORMATION PROCESSING -- Computer Applications); 45.2
(INFORMATION PROCESSING -- Memory Units)

ABSTRACT

PROBLEM TO BE SOLVED: To efficiently distribute the load of a transaction processing between hosts in a computer system constituted by the plural hosts sharing a data base.

SOLUTION: A communication processor 101 uniformly distributes messages on the transaction processing from a terminal 100 to the hosts 1 and 2. In the respective hosts 1 and 2, CPU load monitor mechanisms 104 monitor the CPU loads and transaction monitor mechanisms 106 the present processing multiplex degrees for the respective classes in job processing programs 1101. When the messages are transferred from the communication processor 101, a message distribution control mechanism 103 causes the transaction processing systems 110 of the self hosts to execute a processing only when CPU of the self hosts are not overloaded and the processing multiplex degree of the class of the job processing program processing the pertinent message does not reach a maximum value. At the time except for the above case, the message is stored in a GMQ(global **message queue**) file 111 **shared** by the hosts and the message processing is requested to the other host via an inter-host communication equipment 116.

Set	Items	Description
S1	75860	MESSAGE OR MESSAGING
S2	16002	S1(2N) (MANAG? OR INTERFACE? OR QUEUE? OR MIDDLEWARE?) OR MQ OR MQI
S3	18179	COMMIT? OR UNCOMMIT?
S4	133823	DEFINED() (ORDER? OR RANK? OR ORDER?) OR LIST()ENTRY? OR LI- ST()STRUCTURE? OR KEY? ?
S5	1593293	VALUE? OR HIGHEST? OR LOWEST? OR EXTREME? OR FIRST? OR LA- . ST? OR GREATEST? OR LEAST? OR MOST?
S6	103	S2(S)S3
S7	55	S6(S) (S4 OR S5)
S8	65	S2(10N)S4(10N)S5
S9	84	S2(3N) (SHARED OR SHARING OR SHARES OR SHARE)
S10	26	S9(S) (S3 OR S4 OR S5)
S11	9	S9(S)KEY? ?
S12	7	S9(S)LIST? ?
S13	11	S8 AND IC=G06F-017?
S14	19	S2(10N)S3(10N) (S4 OR S5)
S15	18	S14 AND IC=G06F?
S16	39	S15 OR S13 OR S12 OR S11
S17	35	S16 AND IC=G06F?
S18	2	S11 AND S12
S19	35	S17 OR S18
S20	35	IDPAT (sorted in duplicate/non-duplicate order)
S21	35	IDPAT (primary/non-duplicate records only)
S22	0	S9(3N)LIST? ?
S23	7	S9(S)LIST? ?
S24	0	S23 NOT S20

File 348:EUROPEAN PATENTS 1978-2003/Mar W03

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File 349:PCT FULLTEXT 1979-2002/UB=20030327,UT=20030320

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21/5/1 (Item 1 from file: 348)
DIALOG(R) File 348:EUROPEAN PATENTS
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01264768

Electronic billing with flexible biller controlled electronic bill presentment

Elektronische Rechnungsstellungssystem mit flexibeler vom Rechnungssteller kontrollierten elektronischen Rechnungsvorlage

Systeme de facturation electronique avec presentation flexible des factures electroniques controlees par le facturant

PATENT ASSIGNEE:

CheckFree Services Corporation, (2907041), 4411 East Jones Bridge Road,
Norcross, Georgia 30092, (US), (Applicant designated States: all)

INVENTOR:

Ganesan, Ravi, 5240 Blue Yarrow Run, Norcross, GA 30092, (US)

Hobday, Kenneth, 241 Bluff Ridge Court, Powell, Ohio 43065, (US)

LEGAL REPRESENTATIVE:

Hofstetter, Alfons J., Dr.rer.nat. et al (79921), Hofstetter, Schurack &
Skora Balanstrasse 57, 81541 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 1091330 A2 010411 (Basic)

EP 1091330 A3 020116

APPLICATION (CC, No, Date): EP 2000121882 001006;

PRIORITY (CC, No, Date): US 414731 991008

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;
LU; MC; NL; PT; SE

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: G07F-019/00; **G06F-017/60**

ABSTRACT EP 1091330 A2

An electronic bill presentment network includes a central network station and a plurality of different user stations. The central network station transmits bill availability information to the user stations to identify available bills of different billers for the different users. Information associated with each available bill of a respective biller is available at one of multiple networks addresses associated with that biller. The associated information could, for example, be the bill itself and/or promotional information. Each user station is associated with a respective one of the users and receives the transmitted bill availability information for its associated user and selects one of the identified available bills, such as for viewing or payment. A user station associated with a first user is linked to the first network address associated with the bills of the first biller, based on a bill selection by the first user station. A second user station associated with a second user is linked to the second network address associated with the bills of the first biller based on a bill selection by the second user station.

ABSTRACT WORD COUNT: 180

NOTE:

Figure number on first page: 3

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 010411 A2 Published application without search report

Search Report: 020116 A3 Separate publication of the search report

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200115	1906
SPEC A	(English)	200115	14682
Total word count - document A			16588
Total word count - document B			0
Total word count - documents A + B			16588

21/5/2 (Item 2 from file: 348)
DIALOG(R) File 348:EUROPEAN PATENTS
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01254136

Electronic billing with updateable electronic bill summary

Elektronische Rechnungsstellungssystem mit aktualisierbarer elektronischer Rechnungsübersicht

Système de facturation électronique avec sommaire de facture électronique pouvant être mis-à-jour

PATENT ASSIGNEE:

CheckFree Services Corporation, (2907041), 4411 East Jones Bridge Road, Norcross, Georgia 30092, (US), (Applicant designated States: all)

INVENTOR:

Ganesan, Ravi, 5240 Blue Yarrow Run, Norcross, Georgia 30092, (US)

Hobday, Kenneth, 241 Bluff Ridge Court, Powell, Ohio 43065, (US)

LEGAL REPRESENTATIVE:

Hofstetter, Alfons J., Dr.rer.nat. et al (79921), Hofstetter, Schurack &

Skora Balanstrasse 57, 81541 München, (DE)

PATENT (CC, No, Kind, Date): EP 1083532 A2 010314 (Basic)

EP 1083532 A3 011114

APPLICATION (CC, No, Date): EP 2000118977 000901;

PRIORITY (CC, No, Date): US 387764 990901

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU; MC; NL; PT; SE

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: G07F-019/00; **G06F-017/60** ; G07F-007/10

ABSTRACT EP 1083532 A2

An electronic bill payment network includes a plurality of user network stations associated with different users, a plurality of biller network stations associated with different billers, and a central network station. A first user station operates, in real time, to transmit information relevant to an amount of an available bill and an instruction to pay the available bill. A first biller station operates, in real time, to receive the transmitted information and to compute the amount of the available bill based upon the received information. The central network station operates to receive the computed amount of the available bill and the transmitted pay instruction, and to direct payment of the computed amount of the available bill based upon the transmitted instruction to pay that bill.

ABSTRACT WORD COUNT: 126

NOTE:

Figure number on first page: 3

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 010314 A2 Published application without search report

Change: 011114 A2 International Patent Classification changed: 20010926

Search Report: 011114 A3 Separate publication of the search report

Withdrawal: 030226 A2 Date application deemed withdrawn: 20020515

LANGUAGE (Publication, Procedural, Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200111	1472
SPEC A	(English)	200111	12200
Total word count - document A			13672
Total word count - document B			0
Total word count - documents A + B			13672

21/5/3 (Item 3 from file: 348)

DIALOG(R) File 348: EUROPEAN PATENTS

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01170501

APPLICATION INDEPENDENT MESSAGING SYSTEM

ANWENDUNGSUNABHÄNGIGES NACHRICHTENSYSTEM

SYSTÈME DE MESSAGERIE INDEPENDANT DE L'APPLICATION

PATENT ASSIGNEE:

The Chase Manhattan Bank, (2692692), 270 Park Avenue, 41st floor, New York, NY 10017, (US), (Proprietor designated states: all)

INVENTOR:

CONNELLY, Thomas, 3654 Carrollton Avenue, Wantagh, NY 11793, (US)

LEGAL REPRESENTATIVE:

Schrell, Andreas, Dr. et al (80051), Gleiss & Grosse Heilbronner Strasse
293, 70469 Stuttgart, (DE)

PATENT (CC, No, Kind, Date): EP 1129405 A1 010905 (Basic)

EP 1129405 B1 020904

WO 2000029948 000525

APPLICATION (CC, No, Date): EP 99953223 991019; WO 99US24366 991019

PRIORITY (CC, No, Date): US 108285 P 981113; US 409300 990929

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;
LU; MC; NL; PT; SE

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: G06F-009/46

CITED PATENTS (EP B): EP 522683 A; EP 824236 A; WO 95/29439 A; WO 97/24664
A; US 5557798 A

CITED REFERENCES (EP B):

"HYBRID METHOD FOR LOCATING MOBILE OBJECTS" IBM TECHNICAL DISCLOSURE

BULLETIN,US,IBM CORP. NEW YORK, vol. 36, no. 6A, 1 June 1993

(1993-06-01), pages 381-382, XP000372464 ISSN: 0018-8689;

NOTE:

No A-document published by EPO

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 000719 A1 International application. (Art. 158(1))

Application: 000719 A1 International application entering European
phase

Application: 010905 A1 Published application with search report

Examination: 010905 A1 Date of request for examination: 20010613

Examination: 011219 A1 Date of dispatch of the first examination
report: 20011106

Grant: 020904 B1 Granted patent

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	200236	2876
CLAIMS B	(German)	200236	2666
CLAIMS B	(French)	200236	3065
SPEC B	(English)	200236	11275

Total word count - document A 0

Total word count - document B 19882

Total word count - documents A + B 19882

21/5/4 (Item 4 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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01030324

MOBILE ELECTRONIC COMMERCE SYSTEM

MOBILES ELEKTRONISCHES HANDELSSYSTEM

SYSTEME DE COMMERCE ELECTRONIQUE MOBILE

PATENT ASSIGNEE:

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD, (216884), 1006, Oaza-Kadoma,
Kadoma-shi, Osaka 571-0000, (JP), (Applicant designated States: all)

INVENTOR:

TAKAYAMA, Hisashi, 21-22, Matsubara 4-chome, Setagaya-ku, Tokyo 156-0043,
(JP)

LEGAL REPRESENTATIVE:

Casalonga, Axel (14511), BUREAU D.A. CASALONGA - JOSSE Morassistrasse 8,
80469 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 950968 A1 991020 (Basic)

WO 9909502 990225

APPLICATION (CC, No, Date): EP 98937807 980813; WO 98JP3608 980813

PRIORITY (CC, No, Date): JP 97230564 970813

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G06F-017/60

ABSTRACT EP 950968 A1

The objective of the present invention is to provide a mobile electronic commerce system that is superior in safety and usability. The mobile electronic commerce system comprises an electronic wallet 100, supply sides 101, 102, 103, 104 and 105, and a service providing means 110 that is connected by communication means. The service providing means installs a program for an electronic ticket, an electronic payment card, or an electronic telephone card. The electronic wallet employs the installed card to obtain a product or a service or entrance permission. The settlement process is performed by the electronic wallet and the supply side via the communication means, and data obtained during the settlement process are managed by being transmitted to the service providing means at a specific time. A negotiable card can be easily obtained, and when the negotiable card is used the settlement process can be quickly and precisely performed.

ABSTRACT WORD COUNT: 150

NOTE:

Figure number on first page: 1

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 990519 A1 International application (Art. 158(1))

Application: 991020 A1 Published application with search report

Examination: 991020 A1 Date of request for examination: 19990825

LANGUAGE (Publication,Procedural,Application): English; English; Japanese

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
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CLAIMS A	(English)	9942	17239
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SPEC A	(English)	9942	160346
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Total word count - document A	177585
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Total word count - document B	0
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Total word count - documents A + B	177585
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21/5/5 (Item 5 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2003 European Patent Office. All rts. reserv.

00957813

PERSONAL ELECTRONIC SETTLEMENT SYSTEM, ITS TERMINAL, AND MANAGEMENT APPARATUS

**PERSONLICHES ELEKTRONISCHES REGELUNGSSYSTEM, TERMINAL UND MANAGEMENTAPPARAT
SYSTEME DE REGLEMENT ELECTRONIQUE PERSONNEL, TERMINAL DE CE DERNIER ET
APPAREIL PERMETTANT DE GERER CE SYSTEME**

PATENT ASSIGNEE:

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD., (216883), 1006, Oaza Kadoma,
Kadoma-shi, Osaka-fu, 571, (JP), (applicant designated states:
DE;FR;GB)

INVENTOR:

TAKAYAMA, Hisashi, 21-22, Matsubara 4-chome,Setagaya-ku, Tokyo 156, (JP)

LEGAL REPRESENTATIVE:

Casalonga, Axel et al (14511), BUREAU D.A. CASALONGA - JOSSE
Morassistrasse 8, 80469 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 910028 A1 990421 (Basic)
WO 9821677 980522

APPLICATION (CC, No, Date): EP 97912468 971114; WO 97JP4161 971114

PRIORITY (CC, No, Date): JP 96316897 961114; JP 97117681 970422

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G06F-017/60

CITED PATENTS (WO A): Y Y X

CITED REFERENCES (WO A):

TECHNICAL RESEARCH REPORT OF IEICE ISEC96-36, (October 1996), HIDEKI
NAGANO et al., "A Method of Electronic Settlement (in Japanese)", pages
33-38.

ACADEMIC PRESS, INC., (San Diego, USA), October 1995, (First Edition),
WAYNER PETER, "Digital Cash: Commerce on the Net", pages 85-100.

NTT R&D, 45(11), (November 1996), NIPPON TELEGRAPH & TELEPHONE CORP.,
KOUCHI NOTONO et al., "Application of Authentication/Encoding

Technology to Electronic Shopping Mall (in Japanese)", pages 107-113.
REPORT ON SMART CARDS, Vol. 10, No. 19, (23 September 1996), ANONYMOUS,

"Ferroelectric Smart Cards Go to Market".
CREDIT CARD MANAGEMENT, Vol. 9, No. 1, (April 1996), DALY JAMES J.,
"Guarding the Rear", pages 42-48.;

ABSTRACT EP 910028 A1

According to the present invention provided is a settlement means that is superior in safety and usability. The settlement means comprises: payment means 100 including a plurality of systems of communication means; charging means 101 including a plurality of systems of communication means; and settlement means 102 including a plurality of systems of communication means. Since the payment means and the settlement means exchange transaction data by communicating with each other, it is possible to prevent the assessment of an illegal charge by the charging means. In addition, since a signature (a digital signature) and an accounting statement are exchanged by communication between the payment means and the charging means, the efficiency of the sale can be improved.

ABSTRACT WORD COUNT: 119

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 981007 A1 International application (Art. 158(1))
Application: 990421 A1 Published application (A1with Search Report
;A2without Search Report)
Examination: 990421 A1 Date of filing of request for examination:
981012

LANGUAGE (Publication,Procedural,Application): English; English; Japanese
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9916	12261
SPEC A	(English)	9916	116678
Total word count - document A			128939
Total word count - document B			0
Total word count - documents A + B			128939

21/5/6 (Item 6 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00861038

Method and apparatus for distributing work flow processes among a plurality of users

Verfahren und Gerat zur Verteilung von Arbeitsablaufvorgangen unter einer Vielfalt von Benutzern

Methode et appareil pour la distribution des processus de travail entre plusieurs usagers

PATENT ASSIGNEE:

Dun & Bradstreet Software Services, Inc., (2047260), 3445 Peachtree Street, NE, Atlanta, Georgia 30326-1276, (US), (applicant designated states: AT;BE;CH;DE;DK;ES;FI;FR;GB;GR;IE;IT;LI;LU;MC;NL;PT;SE)

INVENTOR:

Rossi, Charles, Indian Meadow Drive, Nesthborough, Massachusetts 01532, (US)

Vinter, Stephen P., 23 Hundred Oaks Lane, Ashland, Massachusetts 01721, (US)

Ancona, James P., 21 Scar Hill Road, Boylston, Massachusetts 01505, (US)

Morrison, Ed, 168 Kevin Road, Brockton, Massachusetts 02402, (US)

Diebboll, Robert, 25 Hillside Road, Lincoln, Massachusetts 01773, (US)

Delvecchio, Paul, 88 Oakland Street, Medway, Massachusetts 02053, (US)

Eddy, Jonathan, 40 Eastwood Road, Shrewsbury, Massachusetts 01545, (US)

LEGAL REPRESENTATIVE:

Brunner, Michael John (28871), GILL JENNINGS & EVERY Broadgate House 7 Eldon Street, London EC2M 7LH, (GB)

PATENT (CC, No, Kind, Date): EP 793184 A2 970903 (Basic)

EP 793184 A3 980506

APPLICATION (CC, No, Date): EP 96304008 960603;

PRIORITY (CC, No, Date): US 475575 950607

DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU;

MC; NL; PT; SE
INTERNATIONAL PATENT CLASS: G06F-017/60

ABSTRACT EP 793184 A2

A multi-user, client-server computer system utilizes information flow technology, where information in an organization is logically and automatically routed through a predefined sequence of activities to appropriate users who need the information, or who must enter needed information. After a first user completes an activity, the system evaluates the activity performed by the first user and, based upon that activity, routes information to a second user for performing a second activity. In accordance with the invention, data utilized in connection with the activities is stored at a central storage facility. Portions of this data are duplicatively replicated among a plurality of remote storage facilities for access by users. Similarly, Portions of this data are distributed among a plurality of remote storage facilities for access by users. This replication and distribution of data enhances system performance and efficiency.

ABSTRACT WORD COUNT: 138

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 970903 A2 Published application (A1with Search Report
;A2without Search Report)
Search Report: 980506 A3 Separate publication of the European or
International search report
Withdrawal: 990721 A2 Date on which the European patent application
was deemed to be withdrawn: 990105

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9708W5	482
SPEC A	(English)	9708W5	28384
Total word count - document A			28866
Total word count - document B			0
Total word count - documents A + B			28866

21/5/7 (Item 7 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00836626

Method and apparatus for distributing conditional work flow processes among a plurality of users

Verfahren und Vorrichtung zum Verteilen von konditionellen Arbeitsflussprozessen zwischen mehreren Benutzern

Methode et appareil pour la distribution de processus conditionnel de flux de travail entre plusieurs utilisateurs

PATENT ASSIGNEE:

Dun & Bradstreet Software Services, Inc., (2047260), 3445 Peachtree Street, NE, Atlanta, Georgia 30326-1276, (US), (applicant designated states: AT;BE;CH;DE;DK;ES;FI;FR;GB;GR;IE;IT;LI;LU;MC;NL;PT;SE)

INVENTOR:

Rossi, Charles, 1 Indian Meadow Drive, Northborough, Massachusetts 01532, (US)

Vinter, Stephen, 23 Hundred Oaks Lane, Ashland, Massachusetts 01721, (US)

Conte, Leonard, 281 Green Street, Northborough, Massachusetts 01532, (US)

Chang, S. Jay, 3 Duggan Road, Acton, Massachusetts 01720, (US)

Botzer, Robert, 49 Delmar Avenue, Framingham, Massachusetts 01701, (US)

McAllister, Sandra, 28 Lee Street, Lancaster, Massachusetts 01523, (US)

Dorden, Joanne, 2 Nottingham Road, Grafton, Massachusetts 01519, (US)

LEGAL REPRESENTATIVE:

Brunner, Michael John (28871), GILL JENNINGS & EVERY Broadgate House 7 Eldon Street, London EC2M 7LH, (GB)

PATENT (CC, No, Kind, Date): EP 774725 A2 970521 (Basic)

EP 774725 A3 981028

APPLICATION (CC, No, Date): EP 96304925 960703;

PRIORITY (CC, No, Date): US 557531 951114
DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU;
MC; NL; PT; SE
INTERNATIONAL PATENT CLASS: G06F-017/60

ABSTRACT EP 774725 A2

In accordance with the teachings of the present invention, a new computerized information flow distribution technology (work flow) is provided. One feature of the present invention allows the use of conditional logic in determining how information is routed among users. Specifically, conditional logic may be used to determine what the next step in the workflow should be, to determine who the next step should be assigned to, to select which approvers on an approval list are used, etc. Various types of conditional comparisons may be made in order to perform this functionality. Yet another feature of the present invention allows the use of a graphical tool for creating and mapping the work flow processes.

ABSTRACT WORD COUNT: 115

LEGAL STATUS (Type, Pub Date, Kind, Text):

Withdrawal: 20000209 A2 Date application deemed withdrawn: 19990428
Application: 970521 A2 Published application (Alwith Search Report
;A2without Search Report)
Search Report: 981028 A3 Separate publication of the European or
International search report

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB97	946
SPEC A	(English)	EPAB97	41563
Total word count - document A			42509
Total word count - document B			0
Total word count - documents A + B			42509

21/5/8 (Item 8 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00557416

METHOD AND APPARATUS FOR SCOPED INTERPROCESS MESSAGE SWITCHING
VERFAHREN UND GERAT ZUR NACHRICHTENVERMITTLUNG ZWISCHEN PROZESSEN MIT
ABGEGRENZTEM BEREICH
PROCEDE ET APPAREIL POUR LA COMMUTATION DE MESSAGE INTERPROCESSUS A ETENDUE
DELIMITEE

PATENT ASSIGNEE:

SUN MICROSYSTEMS, INC., (1392730), 2550 Garcia Avenue, Mountain View, CA
94043, (US), (Proprietor designated states: all)

INVENTOR:

FOSS, Carolyn L., 2294 Princeton St., Palo Alto, CA 94306, (US)
HARE, Dwight, F., 312 Lexington Drive, Menlo Park, CA 94025, (US)
McALLISTER, Richard, F., 149 Webster Street, Palo Alto, CA 94301, (US)
NGUYEN, Tin, Anh, 304 Windchime Drive, Danville, CA 94506, (US)
PEARL, Amy, 4010 Page Mill Road, Los Altos Hills, California CA 94022,
(US)
SHAIO, Sami, 759 Ramone Street, Palo Alto, California CA 94301, (US)

LEGAL REPRESENTATIVE:

Wombwell, Francis et al (46021), Potts, Kerr & Co. 15, Hamilton Square,
Birkenhead Merseyside L41 6BR, (GB)

PATENT (CC, No, Kind, Date): EP 521153 A1 930107 (Basic)
EP 521153 A1 940323
EP 521153 B1 010314
WO 9213309 920806

APPLICATION (CC, No, Date): EP 92906348 920123; WO 92US564 920123

PRIORITY (CC, No, Date): US 644942 910123; US 646357 910125

DESIGNATED STATES: DE; FR; GB; IT

INTERNATIONAL PATENT CLASS: G06F-015/16 ; G06F-009/46

CITED PATENTS (EP B): US 4470954 A; US 4644470 A; US 4672570 A; US 4706081

A; US 4747130 A; US 4759015 A; US 4760572 A; US 4792947 A; US 4807224 A;
US 4853843 A; US 4864559 A; US 4899333 A; US 4922503 A; US 5018137 A; US
5036518 A; US 5041963 A; US 5058109 A; US 5079767 A; US 5095480 A
CITED PATENTS (WO A): US 4644470 A; US 4672570 A; US 4706081 A; US 4470954
A; US 4747130 A; US 4759015 A; US 4760572 A; US 4792947 A; US 4807224 A;
US 4853843 A; US 4864559 A; US 4899333 A; US 4922503 A; US 5018137 A; US
5036518 A; US 5041963 A; US 5058109 A; US 5079767 A; US 5095480 A

CITED REFERENCES (EP A):

COMPUTER, vol. 23, no. 2 , February 1990 , LONG BEACH, US, pages 56 - 66
XP101459 LUPING LIANG ET AL.: 'Process Groups and Group Communications:
Classifications and Requirements';

CITED REFERENCES (EP B):

COMPUTER, vol. 23, no. 2 , February 1990 , LONG BEACH, US, pages 56 - 66
XP101459 LUPING LIANG ET AL.: 'Process Groups and Group Communications:
Classifications and Requirements';

NOTE:

No A-document published by EPO

LEGAL STATUS (Type, Pub Date, Kind, Text):

Grant: 010314 B1 Granted patent
Application: 930107 A1 Published application (A1with Search Report
;A2without Search Report)
Oppn None: 020306 B1 No opposition filed: 20011215
Change: 930303 A1 Inventor (change)
Examination: 930317 A1 Date of filing of request for examination:
930116
Change: 930519 A1 Inventor (change)
Change: 940309 A1 Obligatory supplementary classification
(change)
Search Report: 940323 A1 Drawing up of a supplementary European search
report: 940131
Examination: 980401 A1 Date of despatch of first examination report:
980213

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	200111	3605
CLAIMS B	(German)	200111	3548
CLAIMS B	(French)	200111	4667
SPEC B	(English)	200111	9906
Total word count - document A			0
Total word count - document B			21726
Total word count - documents A + B			21726

21/5/9 (Item 9 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00495605

Systems and methods for providing recovery capabilities.

Systeme und Verfahren zur Beschaffung von Erholungsfahigkeiten.

Systemes et procedes fournissant des aptitudes de restauration.

PATENT ASSIGNEE:

International Business Machines Corporation, (200120), Old Orchard Road,
Armonk, N.Y. 10504, (US), (applicant designated states: DE;FR;GB)

INVENTOR:

Abraham, Robert Low, 1993 Willeo Creek Point, Marietta, GA 30068, (US)
Priven, Michael P., 698 Jenny Mill Court, Marietta, GA 30068, (US)
Moorman, Thomas Paul, 7170 Riverside Way, Atlanta, GA 30328, (US)

LEGAL REPRESENTATIVE:

Bonneau, Gerard (14161), Compagnie IBM France Departement de Propriete
Intellectuelle, F-06610 La Gaude, (FR)

PATENT (CC, No, Kind, Date): EP 483038 A2 920429 (Basic)
EP 483038 A3 930224

APPLICATION (CC, No, Date): EP 91480153 910926;

PRIORITY (CC, No, Date): US 602443 901023

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G06F-011/14

CITED REFERENCES (EP A):

- IEEE TRANSACTIONS ON SOFTWARE ENGINEERING. vol. 16, no. 9, September 1990, NEW YORK US pages 1076 - 1088 , XP159482 J.M. WING 'Using Larch to Specify Avalon/C++ Objects'
- IEEE TRANSACTIONS ON SOFTWARE ENGINEERING. vol. 15, no. 6, June 1989, NEW YORK US pages 686 - 695 , XP52137 A.R. TRIPATHI 'An Overview of the Nexus Distributed Operating System Design'
- PROCEEDINGS OF THE THIRD SYMPOSIUM ON RELIABILITY IN DISTRIBUTED SOFTWARE AND DATABASE SYSTEMS, CLEARWATER BEACH, US 17 October 1983, SILVER SPRING, US pages 102 - 113 A.R. TRIPATHI ET AL. 'An Object-Oriented Design Model for Reliable Distributed Systems'
- COMPUTER JOURNAL vol. 32, no. 4, August 1989, LONDON GB pages 323 - 332 , XP49906 G.N. DIXON ET AL. 'The Treatment of Persistent Objects in Arjuna';

ABSTRACT EP 483038 A2

The present invention is directed to systems and methods for recovering from unplanned failures in object oriented computing environments. The systems and methods of the present invention recover from unplanned failures in an efficient manner by storing recovery information in recovery objects. During recovery operations from an unplanned failure, object instance methods (which were abnormally terminated by the unplanned failure) use the recovery information to identify committable actions which were executed prior to the unplanned failure. The object instance methods then execute committable actions which were not executed prior to the unplanned failure. Thus, according to the present invention, the committable actions which were executed prior to the unplanned failure are not re-executed, and the committable actions which were not executed prior to the planned failure are executed. (see image in original document)

ABSTRACT WORD COUNT: 135

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 920429 A2 Published application (Alwith Search Report
;A2without Search Report)

Search Report: 930224 A3 Separate publication of the European or
International search report

Withdrawal: 940504 A2 Date on which the European patent application
was deemed to be withdrawn: 930825

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	1776
SPEC A	(English)	EPABF1	8106
Total word count - document A			9882
Total word count - document B			0
Total word count - documents A + B			9882

21/5/10 (Item 10 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00976179

**METHOD FOR ENSURING OPERATION DURING NODE FAILURES AND NETWORK PARTITIONS
IN A CLUSTERED MESSAGE PASSING SERVER**

**PROCEDE PERMETTANT D'ASSURER LE FONCTIONNEMENT AU COURS DE DEFAILLANCES DE
NOEUDS ET PARTITIONS DE RESEAU DANS UNE GRAPPE DE SERVEURS DE PASSAGE
DE MESSAGES**

Patent Applicant/Assignee:

SOFTWIRED AG, Technoparkstrasse 1, CH-8005 Zurich, CH, CH (Residence), CH
(Nationality), (For all designated states except: US)

Patent Applicant/Inventor:

GIOTTA Paul, Bruhlgartenstrasse 31, CH-8400 Winterthur, CH, CH
(Residence), US (Nationality), (Designated only for: US)

SPRING Jesper Honig, Kornerstrasse 10, CH-8004 Zurich, CH, CH (Residence)
, DK (Nationality), (Designated only for: US)

Legal Representative:

FREI PATENTANWALTSBURO (agent), Postfach 768, CH-8029 Zurich, CH,
Patent and Priority Information (Country, Number, Date):
Patent: WO 200305194 A2 20030116 (WO 0305194)
Application: WO 2002CH304 20020610 (PCT/WO CH0200304)
Priority Application: US 2001899662 20010705
Designated States: AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU
CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP
KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO
RU SD SE SG SI SK SL TJ TM TN TR TT TZ UA UG US UZ VN YU ZA ZM ZW
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
(EA) AM AZ BY KG KZ MD RU TJ TM
Main International Patent Class: **G06F-009/46**
Publication Language: English
Filing Language: English
Fulltext Availability:
Detailed Description
Claims
Fulltext Word Count: 16156

English Abstract

A means for guaranteeing the proper behavior as specified by the JMS semantics of clustered message server when the individual computer that comprise the cluster are separated by a network partition. A clustered message server is responsible for the reliable transportation of messages between different distributed computer applications. It employs multiple computers to perform a function that otherwise appears to be performed by a monolithic server running on one computer, but with more capacity and reliability than can be provided by one computer. If a computer in the cluster fails, another computer should automatically assume the role of the failed computer. However, it is not possible for the other machines in the cluster to detect the difference between the failure of one or more computers in the cluster, and the failure of data network connecting those computers. In ordinary clusters, different actions would be required in these two cases, but since they are impossible to distinguish, computer failure is always assumed and network failure is ignored and the consequence non-deterministic. The invention described here provides a means of responding to failures that yields correct behavior as specified by the JMS semantics whether the failure is due to computer failure or network failure.

French Abstract

La presente invention concerne un moyen permettant de garantir le comportement approprié tel que spécifié par la sémantique de JMS d'une grappe de serveurs de messages lorsque l'ordinateur personnel qui comprend la grappe est séparé par une partition de réseau. Une grappe de serveurs de messages est responsable du transport fiable de messages entre différentes applications informatiques distribuées. Il fait appel à de multiples ordinateurs pour mettre en œuvre une fonction qui différemment semble être mise en œuvre par un serveur monolithique fonctionnant sur un ordinateur, mais a capacité et fiabilité plus importante que celle qui peut être fournie par un ordinateur. Si un ordinateur présent dans la grappe défaille, un autre ordinateur peut automatiquement prendre en charge le rôle de l'ordinateur défaillant. Cependant, il est impossible pour les autres machines de la grappe de détecter la différence entre la défaillance d'un ou de plusieurs ordinateurs dans la grappe, et la défaillance du réseau de données reliant ces ordinateurs. Dans les grappes ordinaires, différentes actions sont requises dans ces deux cas, mais étant donné qu'ils sont impossibles à distinguer, une défaillance d'ordinateur est toujours prise en charge et la défaillance d'ordinateur est ignorée et la conséquence non-déterministique. La présente invention concerne un moyen permettant de répondre aux défaillances qui produisent un comportement correct tel que spécifié par la sémantique de JMS si la défaillance est due à une défaillance d'ordinateur ou à une défaillance de réseau.

Legal Status (Type, Date, Text)

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00974205

**MULTI -THREAD SHARED MEMORY MESSAGE QUEUE BUFFER SYSTEMS, METHODS AND
COMPUTER PROGRAM PRODUCTS FOR STORING DATA IN A DATABASE
SYSTEMES UTILISANT UN TAMPON DE FILE D'ATTENTE DE MESSAGES A MEMOIRE
PARTAGEE MULTIFILIERE, PROCEDES ASSOCIES ET PRODUITS-PROGRAMMES
INFORMATIQUES DESTINES A STOCKER DES DONNEES DANS UNE BASE DE DONNEES**

Patent Applicant/Assignee:

TRENDIUM INC, 13450 W. Sunrise Blvd., Suite 200, Fort Lauderdale, FL
33323, US, US (Residence), US (Nationality), (For all designated states
except: US)

Patent Applicant/Inventor:

YU Lin, 1500 NW Avenue, Apt. 361, Fort Lauderdale, FL 33322, US, US
(Residence), US (Nationality), (Designated only for: US)
HONG Jianrong, 395 NW 87th Terrace, Plantation, FL 33324, US, US
(Residence), CN (Nationality), (Designated only for: US)

Legal Representative:

BIGEL Mitchell S (agent), Myers Bigel Sibley & Sajovec, P.O. Box 37428,
Raleigh, NC 27526, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200303256 A2 20030109 (WO 0303256)
Application: WO 2002US6506 20020304 (PCT/WO US0206506)
Priority Application: US 2001896040 20010629

Designated States: AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU
CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP
KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO
RU SD SE SG SI SK SL TJ TM TN TR TT TZ UA UG US UZ VN YU ZA ZM ZW
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: **G06F-017/30**

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description
Claims

Fulltext Word Count: 8059

English Abstract

Systems, methods and/or computer program products for storing data from multiple clients in a database include a Multi-thread **Shared Memory message Queue** Buffer (MSMQB) that includes multiple First-In First-Out (FIFO) queues, a respective one of which is associated with a respective one of the clients. The MSMQB is configured to store sequential bursts of data records that are received from the clients in the associated FIFO queues. The data records in the sequential bursts are sorted by a primary **key**. A Semi-Merge Sort Module (SMSM) also is provided that is responsive to the FIFO queues in the MSMQB. The SMSM is configured to sort the first sequential bursts in the FIFO queues based on the primary **key** of at least one selected record therein, to produce a semi-sorted record stream. The SMSM also serially stores the semi-sorted record stream in the database. Thus, a database can handle multiple clients and multiple bursts for high throughput writing of data into the database. Moreover, by performing a semi-merge sort, the records may be placed into the record stream based on the value of the primary **key** of at least one selected record, so that an efficient rough sort may be provided.

French Abstract

L'invention concerne des systemes, des procedes et/ou des produits-programmes informatiques destines a stocker des donnees en provenance de nombreux clients dans une base de donnees, et faisant intervenir un tampon de file d'attente de messages a memoire partagee multifiliere (MSMQB) comprenant des files d'attente "premier entre premier sorti" (FIFO) dont l'une est associee a une file d'attente respective des clients. Le MSMQB est configure pour le stockage de rafales sequentielles de fichiers de donnees recus en provenance des clients dans les files d'attente FIFO associees. Les fichiers de donnees dans les rafales sequentielles sont tries au moyen d'une cle primaire. L'invention concerne egalement un module de semi-tri par fusion (SMSM) sensible aux files d'attente FIFO dans le MSMQB. Le SMSM est configure pour trier les rafales sequentielles dans les files d'attente FIFO sur la base de la cle primaire d'au moins un fichier selectionne en vue de produire un flux de fichiers semi-tries. Le SMSM permet egalement de stocker en serie le flux de fichiers semi-tries dans la base de donnees. Ainsi, une base de donnees peut traiter plusieurs clients et plusieurs rafales pour une ecriture de donnees a vitesse elevee dans la base de donnees. Par ailleurs, grace au semi-tri par fusion, les fichiers peuvent etre places dans le flux de fichiers sur la base de la valeur de la cle primaire d'au moins un fichier selectionne, d'ou l'obtention d'un tri grossier efficace.

Legal Status (Type, Date, Text)

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00963611 **Image available**

EXTENDED WEB ENABLED MULTI-FEATURED BUSINESS TO BUSINESS COMPUTER SYSTEM FOR RENTAL VEHICLE SERVICES

SYSTEME INFORMATIQUE INTERENTREPRISES A ELEMENTS MULTIPLES A ACCES INTERNET POUR SERVICES DE LOCATION DE VEHICULES

Patent Applicant/Assignee:

THE CRAWFORD GROUP INC, 600 Corporate Park Drive, St. Louis, MO 63105, US
, US (Residence), US (Nationality), (For all designated states except: US)

Patent Applicant/Inventor:

WEINSTOCK Timothy Robert, 1845 Highcrest Drive, St. Charles, MO 63303, US
, US (Residence), US (Nationality), (Designated only for: US)

DE VALLANCE Kimberly Ann, 2037 Silent Spring Drive, Maryland Heights, MO 63043, US, US (Residence), US (Nationality), (Designated only for: US)

HASELHORST Randall Allan, 1016 Scenic Oats Court, Imperial, MO 63052, US, US (Residence), US (Nationality), (Designated only for: US)

KENNEDY Craig Stephen, 9129 Meadowglen Lane, St. Louis, MO 63126, US, US (Residence), US (Nationality), (Designated only for: US)

SMITH David Gary, 10 Venice Place Court, Wildwood, MO 63040, US, US (Residence), US (Nationality), (Designated only for: US)

TINGLE William T, 17368 Hilltop Ridge Drive, Eureka, MO 63025, US, US (Residence), US (Nationality), (Designated only for: US)

KLOPFENSTEIN Anita K, 433 Schwarz Road, O'Fallon, IL 62269, US, US (Residence), US (Nationality), (Designated only for: US)

Legal Representative:

HAFERKAMP Richard E (et al) (agent), Howell & Haferkamp, L.C., Suite 1400, 7733 Forsyth Blvd., St. Louis, MO 63105-1817, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200297700 A2 20021205 (WO 0297700)

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Priority Application: US 2000694050 20001020

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CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP
KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PH PL PT RO RU
SD SE SG SI SK SL TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW
(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: **G06F-017/60**

Publication Language: English

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Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 237932

English Abstract

French Abstract

La presente invention concerne un systeme informatique de transaction entre entreprises qui dans un mode de realisation prefere est destine a fournir des services de location de vehicules pour des utilisateurs a demande elevee comportant un portail de reseau Internet grace auquel l'utilisateur a demande elevee peut acceder a une pluralite de fournisseurs de services comportant un reseau informatique d'entreprise integre pour au moins un fournisseur de services de location de vehicules. Le reseau informatique de fournisseur de services de location de vehicules est configure pour l'interconnexion d'une pluralite de succursales de diversite geographique, presentant le catalogue de leurs vehicules de location disponibles et des programmes les concernant ainsi que pour la gestion de toutes les donnees de transaction concernant son entreprise. Le portail de reseau Internet permet une connectivite et une transferabilite universelles pour une association d'entreprises a plusieurs niveaux qui placent regulierement des demandes elevees d'achat de location avec son associe commercial et egalement les autres fournisseurs de services qui peuvent ou non avoir le meme systeme et logiciel informatique d'entreprise integre. L'utilisation du procede et de l'appareil de la presente invention permet de placer, de grands volumes de transactions de location, de les controler, de les modifier en cours d'operation, et de les conclure avec des operations de comptabilite financiere et paiement pratiquement sans intervention humaine.

Legal Status (Type, Date, Text)

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Declaration 20030220 Late publication under Article 17.2a

Republication 20030220 A2 With declaration under Article 17(2)(a); without abstract; title not checked by the International Searching Authority.

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DIALOG(R)File 349:PCT FULLTEXT

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00933152 **Image available**

EXTENDED WEB ENABLED MULTI-FEATURED BUSINESS TO BUSINESS COMPUTER SYSTEM FOR RENTAL VEHICLE SERVICES

SYSTEME INFORMATIQUE ETENDU ENTRE ENTREPRISES, A FONCTIONS MULTIPLES, FONCTIONNANT SUR LE WEB, POUR DES SERVICES DE LOCATION DE VEHICULES

Patent Applicant/Assignee:

THE CRAWFORD GROUP INC, 600 Corporate Park Drive, St. Louis, MO 63105, US
, US (Residence), US (Nationality), (For all designated states except: US)

Patent Applicant/Inventor:

WEINSTOCK Timothy Robert, 1845 Highcrest Drive, St. Charles, MO 63303, US
, US (Residence), US (Nationality), (Designated only for: US)

DE VALLANCE Kimberly Ann, 2037 Silent Spring Drive, Maryland Heights, MO

63043, US, US (Residence), US (Nationality), (Designated only for: US)
HASELHORST Randall Allan, 1016 Scenic Oats Court, Imperial, MO 63052, US,
US (Residence), US (Nationality), (Designated only for: US)
KENNEDY Craig Stephen, 9129 Meadowglen Lane, St. Louis, MO 63126, US, US
(Residence), US (Nationality), (Designated only for: US)
SMITH David Gary, 10 Venice Place Court, Wildwood, MO 63040, US, US
(Residence), US (Nationality), (Designated only for: US)
TINGLE William T, 17368 Hilltop Ridge Drive, Eureka, MO 63025, US, US
(Residence), US (Nationality), (Designated only for: US)
KLOPFENSTEIN Anita K, 433 Schwarz Road, O'Fallon, IL 62269, US, US
(Residence), US (Nationality), (Designated only for: US)

Legal Representative:

HAFERKAMP Richard E (et al) (agent), HOWELL & HAFERKAMP, L.C., Suite
1400, 7733 Forsyth Blvd., St. Louis, MO 63105-1817, US,

Patent and Priority Information (Country, Number, Date):

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Application: WO 2001US51437 20011019 (PCT/WO US0151437)
Priority Application: US 2000694050 20001020

Parent Application/Grant:

Related by Continuation to: US 2000694050 20001020 (CIP)

Designated States: AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU

CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP
KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PH PL PT RO RU
SD SE SG SI SK SL TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR

(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

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Detailed Description

Claims

Fulltext Word Count: 243912

English Abstract

French Abstract

Legal Status (Type, Date, Text)

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abstract; title not checked by the International
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21/5/14 (Item 14 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00927480 **Image available**

TRUSTED OPERATING SYSTEM

SYSTEME D'EXPLOITATION VALIDE

Patent Applicant/Assignee:

HEWLETT-PACKARD COMPANY, A Delaware Corporation, 3000 Hanover Street, Palo
Alto, CA 94304, US, US (Residence), US (Nationality), (For all
designated states except: US)

Patent Applicant/Inventor:

CHOO Tse Huong, 46 The Culvert, Bradley Stoke, Bristol BS32 8AB, GB, GB
(Residence), MY (Nationality), (Designated only for: US)

DALTON Christopher I, 19 Burlington Road, Redland, Bristol BS6 6TJ, GB,
GB (Residence), GB (Nationality), (Designated only for: US)

NORMAN Andrew Patrick, 254 Juniper Way, Bradley Stoke, Bristol BS32 0DR,

GB, GB (Residence), GB (Nationality), (Designated only for: US)
Legal Representative:
LAWRENCE Richard Anthony (agent), Hewlett Packard Limited, Intellectual
Property Section, Filton Road, Stoke Gifford, Bristol BS34 8QZ, GB,
Patent and Priority Information (Country, Number, Date):
Patent: WO 200261554 A1 20020808 (WO 0261554)
Application: WO 2002GB419 20020129 (PCT/WO GB0200419)
Priority Application: GB 20012518 20010131
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Main International Patent Class: **G06F-001/00**
Publication Language: English
Filing Language: English
Fulltext Availability:
Detailed Description
Claims
Fulltext Word Count: 17264

English Abstract

An operating system comprising a kernel 100 incorporating mandatory access controls as a means to counter the effects posed by application compromise. The operating system uses a technique known as "containment" to at least limit the scope of damage when security breaches occur. In a preferred embodiment, each application supported by the operating system, is assigned a tag or label, each tag or label being indicative of a logically protected computing environment or "compartment", and applications having the same tag or label belonging to the same compartment. By default, only applications running in the same compartment can communicate with each other. Access control rules define very narrow tightly-controlled communications paths between compartments.

French Abstract

L'invention concerne un systeme d'exploitation valide comportant un noyau (100) qui detient des commandes d'accès obligatoires de protection contre les effets des compromis entre applications. La technique utilisee par le systeme d'exploitation est appelee "endiguement": il s'agit au moins de limiter l'envergure des degats en cas de breche de securite. En mode de realisation prefere, chaque application assuree par le systeme d'exploitation recoit une etiquette indiquant en environnement ou "compartment" de calcul a protection logique, et les applications dotees de la meme etiquette appartiennent au meme compartiment. Par default, seules les applications lancees dans le meme compartiment peuvent communiquer entre elles. Les regles de controle d'accès definissent des trajets de communications tres rigoureusement controles entre les compartiments.

Legal Status (Type, Date, Text)
Publication 20020808 A1 With international search report.
Examination 20021114 Request for preliminary examination prior to end of
19th month from priority date except in: US

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DIALOG(R)File 349:PCT FULLTEXT
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00878814 **Image available**
COMMON APPLICATION METAMODEL INCLUDING C/C++ METAMODEL
METAMODELE D'APPLICATION COMMUN DONT LE METAMODELE C/C++
Patent Applicant/Assignee:
INTERNATIONAL BUSINESS MACHINES CORPORATION, New Orchard Raad, Armonk, NY
10504, US, US (Residence), US (Nationality)
Inventor(s):
BRODSKY Stephen, 108 Mozart Avenue, Los Gatos, CA 95032, US,
HO Shyh-Mei F, 10375 Moretti Drive, Cupertino, CA 95014, US,
RHYNE James Rush, 719 Bicknell Road, Los Gatos, CA 95030, US,
Legal Representative:
GOLDMAN Richard M (et al) (agent), International Business Machines

Corporation, Intellectual Property Law J46/G4, 555 Bailey Avenue, San Jose, CA 95141-1003, US,
Patent and Priority Information (Country, Number, Date):
Patent: WO 200213007 A1 20020214 (WO 0213007)
Application: WO 2001US24280 20010802 (PCT/WO US0124280)
Priority Application: US 2000223671 20000808; US 2001849107 20010504
Designated States: AT AU BR CA CH CN CZ DE ES GB HU IL IN JP KR MX PL RU SE SG VN
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR
Main International Patent Class: G06F-009/44
Publication Language: English
Filing Language: English
Fulltext Availability:
Detailed Description
Claims
Fulltext Word Count: 46310

English Abstract

A method of and a system for processing an enterprise application (FIGURE 2, #221) request on an end user application and an application server. This is accomplished by initiating the application request on the end user application in a first language with a first application program, and transmitting the application request to the server and converting the application from the first language (FIGURE 2, #209) of the first end user application to a language running on the application server, processing the application request on the application server, and transmitting the response from the application server back to the end user application, and converting the response from the language running on the application server to the language of the end user application (figure 2, #221).

French Abstract

L'invention concerne un procede et un systeme destines a traiter une demande d'application d'entreprise (221) sur une application d'utilisateur final et un serveur d'application, par l'elaboration de la demande d'application sur l'application d'utilisateur final dans un premier langage au moyen d'un premier programme d'application, puis par l'emission de la demande d'application a un serveur et par la transformation de ladite application du premier langage (209) de la premiere application d'utilisateur final en un langage utilise sur le serveur d'application. Le procede consiste ensuite a renvoyer la reponse du serveur d'application a l'application de l'utilisateur final, et a transformer la reponse du langage utilise sur le serveur d'application en langage de l'application de l'utilisateur final (221).

Legal Status (Type, Date, Text)

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00869165 **Image available**

**CACHING SCHEME FOR MULTI-DIMENSIONAL DATA
PROCEDURE DE MISE EN ANTEMEMOIRE DE DONNEES MULTIDIMENSIONNELLES**

Patent Applicant/Assignee:

ALPHABLOX CORPORATION, 800 Maude Avenue, Mountain View, CA 94043, US, US
(Residence), US (Nationality)

Inventor(s):

WARREN Christina E, 6631 Bubblingwell Place, San Jose, CA 95120, US,
JOHNSON Galt, 65 Crestmont Drive, San Francisco, CA 94131, US,

Legal Representative:

BUDNITSKAYA Rimma (et al) (agent), Fenwick & West LLP, Two Palo Alto Square, Palo Alto, CA 94306, US,

Patent and Priority Information (Country, Number, Date):

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Application: WO 2001US20817 20010629 (PCT/WO US0120817)
Priority Application: US 2000215699 20000629
Designated States: AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ
DE DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ
LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG
SI SK SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR
(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW
(EA) AM AZ BY KG KZ MD RU TJ TM
Main International Patent Class: G06F-017/30
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Fulltext Availability:
Detailed Description
Claims
Fulltext Word Count: 7702

English Abstract

A system, method, and a computer program product for caching multi-dimensional data based on an assumption of locality of reference. A user sends a query for data. A described compilation module converts the query into a set of cubelet addresses and canonical addresses. In the described embodiment, if the data corresponding to the cubelet address is found in a data cache, the data cache returns the cubelet, which may contain the requested data and data for "nearby" cells. The data corresponding to the canonical addresses is extracted from the returned cubelet. If the data is not found in a data cache, a fault handler queries a back-end database for the cubelet identified by the cubelet address. This cubelet includes the requested data and data for "nearby" cells. The requested data and the data for "nearby cells" are in the form of values of measure attributes and associated canonical addresses. The returned cubelet is then cached and the data corresponding to the canonical addresses is extracted.

French Abstract

La presente invention concerne un systeme, un procede et un progiciel informatique qui permettent de placer en antememoire des donnees en fonction d'une hypothese portant sur un lieu de reference. Un utilisateur envoie une requete relative a des donnees. Un module de compilation convertit la requete en un groupe d'adresses de cube et d'adresses canoniques. Dans la forme de realisation presentee, si les donnees correspondant a l'adresse de cube se trouvent dans une antememoire de donnees, l'antememoire de donnees renvoie le cube, lequel peut contenir les donnees demandees et des donnees pour des cellules 'voisines'. Les donnees correspondant aux adresses canoniques sont extraites du cube renvoye. Si les donnees ne se trouvent pas dans une antememoire de donnees, un gestionnaire d'anomalies demande a une base de donnees secondaire le cube identifie par l'adresse de cube. Ce cube comprend les donnees demandees et les donnees pour des cellules 'voisines'. Les donnees demandees et les donnees pour les cellules 'voisines' se presentent sous forme de valeurs d'attribut de mesure et d'adresses canoniques associees. Le cube renvoye est ensuite place en antememoire et les donnees correspondant aux adresses canoniques sont extraites.

Legal Status (Type, Date, Text)

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21/5/17 (Item 17 from file: 349)
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00852813 **Image available**
COMMUNICATION HANDLING IN INTEGRATED MODULAR AVIONICS

GESTION DES COMMUNICATIONS DANS UN SYSTEME AVIONIQUE MODULAIRE INTEGRE

Patent Applicant/Assignee:

HONEYWELL INTERNATIONAL INC, 101 Columbia Avenue, P.O. Box 2245,
Morristown, NJ 07960, US, US (Residence), US (Nationality)

Inventor(s):

YOUNIS Mohamed, 5029 Columbia Road, Apt. 301, Columbia, MD 21044, US,
ABOUTABL Mohamed Said, 1402 Spring lane, East Stroudsburg, PA 18301, US,

Legal Representative:

CRISS Roger H (et al) (agent), Honeywell International Inc., 101 Columbia
Avenue, P.O. Box 2245, Morristown, NJ 07960, US,

Patent and Priority Information (Country, Number, Date):

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Application: WO 2001US14895 20010509 (PCT/WO US0114895)

Priority Application: US 2000202984 20000509; US 2001821601 20010329

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DM EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR

LS LT LU LV MA MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ

TM TR TT TZ UA UG UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: **G06F-009/54**

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 10160

English Abstract

Techniques for inter-application communication and handling of I/O devices in an Integrated Modular Avionics (IMA) system enable the integration of multiple applications (B) while maintaining strong spatial and temporal partitioning between application software modules or partitioned applications (P1, P2). The integration of application modules is simplified by abstracting the desired application interactions in a manner similar to device access. Such abstraction facilitates the integration of previously developed applications as well as new applications. The invention requires the least support from the operating system and minimizes the dependency of the integrated environment on application characteristics.

French Abstract

L'invention se rapporte a des techniques de communication inter-applications et de gestion des dispositifs d'E/S dans un systeme avionique modulaire integre (IMA), qui permettent l'integration de multiples applications (B) et maintiennent simultanement un partitionnement spatial et temporel fort entre des modules logiciels d'applications ou des applications partitionnees (P1, P2). L'integration de modules d'applications est simplifiee du fait que les interactions souhaitees entre applications sont conceptualisees d'une maniere similaire a l'accès aux dispositifs. Une telle abstraction facilite l'integration d'applications developpees anterieurement ainsi que celle d'applications nouvelles. L'invention ne necessite qu'un faible soutien du systeme d'exploitation et elle minimise la dependance de l'environnement integre par rapport aux caracteristiques des applications.

Legal Status (Type, Date, Text)

Publication 20011115 A2 Without international search report and to be
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DIALOG(R) File 349: PCT FULLTEXT

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00784185 **Image available**

A SYSTEM AND METHOD FOR STREAM-BASED COMMUNICATION IN A COMMUNICATION SERVICES PATTERNS ENVIRONMENT
SYSTEME, PROCEDE ET ARTICLE DE PRODUCTION FOURNISSANT UN SYSTEME DE COMMUNICATION EN CONTINU DANS UN ENVIRONNEMENT DE CONFIGURATIONS DE SERVICES DE COMMUNICATION

Patent Applicant/Assignee:

ACCENTURE LLP, 1661 Page Mill Road, Palo Alto, CA 94304, US, US
(Residence), US (Nationality)

Inventor(s):

BOWMAN-AMUAH Michel K, 6426 Peak Vista Circle, Colorado Springs, CO 80918
, US,

Legal Representative:

HICKMAN Paul L (agent), Hickman Coleman & Hughes, LLP, P.O. Box 52037,
Palo Alto, CA 94303-0746, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200117195 A2-A3 20010308 (WO 0117195)

Application: WO 2000US24125 20000831 (PCT/WO US0024125)

Priority Application: US 99386717 19990831

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DE DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ

LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG

SI SK SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: H04L-029/06

International Patent Class: **G06F-017/22** ; H04L-029/12

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 150532

English Abstract

A system, method, and article of manufacture are disclosed for providing a stream-based communication system. A shared format is defined on interface code for a sending system and a receiving system. A message to be sent from the sending system to the receiving system is translated based on the shared format. Once translated, the message is then sent from the sending system and received by the receiving system. Once the message is received by the receiving system, the message is then translated based on the shared format.

French Abstract

L'invention concerne un systeme, un procede et un article de production fournissant un systeme de communication en continu. Un format partage est defini selon un code d'interface pour un systeme emetteur et un systeme recepteur. Un message devant etre envoye par le systeme emetteur est traduit sur la base du format partage. Une fois traduit, le message est envoye du systeme emetteur et recu par le systeme recepteur. Le message recu par le systeme recepteur est ensuite traduit sur la base du format partage.

Legal Status (Type, Date, Text)

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Examination 20010907 Request for preliminary examination prior to end of 19th month from priority date

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DIALOG(R)File 349:PCT FULLTEXT

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00784184 **Image available**

A SYSTEM, METHOD FOR FIXED FORMAT STREAM COMMUNICATION IN A COMMUNICATION SERVICES PATTERNS ENVIRONMENT
SYSTEME, PROCEDE ET ARTICLE POUR FLUX DE FORMAT FIXE DANS UN ENVIRONNEMENT A CONFIGURATIONS DE SERVICES DE COMMUNICATION

Patent Applicant/Assignee:

ACCENTURE LLP, 1661 Page Mill Road, Palo Alto, CA 94304, US, US
(Residence), US (Nationality)

Inventor(s):

BOWMAN-AMUAH Michel K, 6426 Peak Vista Circle, Colorado Springs, CO 80918
, US,

Legal Representative:

HICKMAN Paul L (agent), Oppenheimer Wolff & Donnelly LLP, P.O. Box 52037,
Palo Alto, CA 94303-0746, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200117194 A2-A3 20010308 (WO 0117194)

Application: WO 2000US24114 20000831 (PCT/WO US0024114)

Priority Application: US 99386430 19990831

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DE DK DZ EE ES FI GB GE GH GM HR HU ID IL IS JP KE KG KP KR KZ LC LK LR

LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL

TJ TM TR TT UA UG UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: H04L-029/06

International Patent Class: **G06F-017/22** ; H04L-029/12

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 149954

English Abstract

A system, method, and article of manufacture provide a fixed format stream-based communication system. A sending fixed format contract on interface code is defined for a sending system. A receiving fixed format contract on interface code is also defined for a receiving system. A message to be sent from the sending system to the receiving system is translated based on the sending fixed format contract. The message is then sent from the sending system and subsequently received by the receiving system. The message received by the receiving system is then translated based on the receiving fixed format contract.

French Abstract

L'invention concerne un systeme, un procede et un article pour systeme de communication a flux de format fixe. Un contrat de format fixe de transmission sur code d'interface est defini pour un systeme de transmission. Un contrat de format fixe de reception sur code d'interface est egalement defini pour un systeme de reception. Un message destine a etre envoye du systeme de transmission au systeme de reception est converti sur la base du contrat de format fixe de transmission. Le message est ensuite transmis depuis le systeme de transmission, puis il est recu par le systeme de reception et converti sur la base du contrat de format fixe.

Legal Status (Type, Date, Text)

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Examination 20010816 Request for preliminary examination prior to end of 19th month from priority date

Search Rpt 20020103 Late publication of international search report

Republication 20020103 A3 With international search report.

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DIALOG(R) File 349:PCT FULLTEXT
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00784143

**SYSTEM, METHOD AND ARTICLE OF MANUFACTURE FOR LOAD BALANCING REQUESTS AMONG
SERVERS**

**SYSTEME, PROCEDE ET ARTICLE POUR EQUILIBREUR DE CHARGE DANS UN
ENVIRONNEMENT DE STRUCTURES DE SERVICES**

Patent Applicant/Assignee:

ACCENTURE LLP, 1661 Page Mill Road, Palo Alto, CA 94304, US, US
(Residence), US (Nationality)

Inventor(s):

BOWMAN-AMUAH Michel K, 6426 Peak Vista Circle, Colorado Springs, CO 80918
, US,

Legal Representative:

HICKMAN Paul L (agent), Hickman Coleman & Hughes, LLP, P.O. Box 52037,
Palo Alto, CA 94303-0746, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200116739 A2-A3 20010308 (WO 0116739)

Application: WO 2000US24236 20000831 (PCT/WO US0024236)

Priority Application: US 99387576 19990831

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DE DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ

LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG

SI SK SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: **G06F-009/50**

International Patent Class: **G06F-009/46**

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 150248

English Abstract

A system, method and article of manufacture are provided for distributing incoming requests amongst server components for optimizing usage of resources. Incoming requests are received and stored. An availability of server components is determined and a listing of available server components is compiled. A determination is made as to which server component on the listing of available server components is most appropriate to receive a particular request. Each particular request is sent to the selected server component determined to be most appropriate to receive the particular request.

French Abstract

L'invention porte sur un systeme, un procede et un article de fabrication s'appliquant a la distribution de requetes entrantes parmi des composants de serveur afin d'optimiser l'utilisation de ressources. Le procede consiste a accueillir les requetes et les stocker; determiner la disponibilite des composants du serveur et compiler une liste des composants disponibles; proceder a une determination selon laquelle un composant du serveur de la liste des composants disponibles est plus approprie a recevoir une requete particuliere; envoyer chaque requete particuliere au composant selectionne determine comme etant le plus approprie a recevoir une requete particuliere.

Legal Status (Type, Date, Text)

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00784140

**A SYSTEM, METHOD AND ARTICLE OF MANUFACTURE FOR A GLOBALLY ADDRESSABLE
INTERFACE IN A COMMUNICATION SERVICES PATTERNS ENVIRONMENT
SYSTEME, PROCEDE ET ARTICLE DE FABRICATION S'APPLIQUANT DANS UN
ENVIRONNEMENT DE STRUCTURE DE SERVICES DE COMMUNICATIONS VIA UNE
INTERFACE ADRESSABLE GLOBALEMENT**

Patent Applicant/Assignee:

ACCENTURE LLP, 1661 Page Mill Road, Palo Alto, CA 94304, US, US
(Residence), US (Nationality)

Inventor(s):

BOWMAN-AMUAH Michel K, 6426 Peak Vista Circle, Colorado Springs, CO 80918
, US,

Legal Representative:

HICKMAN Paul L (agent), Oppenheimer Wolff & Donnelly, LLP, 1400 Page Mill
Road, Palo Alto, CA 94304, US,

Patent and Priority Information (Country, Number, Date):

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Application: WO 2000US24198 20000831 (PCT/WO US0024198)

Priority Application: US 99387214 19990831

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DZ EE ES FI GB GE GH GM HR HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT
LU LV MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR
TT UA UG UZ VN YU ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: **G06F-009/46**

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 150371

English Abstract

A system, method, and article of manufacture are provided for delivering service via a globally addressable interface. A plurality of interfaces are provided with access allowed to a plurality of different sets of services from each of the interfaces. Each interface has a unique set of services associated therewith. Each of the interfaces is named with a name indicative of the unique set of services associated therewith. The names of the interfaces are then broadcast to a plurality of systems requiring service.

French Abstract

L'invention porte sur un systeme, un procede et un article de fabrication appliques dans la distribution de services via une interface adressable globalement. Une pluralite d'interfaces permettent d'accéder a une pluralite de differents ensembles de services. A chaque interface est associe un ensemble unique de services. Chacune de ces interfaces est affectee d'un nom designant l'ensemble unique de services correspondant. Les noms des interfaces sont ensuite diffuses a une pluralite de systemes requerant un service.

Legal Status (Type, Date, Text)

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DIALOG(R)File 349:PCT FULLTEXT
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00784139

**A SYSTEM, METHOD AND ARTICLE OF MANUFACTURE FOR A SELF-DESCRIBING STREAM IN
A COMMUNICATION SERVICES PATTERNS ENVIRONMENT
SYSTEME, PROCEDE ET ARTICLE DE FABRICATION DESTINES A UN FLUX
D'AUTODESCRIPTEURS DANS UN ENVIRONNEMENT DE MODELES DE SERVICES DE
COMMUNICATION**

Patent Applicant/Assignee:

ACCENTURE LLP, 1661 Page Mill Road, Palo Alto, CA 94304, US, US
(Residence), US (Nationality)

Inventor(s):

BOWMAN-AMUAH Michel K, 6426 Peak Vista Circle, Colorado Springs, CO 80918
, US,

Legal Representative:

HICKMAN Paul L (agent), Oppenheimer Wolff & Donnelly, LLP, 1400 Page Mill
Road, Palo Alto, CA 94304, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200116734 A2-A3 20010308 (WO 0116734)

Application: WO 2000US23999 20000831 (PCT/WO US0023999)

Priority Application: US 99387070 19990831

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DE DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ

LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG

SI SK SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: G06F-009/46

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 150517

English Abstract

A system, method, and article of manufacture are described for providing a self-describing stream-based communication system. Messages are sent which include data between a sending system and a receiving system. Meta-data is attached to the messages being sent between the sending system and the receiving system. The data of the messages sent from the sending system to the receiving system is translated based on the meta-data. The meta-data includes first and second sections. The first section identifies a type of object associated with the data and a number of attribute descriptors in the data. The second section includes a series of the attribute descriptors defining elements of the data.

French Abstract

L'invention concerne un systeme, un procede et un article de fabrication destines a constituer un systeme de communication a base d'un flux d'autodescripteurs. Des messages comprenant des donnees sont envoyes, entre un systeme expéditeur et un systeme recepteur. Des metadonnees sont attachees aux messages en cours d'envoi entre le systeme expéditeur et le systeme recepteur. Les donnees des messages envoyes du systeme expéditeur au systeme recepteur sont traduites d'apres les metadonnees, lesquelles comprennent des premiere et seconde sections. La premiere section identifie un type d'objet associe aux donnees et un nombre de descripteurs d'attributs presents dans celles-ci. La seconde section comprend une serie de descripteurs d'attributs definissant des elements des donnees.

Legal Status (Type, Date, Text)
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DIALOG(R)File 349:PCT FULLTEXT
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00784138

SYSTEM, METHOD, AND ARTICLE OF MANUFACTURE FOR A REQUEST BATCHER IN A
TRANSACTION SERVICES PATTERNS ENVIRONMENT
SYSTEME, PROCEDE ET ARTICLE MANUFACTURE POUR MODULE DE MISE EN LOTS DES
REQUETES DANS UN ENVIRONNEMENT CARACTERISE PAR DES SERVICES
TRANSACTIONNELS

Patent Applicant/Assignee:

ACCENTURE LLP, 1661 Page Mill Road, Palo Alto, CA 94304, US, US
(Residence), US (Nationality)

Inventor(s):

BOWMAN-AMUAH Michel K, 6426 Peak Vista Circle, Colorado Springs, CO 80918
, US,

Legal Representative:

HICKMAN Paul L (agent), Oppenheimer Wolff & Donnelly, LLP, 1400 Page
Mills Road, Palo Alto, CA 94304, US,

Patent and Priority Information (Country, Number, Date):

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Priority Application: US 99387575 19990831

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DZ EE ES FI GB GE GH GM HR HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT
LU LV MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR
TT UA UG UZ VN YU ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

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Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 150393

English Abstract

A system, method and article of manufacture are provided for batching
logical requests for reducing network traffic. A group of business
objects necessary for a transaction are provided and managed in a logical
unit of work. Logically-related requests received from the logical unit
of work are grouped into a single network message which is then stored.
The message is sent upon receiving an order to send the message.

French Abstract

La presente invention concerne un systeme, un procede et un article
manufacture destine a la mise en lots des requetes de facon a reduire le
trafic reseau. A cet effet, on constitue un groupe d'objets d'affaire
necessaires a une transaction et on le gere dans une unite logique de
travail. Les requetes entre lesquelles existent des liaisons logiques
sont regroupees en un unique message de reseau qui est alors mis en
memoire. L'envoi du message intervient des la reception d'un ordre
d'envoi du message.

Legal Status (Type, Date, Text)

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00784136

**A SYSTEM, METHOD AND ARTICLE OF MANUFACTURE FOR BUSINESS LOGIC SERVICES
PATTERNS IN A NETCENTRIC ENVIRONMENT**
**SYSTEME, PROCEDE ET ARTICLE DE FABRICATION POUR STRUCTURES DE SERVICES DE
LOGIQUE DE COMMERCE DANS UN ENVIRONNEMENT S'ARTICULANT AUTOUR DE
L'INTERNET**

Patent Applicant/Assignee:

ACCENTURE LLP, 1661 Page Mill Road, Palo Alto, CA 94304, US, US
(Residence), US (Nationality)

Inventor(s):

BOWMAN-AMUAH Michel K, 6426 Peak Vista Circle, Colorado Springs, CO 80918
, US,

Legal Representative:

HICKMAN Paul L (agent), Oppenheimer Wolff & Donnelly, LLP, 38th Floor,
2029 Century Park East, Los Angeles, CA 90067-3024, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200116728 A2-A3 20010308 (WO 0116728)

Application: WO 2000US24197 20000831 (PCT/WO US0024197)

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LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI

SK SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

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International Patent Class: **G06F-009/46**

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 150863

English Abstract

A system, method, and article of manufacture are provided for implementing business logic service patterns for allowing reuse of a business object in a component-based architecture. An attribute dictionary pattern is used for controlling access to data of a business object via an attribute dictionary. A constant class pattern is provided for ensuring correct data at an attribute level. The patterns are utilized for reusing a business object which is classified as a business component, a business service, and/or a business facility.

French Abstract

L'invention porte sur un systeme, un procede et un article de fabrication s'appliquant a la mise en oeuvre de structures de services de logique de commerce en vue d'etre autorise a utiliser un objet commercial dans une architecture a base de composants. Une structure de dictionnaire d'attributs est utilisee pour commander l'accès aux données d'un objet commercial via un dictionnaire d'attributs. Une structure de classement constant assure la correction des données a un niveau d'attributs. Les structures sont utilisees pour reutiliser un objet commercial classifie comme composant commercial, service commercial et/ou installation commerciale.

Legal Status (Type, Date, Text)

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00784135

**A SYSTEM, METHOD AND ARTICLE OF MANUFACTURE FOR A LOCALLY ADDRESSABLE
INTERFACE IN A COMMUNICATION SERVICES PATTERNS ENVIRONMENT
SYSTEME, PROCEDE ET ARTICLE DE PRODUCTION METTANT EN OEUVRE UNE INTERFACE
ADRESSABLE LOCALEMENT DANS UN ENVIRONNEMENT DE CONFIGURATIONS DE
SERVICES DE COMMUNICATION**

Patent Applicant/Assignee:

ACCENTURE LLP, 1661 Page Mill Road, Palo Alto, CA 94304, US, US

(Residence), US (Nationality)

Inventor(s):

BOWMAN-AMUAH Michel K, 6426 Peak Vista Circle, Colorado Springs, CO 80918
, US,

Legal Representative:

HICKMAN Paul L (agent), Oppenheimer Wolff & Donnelly, LLP, 38th Floor,
2029 Century Park East, Los Angeles, CA 09967-3024, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200116727 A2-A3 20010308 (WO 0116727)

Application: WO 2000US24189 20000831 (PCT/WO US0024189)

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FI GB GE GH GM HR HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD

MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG UZ

VN YU ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

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Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 151048

English Abstract

A system, method, and article of manufacture are provided for delivering service via a locally addressable interface. A plurality of globally addressable interfaces and a plurality of locally addressable interfaces are provided. Access is allowed to a plurality of different sets of services from each of the globally addressable interfaces and the locally addressable interface. Each interface has a unique set of services associated therewith. The globally addressable interfaces are registered in a naming service for facilitating access thereto. Use of the locally addressable interfaces is permitted only via the globally addressable interfaces or another locally addressable interface.

French Abstract

L'invention concerne un systeme, un procede et un article de production qui mettent en oeuvre une interface adressable localement pour fournir des services. Plusieurs interfaces adressables globalement et plusieurs interfaces adressables localement sont mises en place. L'accès à plusieurs ensembles de services différents est autorisé à partir de chacune des interfaces adressables globalement et des interfaces adressables localement. A chaque interface est associé un ensemble unique de services. Les interfaces adressables globalement sont enregistrées

dans un service d'affectation de noms pour en faciliter l'accès.
L'utilisation des interfaces adressables localement n'est autorisée que si l'on passe par des interfaces adressables globalement ou par une autre interface adressable localement.

Legal Status (Type, Date, Text)

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Examination 20010809 Request for preliminary examination prior to end of 19th month from priority date

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Republication 20020110 A3 With international search report.

21/5/26 (Item 26 from file: 349)

DIALOG(R) File 349:PCT FULLTEXT

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00784134

A SYSTEM, METHOD AND ARTICLE OF MANUFACTURE FOR A CONSTANT CLASS COMPONENT IN A BUSINESS LOGIC SERVICES PATTERNS ENVIRONMENT

SYSTEME, PROCEDURE ET ARTICLE MANUFACTURE UN COMPOSANT DE CLASSE DE CONSTANTE DANS UN ENVIRONNEMENT DE SCHEMAS DE SERVICES DE LOGIQUE D'AFFAIRES

Patent Applicant/Assignee:

ACCENTURE LLP, 1661 Page Mill Road, Palo Alto, CA 94304, US, US
(Residence), US (Nationality)

Inventor(s):

BOWMAN-AMUAH Michel K, 6426 Peak Vista Circle, Colorado Springs, CO 80918, US,

Legal Representative:

HICKMAN Paul L (agent), Oppenheimer Wolff & Donnelly LLP, Suite 3800, 2029 Century Park East, Los Angeles, CA 90067-3024, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200116726 A2-A3 20010308 (WO 0116726)

Application: WO 2000US24188 20000831 (PCT/WO US0024188)

Priority Application: US 99387213 19990831

Designated States: AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES

FI GB GE GH GM HR HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD

MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG UZ

VN YU ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: **G06F-009/44**

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 150446

English Abstract

A system, method, and article of manufacture are provided for managing constants in a computer program. A plurality of constant names are provided. Each of the constant names has a corresponding constant value. The constant names are grouped into constant classes based on an entity which the constant values represents. Access is allowed to the constant values by receiving a call including the corresponding constant name and corresponding constant class.

French Abstract

L'invention porte sur un système, un procédé et un article de gestion des constantes d'un programme d'ordinateur. On établit les noms de différentes constantes à chacun desquels correspond la valeur d'une constante, puis les noms sont regroupés par classes de constantes en fonction d'une entité représentant les valeurs des constantes. L'accès à une valeur de constante est autorisé lors de la réception d'un appel comprenant le nom et la classe de la constante correspondante.

Legal Status (Type, Date, Text)

Publication 20010308 A2 Without international search report and to be
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Examination 20010809 Request for preliminary examination prior to end of
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DIALOG(R)File 349:PCT FULLTEXT

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00784126

**SYSTEM, METHOD, AND ARTICLE OF MANUFACTURE FOR AN EXCEPTION RESPONSE TABLE
IN ENVIRONMENT SERVICES PATTERNS**

**SYSTEME, PROCEDE ET ARTICLE DE PRODUCTION DESTINES A UNE TABLE DE REPONSE
D'EXCEPTION DANS DES CONFIGURATIONS DE SERVICES D'ENVIRONNEMENT**

Patent Applicant/Assignee:

ACCENTURE LLP, 1661 Page Mill Road, Palo Alto, CA 94304, US, US

(Residence), US (Nationality)

Inventor(s):

BOWMAN-AMUAH Michel K, 6426 Peak Vista Circle, Colorado Springs, CO 80918
, US,

Legal Representative:

HICKMAN Paul L (et al) (agent), Oppenheimer Wolff & Donnelly LLP, 38th
Floor, 2029 century Park East, Los Angeles, CA 90067-3024, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200116706 A2-A3 20010308 (WO 0116706)

Application: WO 2000US24086 20000831 (PCT/WO US0024086)

Priority Application: US 99387873 19990831

Designated States: AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CU CZ DE DK
DZ EE ES FI GB GE GH GM HR HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT
LU LV MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR
TT UA UG UZ VN YU ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: G06F-009/44

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 150318

English Abstract

A system, method and article of manufacture are provided for recording
exception handling requirements for maintaining a consistent error
handling approach. An exception response table is provided in which an
exception is recorded. The context of the exception is entered in the
exception response table and a response for the exception is listed in
the exception response table. The response is subsequently outputted upon
the exception occurring in the context.

French Abstract

L'invention concerne un systeme, un procede et un article de production
qui permettent d'enregistrer des exigences de traitement d'exception dans
le but de maintenir une approche de traitement d'erreurs coherente. Une
table de reponse d'exception est fournie et une exception enregistree
dans cette table. Le contexte de l'exception est entre dans la table de
reponse d'exception apres quoi une reponse pour l'exception est listee
dans la table. Cette reponse est ensuite produite si l'exception apparait
dans le contexte.

Legal Status (Type, Date, Text)

Publication 20010308 A2 Without international search report and to be

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Republication 20011122 A3 With international search report.
Examination 20011220 Request for preliminary examination prior to end of
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21/5/28 (Item 28 from file: 349)
DIALOG(R) File 349: PCT FULLTEXT
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00784125

SYSTEM, METHOD, AND ARTICLE OF MANUFACTURE FOR PIECEMEAL RETRIEVAL IN AN
INFORMATION SERVICES PATTERNS ENVIRONMENT
SYSTEME, PROCEDE ET ARTICLE DE FABRICATION DESTINES A LA RECHERCHE
FRAGMENTAIRE DANS UN ENVIRONNEMENT DE MODELES DE SERVICES
D'INFORMATIONS

Patent Applicant/Assignee:

ACCENTURE LLP, 1661 Page Mill Road, Palo Alto, CA 94304, US, US
(Residence), US (Nationality)

Inventor(s):

BOWMAN-AMUAH Michel K, 6426 Peak Vista Circle, Colorado Springs, CO 80918
, US,

Legal Representative:

HICKMAN Paul L (agent), Oppenheimer Wolff & Donnelly, LLP, 38th Floor,
2029 Century Park East, Los Angeles, CA 90067-3024, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200116705 A2-A3 20010308 (WO 0116705)

Application: WO 2000US24085 20000831 (PCT/WO US0024085)

Priority Application: US 99386433 19990831

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FI GB GE GH GM HR HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD

MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG UZ

VN YU ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: G06F-009/44

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 150355

English Abstract

A system, method and article of manufacture are provided for providing a
warning upon retrieval of objects that are incomplete. An object is
provided with at least one missing attribute. Upon receipt of a request
from an application for the object access to the attributes of the object
is allowed by the application. A warning is provided upon an attempt to
access the attribute of the object that is missing.

French Abstract

L'invention concerne un systeme, un procede et un article de fabrication
concus pour emettre un avertissement lors de l'extraction d'objets qui
sont incomplets. L'objet fourni presente au moins un attribut manquant.
Des la reception d'une requete d'une application pour l'objet, ladite
application autorise l'accès aux attributs de cet objet. Un avertissement
est emis lorsque l'on tente d'accéder a l'attribut manquant de l'objet.

Legal Status (Type, Date, Text)

Publication 20010308 A2 Without international search report and to be
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Examination 20011018 Request for preliminary examination prior to end of
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DIALOG(R)File 349:PCT FULLTEXT
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00777020

**A SYSTEM, METHOD AND ARTICLE OF MANUFACTURE FOR RESOURCE ADMINISTRATION IN
AN E-COMMERCE TECHNICAL ARCHITECTURE**

**SYSTEME, PROCEDE ET ARTICLE MANUFACTURE POUR L'ADMINISTRATION DE RESSOURCES
DANS UNE ARCHITECTURE TECHNIQUE DE COMMERCE ELECTRONIQUE**

Patent Applicant/Assignee:

ACCENTURE LLP, Parkstraat 83, NL-2514 JG 'S Gravenhage, NL, NL
(Residence), NL (Nationality), (For all designated states except: US)

Patent Applicant/Inventor:

UNDERWOOD Roy A, 4436 Hearthmoor Court, Long Grove, IL 60047, US, US
(Residence), US (Nationality), (Designated only for: US)

Legal Representative:

HICKMAN Paul L (agent), Oppenheimer Wolff & Donnelly, LLP, P.O. Box
52037, Palo Alto, CA 94303-0746, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200109791 A2-A3 20010208 (WO 0109791)

Application: WO 2000US20547 20000728 (PCT/WO US0020547)

Priority Application: US 99364161 19990730

Designated States: AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ

DE DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ

LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG

SI SK SL TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: G06F-009/46

International Patent Class: G06F-009/44 ; G06F-017/60

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 136396

English Abstract

A system, method and article of manufacture provide a resources e-commerce technical architecture. One embodiment of the present invention includes first performing network performance modeling on a network. Context objects are shared among a plurality of components executed on a transaction server on the network. Application consistency is maintained by referencing text phrases through a short codes framework. Further, software modules are managed during development of the architecture.

French Abstract

Cette invention se rapporte a un systeme, a un procede et a un article manufacture qui forment une architecture technique de commerce electronique pour l'administration de ressources. Dans un mode de realisation de cette invention, on soumet d'abord un reseau a une operation de modelisation des performances reseau. Les objets contextes sont partages entre plusieurs elements executes sur un serveur de transactions du reseau. On maintient la coherence des applications en referencant des phrases textes via une structure de codes courts. Des modules de logiciels sont en outre geres pendant l'elaboration de cette architecture.

Legal Status (Type, Date, Text)

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Examination 20010719 Request for preliminary examination prior to end of 19th month from priority date

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00573136 **Image available**

COMPUTER SYSTEM AND METHOD FOR OPERATING MULTIPLE OPERATING SYSTEMS IN
DIFFERENT PARTITIONS OF THE COMPUTER SYSTEM AND FOR ALLOWING THE
DIFFERENT PARTITIONS TO COMMUNICATE WITH ONE ANOTHER THROUGH SHARED
MEMORY

SYSTEME ET PROCEDE INFORMATIQUES DE COMMANDE DE SYSTEMES D'EXPLOITATION
MULTIPLES DANS DIFFERENTES PARTITIONS DU SYSTEME INFORMATIQUE ET
PERMETTANT AUX DIFFERENTES PARTITIONS DE COMMUNIQUER ENTRE ELLES PAR
UNE MEMOIRE PARTAGEE

Patent Applicant/Assignee:

UNISYS CORPORATION,

Inventor(s):

GULICK Robert C,
MORRISSEY Douglas E,
CALDARALE Charles Raymond,
VESSEY Bruce Alan,
RUSS Craig F,
TROXELL Eugene W,
MIKKELSEN Hans Christian,
MAUER Sharon M,
CONNELL Maureen P,
HUNTER James R,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200036509 A2 20000622 (WO 0036509)

Application: WO 99US30437 19991217 (PCT/WO US9930437)

Priority Application: US 98215424 19981218

Designated States: BR CA JP AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL
PT SE

Main International Patent Class: G06F-009/54

Publication Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 45843

English Abstract

A computer system comprises a plurality of processing modules that can be configured into different partitions within the computer system, and a main memory. Each partition operates under the control of a separate operating system. At least one shared memory window is defined within the main memory to which multiple partitions have shared access, and each partition may also be assigned an exclusive memory window. Program code executing on different partitions enables those partitions to communicate with each other through the shared memory window. Means are also provided for mapping the physical address space of the processors in each partition to the respective exclusive memory windows assigned to each partition, so that the exclusive memory windows assigned to each partition appear to the respective operating systems executing on those partitions as if they all start at the same base address.

French Abstract

L'invention concerne un systeme informatique qui comprend une pluralite de modules de traitement que l'on peut configurer en differentes partitions dans le systeme informatique, et une memoire principale. Chaque partition fonctionne sous la commande d'un systeme d'exploitation separe. Au moins une fenetre de memoire partagee est definie dans la memoire principale a laquelle plusieurs partitions ont un acces partage, et chaque partition peut aussi se faire attribuer une fenetre de memoire exclusive. L'execution d'un code programme dans differentes partitions permet a ces partitions de communiquer entre elles par la fenetre de

memoire partagee. Cette invention concerne aussi des moyens permettant de projeter l'espace d'adresses physiques des processeurs dans chaque partition dans les fenetres de memoire exclusives respectives attribuees a chaque partition, de facon que les fenetres de memoire exclusives attribuees a chaque partition semblent toutes partir de la meme adresse de base pour les systemes d'exploitation respectifs qui s'executent dans ces partitions.

21/5/31 (Item 31 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00566575 **Image available**

APPLICATION INDEPENDENT MESSAGING SYSTEM
SYSTEME DE MESSAGERIE INDEPENDANT DE L'APPLICATION

Patent Applicant/Assignee:

THE CHASE MANHATTAN BANK,

Inventor(s):

CONNELLY Thomas,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200029948 A1 20000525 (WO 0029948)

Application: WO 99US24366 19991019 (PCT/WO US9924366)

Priority Application: US 98108285 19981113; US 99409300 19990929

Designated States: AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES

FI GB GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV

MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG

UZ VN YU ZA ZW GH GM KE LS MW SD SL SZ TZ UG ZW AM AZ BY KG KZ MD RU TJ

TM AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE BF BJ CF CG CI

CM GA GN GW ML MR NE SN TD TG

Main International Patent Class: **G06F-009/46**

Publication Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 15218

English Abstract

A messaging system that isolates application programs from the underlying transport and routing mechanisms that are actually used to convey messages. This isolation is provided in part through the use of directory files. The directory files contain all of the specific details (e.g. conversion and routing parameters) necessary to interface with the transport and routing mechanisms. As the transport and routing details change (e.g. due to updated versions or new mechanisms) only the directory files need to be changed. The present invention further includes Message Processing Servers (MPSs) which provide additional messaging functionality such as message storage, tracing, reconciliation, statistical auditing, reformatting, content based routing and multicasting. As additional functionality is developed, this functionality can be flexibly incorporated into a new MPS.

French Abstract

La presente invention concerne un systeme de messagerie qui isole l'application de programmes des couches inferieures de transport et d'acheminement actuellement utilisees pour envoyer des messages. Cette isolement decoule en partie de l'utilisation de fichiers repertoires. Ces derniers contiennent tous les details specifiques (tels que les parametres de conversion et d'acheminement) necessaires a l'interface avec les couches de transport et acheminement. Lorsque les details concernant le transport et l'acheminement changent, du fait par exemple de versions mises a jour ou de nouveaux mecanismes, seuls les fichiers repertoires doivent etre changes. En outre, cette invention comprend des serveurs de traitement de messages (MPS) qui offrent une fonctionnalite de messagerie supplementaire telle que stockage, historique, recollement, controle statistique, reformatage, acheminement d'apres le contenu et multidiffusion. Lorsqu'une fonctionnalite supplementaire est developpee, elle vient s'introduire en souplesse avec un nouveau MPS.

21/5/32 (Item 32 from file: 349)
DIALOG(R) File 349:PCT FULLTEXT
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00468008 **Image available**

NETWORK SECURITY AND INTEGRATION METHOD AND SYSTEM

**PROCEDE ET SYSTEME DE SECURITE POUR RESEAU ET INTEGRATION DUDIT SYSTEME
DANS UN RESEAU EXISTANT**

Patent Applicant/Assignee:

NICKLES Alfred,

Inventor(s):

NICKLES Alfred,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9858473 A2 19981223

Application: WO 98US12571 19980615 (PCT/WO US9812571)

Priority Application: US 97878279 19970618

Designated States: AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES
FI GB GE GH GM GW HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD
MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG UZ
VN YU ZW

Main International Patent Class: H04L-029/06

International Patent Class: G06F-001/00 ; H04L-009/08

Publication Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 12051

English Abstract

A method of the present invention includes a network security system (10) that has a single point of access control (24) to a source computer system (20). The network security system (10) provides various mechanisms for securing access to source computer systems (20) that includes generating single-use encryption keys, generating random port assignments for communication between devices, an asynchronous message protocol used in the security system and utilizing various levels of transaction tables to help secure and manage security parameters of the system. More particularly, the present invention provides a method for securing access to a plurality of computers (20) connected via a network (26, or 22). An indication is received that a first user of a first computer program module of a first computer (16) desires to communicate with a destination computer system (20). When this indication is received, a message is directed to a security computer system (24). The security computer system (24) determines whether the first user of the first computer (16) is authorized to access the destination computer program module of the destination computer system (20). If the security computer system (24) determines that the first user is authorized to access the destination computer system (20), the security computer system (24) sets up a communication protocol between the first computer program module and the destination computer program module.

French Abstract

L'invention concerne un procede comportant un systeme (10) de securite pour reseau dote d'un seul point de controle d'accès (24) a un systeme informatique source (20). Ce systeme (10) de securite pour reseau comporte divers mecanismes permettant de rendre plus sur l'accès a des systemes informatiques source (20). Le procede consiste a generer des cles de cryptage a usage unique, a generer des affectations de points d'accès aleatoires permettant la communication entre des dispositifs; un protocole de message asynchrone utilise dans ce systeme de securite et utilisant divers niveaux de tables de transaction destinees a rendre plus faciles la securite et la gestion des parametres de securite de ce systeme. Plus particulierement, la presente invention concerne un procede permettant de rendre plus sur l'accès a une serie d'ordinateurs (20) connectes par l'intermediaire d'un reseau (26 ou 22). Une indication selon laquelle un premier utilisateur d'un premier module de programme

d'un premier ordinateur (16) desire communiquer avec un systeme informatique de destination (20) est recue . Lorsque cette indication est recue, un message est adresse a un systeme informatique de securite (24) qui determine si le premier utilisateur du premier ordinateur (16) est autorise a acceder au module de programme du systeme informatique de destination (20). Si le systeme informatique de securite (24) determine que le premier utilisateur est autorise a acceder au systeme informatique de destination (20), le systeme informatique de securite (24) etablit un protocole de communication entre le module de programme du premier ordinateur et le module de programme de l'ordinateur de destination.

21/5/33 (Item 33 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00431190 **Image available**

**LOG BASED DATA ARCHITECTURE FOR A TRANSACTIONAL MESSAGE QUEUING SYSTEM
ARCHITECTURE DE DONNEES A JOURNALISATION POUR SYSTEME TRANSACTIONNEL DE
GESTION DE FILES D'ATTENTE DE MESSAGES**

Patent Applicant/Assignee:

MITSUBISHI ELECTRIC INFORMATION TECHNOLOGY CENTER AMERICA INC,

Inventor(s):

WONG David W H,

SCHWENKE Derek L,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9821654 A1 19980522

Application: WO 97US20561 19971111 (PCT/WO US9720561)

Priority Application: US 9630905 19961114

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GR IE IT LU MC NL PT SE

Main International Patent Class: G06F-011/00

International Patent Class: G06F-12:00

Publication Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 104413

English Abstract

A message queuing system is provided that saves and stores messages and their state in an efficient single file on a single disk to enable rapid recovery from server failures. The single disk, single file storage system into which messages and their states are stored eliminates writes to three different disks, the data disk, the index structure disk and the log disk. The single disk, single file storage is made possible by clustering all information together in a contiguous space on the same disk. The result is that all writes are contained in one sweeping motion of the write head in which the write head moves only in one direction and only once to find the area where it needs to start writing messages and their states are stored. In order to keep track of the clustered information, a unique Queue Entry Map Table (100) is used which includes control information (100), message blocks (102) and log records (104) in conjunction with single file disk storage that allows the write head never to have to back-up to traverse saved data when writing new records. The system also permits locating damaged files without the requirement of scanning entire log files.

French Abstract

La presente invention concerne un systeme de gestion de files d'attente de messages permettant de sauvegarder et de stocker des messages et leurs etats dans un fichier unique efficace residant sur un seul disque de facon a recuperer rapidement les defaillances d'un serveur. Le systeme de stockage monodisque monofichier, dans lequel des messages et leurs etats sont stockes, permet d'eliminer des operations d'ecriture sur trois disques differents: le disque de donnees, le disque de structure d'index et le disque de journalisation. Le stockage monodisque monofichier est rendu possible par le regroupement des informations dans un espace

contigu sur le meme disque. Toutes les operations d'ecriture sont, donc, effectuees en un seul mouvement de balayage de la tete de lecture qui se deplace dans un seul sens et une seule fois de facon a trouver la zone dans laquelle elle doit commencer a ecrire des messages et stocker leurs etats. Pour conserver une trace des informations regroupees, on utilise une seule table de correspondance d'entree de mise en file d'attente (100) comportant des informations de gestion (100), des blocs de messages (102) et des articles de journalisation (104) conjointement avec un stockage sur disque monofichier permettant a la tete de lecture de ne jamais avoir, a des fins de sauvegarde, a faire defiler des donnees sauvegardees lors de l'ecriture de nouveaux articles. Le systeme permet, egalement, de localiser des fichiers endommages sans qu'il soit necessaire de balayer la totalite des fichiers de journalisation.

21/5/34 (Item 34 from file: 349)

DIALOG(R) File 349: PCT FULLTEXT

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00412371 **Image available**

OBJECT GRAPH EDITING CONTEXT AND METHODS OF USE

CONTEXTE D'EDITION DE GRAPHIQUES D'OBJETS ET SES PROCEDES D'UTILISATION

Patent Applicant/Assignee:

NEXT SOFTWARE INC,

Inventor(s):

FEDERIGHI Craig,

NOYAU Eric,

WILLHITE Dan,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9802832 A1 19980122

Application: WO 97US13466 19970715 (PCT/WO US9713466)

Priority Application: US 96682198 19960717

Designated States: AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES

FI GB GE HU IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG MK MN MW

MX NO NZ PL PT RO RU SD SE SG SI SK TJ TM TR TT UA UG UZ VN GH KE LS MW

SD SZ UG ZW AM AZ BY KG KZ MD RU TJ TM AT BE CH DE DK ES FI FR GB GR IE

IT LU MC NL PT SE BF BJ CF CG CI CM GA GN ML MR NE SN TD TG

Main International Patent Class: **G06F-017/30**

Publication Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 10994

English Abstract

The present invention comprises a novel system for managing changes to a graph of data bearing objects. In one embodiment, an object graph manager object referred to as an editing context is used to identify changes made to data bearing enterprise objects and to notify other interested objects when changes occur. As a result, data bearing objects need not themselves contain code necessary for monitoring changes. In another embodiment of the invention, the editing context is used to provide event-based "undo" capabilities. In another embodiment of the invention, each enterprise object has a primary key that is used to maintain the identification between an enterprise object instance and a corresponding database row. In another embodiment of the invention, multiple levels of editing contexts are used to provide multiple isolated object graphs, each of which allows independent manipulation of the underlying data bearing objects.

French Abstract

Nouveau systeme de gestion des modifications apportees a un graphique d'objets portant des donnees. Dans un mode de realisation, un objet de gestion de graphique d'objets, appele un contexte d'edition, est utilise pour identifier des modifications apportees a des objets d'entreprise portant des donnees, et pour avertir d'autres objets concernes quand des modifications ont lieu. En consequence, les objets portant des donnees ne doivent pas eux-memes contenir des codes necessaires pour surveiller les

modifications. Dans un autre mode de realisation, le contexte d'edition est utilise pour fournir de fonctions "annuler" declenchees par des evenements. Dans une autre variante de l'invention, chaque objet d'entreprise comprend une cle principale qui est utilisee pour maintenir l'identification entre une instance d'objet d'entreprise et une rangee de base de donnees correspondante. Dans encore une autre variante, plusieurs niveaux de contextes d'edition sont utilises pour produire des graphiques d'objets isoles multiples, dont chacun permet la manipulation independante des objets portant des donnees sous-jacents.

21/5/35 (Item 35 from file: 349)

DIALOG(R) File 349:PCT FULLTEXT

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00406194 **Image available**

METHOD AND STRUCTURE FOR BALANCED QUEUE COMMUNICATION BETWEEN NODES IN A DISTRIBUTED COMPUTING APPLICATION

PROCEDE ET STRUCTURE DE COMMUNICATIONS ENTRE NOEUDS A FILES D'ATTENTE EQUILIBREES DANS UNE APPLICATION INFORMATIQUE REPARTIE

Patent Applicant/Assignee:

NEW ERA OF NETWORKS INC,

Inventor(s):

PISKIEL Harold Aron,

MUI Gerald Sui,

PELIZZOLI Paolo Hendrik Natale,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9746939 A1 19971211

Application: WO 97US8856 19970605 (PCT/WO US9708856)

Priority Application: US 96658382 19960605

Designated States: JP SG AT BE CH DE DK ES FI FR GB GR IE IT LU MC NL PT SE

Main International Patent Class: **G06F-009/46**

Publication Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 13395

English Abstract

Methods and associated structure providing a balanced queue communication system for the exchange of messages between an originating node and a receiving node in a distributed computing environment. A balanced queue comprises a pair of wraparound (circular) queues, one each associated with the originating node and the receiving node. A message is queued by the originated node in the queue associated therewith. Background processing within the originated node retrieves message queued therein and transmits the messages to the intended receiving node. The position in the originating node's circular queue is pre-pended to the message and transmitted therewith. The receiving node places the received message in its circular queue in the position specified by the pre-pended location with each message. Background daemon and application processing within the receiving node then retrieves the queued messages for application specific processing. The queue control structures of the balanced queue are integrated with the transaction processing recovery management tables on each of the respective nodes to assure synchronization of the messaging queues of the balanced queue with the application specific processing. These methods and structures of the balanced queue assure robust exchange of messages, synchronized with the application's database, while improving the message processing performance as compared to prior solutions. Messages are delivered "exactly once" while reducing overhead (thereby improving performance) as compared to prior techniques.

French Abstract

L'invention porte sur des procedes et les structures associees constituant un systeme de communication a files d'attente equilibrees pour l'echange de messages entre un noeud emetteur et un noeud recepteur dans un environnement informatique reparti. Une file d'attente equilibree

comporte une paire de files d'attente circulaires associees l'une a un noeud emetteur et l'autre a un noeud recepteur. Un message est mis en file d'attente par le noeud emetteur dans la file d'attente lui etant associee. Le traitement de fond a l'interieur du noeud emetteur retrouve les messages s'y trouvant en file d'attente et les transmet au noeud recepteur voulu. La position a l'interieur de la file d'attente circulaire du noeud emetteur est placee avant le message et transmise avec lui. Le noeud recepteur place le message reçu dans sa file d'attente circulaire a la position indiquee par l'emplacement place avant chaque message. Le traitement de fond des demons et le traitement des applications dans le noeud recepteur recuperent alors les messages en file d'attente en vue du traitement specifique a l'application. Les structures de commande des files d'attente equilibrees sont integrees aux tables de gestion de recuperation des traitement de transactions pour chacun des differents noeuds de maniere a assurer la synchronisation des files d'attente de messagerie de la file d'attente equilibree avec le traitement specifique a une application. Ces procedes et structures de mise en file d'attente equilibree permettent un echange important de messages, synchronise avec la base de donnees de l'application, tout en ameliorant les performances du traitement des messages par rapport aux solutions anterieures. Les messages sont fournis "exactement une fois" tout en reduisant le temps systeme (ce qui ameliore les performances) par rapport aux techniques anterieures.

Set	Items	Description
S1	89135	MESSAGE OR MESSAGING
S2	2945	S1(2N) (MANAG? OR INTERFACE? OR QUEUE? OR MIDDLEWARE?) OR MQ OR MQI
S3	2322	COMMIT? OR UNCOMMIT?
S4	202062	DEFINED() (ORDER? OR RANK? OR ORDER?) OR LIST()ENTRY? OR LI- ST()STRUCTURE? OR KEY? ?
S5	4422613	VALUE? OR HIGHEST? OR LOWEST? OR EXTREME? OR FIRST? OR LA- ST? OR GREATEST? OR LEAST? OR MOST?
S6	1	S2 AND S3 AND S4
S7	10	S2 AND S3
S8	106	S2 AND S4
S9	26	S8 AND S5
S10	35	S6 OR S7 OR S9
S11	2	S8 AND MC=(T01-F02C2 OR T01-N01C OR T01-J05B3 OR T01-G03)
S12	1	S10 AND IC=G06F-007?
S13	844	S2 AND (S3 OR S4 OR S5)
S14	7	S13 AND IC=G06F-007?
S15	302	S13 AND IC=G06F?
S16	16	S10 AND IC=G06F?
S17	24	S6 OR S7 OR S11 OR S12 OR S14 OR S16
S18	24	IDPAT (sorted in duplicate/non-duplicate order)
S19	24	IDPAT (primary/non-duplicate records only)
File 344:Chinese Patents Abs Aug 1985-2003/Jan		
(c) 2003 European Patent Office		
File 347:JAPIO Oct 1976-2002/Nov(Updated 030306)		
(c) 2003 JPO & JAPIO		
File 350:Derwent WPIX 1963-2003/UD,UM &UP=200321		
(c) 2003 Thomson Derwent		

19/5/1 (Item 1 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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015028593 **Image available**
WPI Acc No: 2003-089110/200308
XRPX Acc No: N03-070171

Network equipment e.g. switch used in computer network, transmits status message of quality of service parameters of virtual circuit to router through communication interface

Patent Assignee: CISCO TECHNOLOGY INC (CISC-N)
Inventor: LANGLEY D; RAWAT V
Number of Countries: 001 Number of Patents: 001
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 6446122	B1	20020903	US 98103755	A	19980624	200308 B

Priority Applications (No Type Date): US 98103755 A 19980624

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 6446122	B1	13	G06F-015/173	

Abstract (Basic): US 6446122 B1

NOVELTY - A storage (250) stores quality of service (QoS) parameters such as **committed** information rate (CIR), **committed** burst size (Bc), excess burst size (Be), etc., of virtual circuits. A status provider (252) retrieves the QoS parameters and provides status message to a communication interface (240). The **interface** transmits the **message** to a router (204).

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are included for the following:

- (1) User equipment;
- (2) Quality of service parameter receiving method;
- (3) Quality of service parameter provision method;
- (4) Computer program product for receiving quality of service parameters;
- (5) Quality of service parameter receiving system; and
- (6) Computer program product for providing QoS parameters.

USE - Network equipment e.g. switch used in computer network.

ADVANTAGE - By transmitting the status message of the QoS parameters from the switch to the router, the parameters in the router are matched with that of the switch, hence reduces efforts to maintain compatibility of the parameters between the network devices.

DESCRIPTION OF DRAWING(S) - The figure shows a schematic block diagram of the router and switch.

Router (204)
Communication interface (240)
QoS parameters storage (250)
Status provider (252)
pp; 13 DwgNo 2/3

Title Terms: NETWORK; EQUIPMENT; SWITCH; COMPUTER; NETWORK; TRANSMIT;
STATUS; MESSAGE; QUALITY; SERVICE; PARAMETER; VIRTUAL; CIRCUIT; ROUTER;
THROUGH; COMMUNICATE; INTERFACE

Derwent Class: T01; W01

International Patent Class (Main): G06F-015/173

International Patent Class (Additional): H04L-012/28

File Segment: EPI

19/5/2 (Item 2 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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014995341 **Image available**
WPI Acc No: 2003-055856/200305
Radio resource simulator

Patent Assignee: HYNIX SEMICONDUCTOR INC (HYNI-N)

Inventor: CHOI E J

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
KR 2002057176	A	20020711	KR 200087451	A	20001230	200305 B

Priority Applications (No Type Date): KR 200087451 A 20001230

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
KR 2002057176	A	1	H04B-017/00	

Abstract (Basic): KR 2002057176 A

NOVELTY - A radio resource simulator is provided to perform a test with high reliability and easily develop a software by performing the test of the software under various hardware environment.

DETAILED DESCRIPTION - An RRC(Radio Resource Control) module(101) defines an interface with an RLC(Radio Link Control) module(102) and a MAC(Medium Access Control) module(103) as a message type designated in the CCITT(International Telegraph and Telephone Consultative Committee) recommendation, defines an NAS(Non-Access Stratum) **interface** as the **message** type designated in the CCITT recommendation, and control radio resources. The RLC module(102) measures the performance of a flow control algorithm through buffer management. The MAC module(103) performs the mapping of a transport channel and a logic channel, defines an interface for control information with the RRC module(101) and an interface for user information with the RLC module(102), and maps a lower transport channel to an ATM(Asynchronous Transfer Mode) connection. An RTDB(Real Time DataBase)(104) stores radio resources in real time. A data collecting and transmitting module(105) collects and transmits data. A GUI(Graphic User Interface) module(106) performs a GUI and a data interface. A microprocessor(108) controls an entire operation for a radio resource test.

pp; 1 DwgNo 1/10

Title Terms: RADIO; RESOURCE; SIMULATE

Derwent Class: W02

International Patent Class (Main): H04B-017/00

File Segment: EPI

19/5/3 (Item 3 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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014797348 **Image available**

WPI Acc No: 2002-618054/200266

XRPX Acc No: N02-489228

Message **retrieval** managing **method** in **data processing apparatus**, involves **monitoring availability** of message in queue with reference to assigned key used for identifying committed messages having specified attribute value

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)

Inventor: HOPEWELL P; KETTLEY P; NICK J M; SIDDALL P; WARNES J H

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20020087507	A1	20020704	US 2000219889	A	20000721	200266 B
			US 2001909538	A	20010720	

Priority Applications (No Type Date): US 2000219889 P 20000721; US 2001909538 A 20010720

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 20020087507	A1	13	G06F-007/00	Provisional application US 2000219889

Abstract (Basic): US 20020087507 A1

NOVELTY - An index **key** having attribute **value** of message is assigned to the message in response to **commitment** operation of putting the **message** on the **queue**. The availability of message is

monitored in response to message retrieval request and with reference to assigned **key** used for identifying **committed** messages having specified attribute **value**.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are included for the following:

- (1) Program product storing program code instructions;
- (2) Data processing apparatus; and
- (3) Resource manager component.

USE - For **managing message** retrieval in data processing apparatus (claimed).

ADVANTAGE - Provides an efficient queue indexing solution to avoid retrieval of **uncommitted** messages, complexities of lock allocation and maintenance in a shared queue environment and provides notification to waiting requesters in the shared queue environment.

DESCRIPTION OF DRAWING(S) - The figure shows a schematic representation of queue manager program having shared access to a coupling facility.

pp; 13 DwgNo 1/4

Title Terms: MESSAGE; RETRIEVAL; MANAGE; METHOD; DATA; PROCESS; APPARATUS; MONITOR; AVAILABLE; MESSAGE; QUEUE; REFERENCE; ASSIGN; **KEY**; IDENTIFY; **COMMIT**; MESSAGE; SPECIFIED; ATTRIBUTE; **VALUE**

Derwent Class: T01

International Patent Class (Main): G06F-007/00

File Segment: EPI

19/5/4 (Item 4 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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014752311 **Image available**

WPI Acc No: 2002-573015/200261

XRPX Acc No: N02-453934

Message communication method involves storing messages and sequence identifier sent by source device, in destination device such that messages are available to user regardless of sending of commit message

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)

Inventor: BRITTON K H; CITRON A P; HOUSEL B C; WESLEY A A

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 6401136	B1	20020604	US 98191637	A	19981113	200261 B

Priority Applications (No Type Date): US 98191637 A 19981113

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 6401136	B1	14	G06F-009/46	

Abstract (Basic): US 6401136 B1

NOVELTY - A source device sends the stored messages with an associated sequence identifier to a destination device via an external communication link. The destination device sends a **commit** message identifying the large sequence identifier, to the source device upon receiving the messages and sequence identifier. A user accesses the stored messages in the destination device regardless of the sending of the **commit** message.

USE - Applicable for communication between source device and destination device over external communication link using queue-to-queue transfer system.

ADVANTAGE - Enables efficient transfer of fault tolerant message from source device to destination device in different and unreliable communication environments. Enables quick access to data for application communication using **message queue**.

DESCRIPTION OF DRAWING(S) - The figure shows the flowchart for operation of a source device.

pp; 14 DwgNo 2/3

Title Terms: MESSAGE; COMMUNICATE; METHOD; STORAGE; MESSAGE; SEQUENCE; IDENTIFY; SEND; SOURCE; DEVICE; DESTINATION; DEVICE; MESSAGE; AVAILABLE;

USER; SEND; COMMIT ; MESSAGE
Derwent Class: T01
International Patent Class (Main): G06F-009/46
File Segment: EPI

19/5/5 (Item 5 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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014668385 **Image available**
WPI Acc No: 2002-489089/200252
Related WPI Acc No: 2002-403571; 2002-739611
XRPX Acc No: N02-386617

Electronic message transmission method e.g. for electronic-mail, involves
encrypting decrypted message using encryption key and transmitting
encrypted message to recipient based on request for message from
recipient

Patent Assignee: ANDERSON E D (ANDE-I)
Inventor: ANDERSON E D
Number of Countries: 001 Number of Patents: 001
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20020052923	A1	20020502	US 99231158	A	19990115	200252 B
			US 2001943892	A	20010829	

Priority Applications (No Type Date): US 99231158 A 19990115; US 2001943892
A 20010829

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 20020052923	A1	15	G06F-015/16	Div ex application	US 99231158

Abstract (Basic): US 20020052923 A1

NOVELTY - The reference of an electronic messages is distributed to recipients, when encrypted electronic-message is received in a server (132). Based on request for electronic-message from recipient, the encrypted message is decrypted and then encrypted using an encryption key . The encrypted message is transmitted to recipient.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are included for the following:

(1) Recorded medium storing electronic message transmission program; and

(2) Computer system.

USE - For transmitting electronic message such as e-mail, paging messages, transmitted documents, etc., to user.

ADVANTAGE - Recipient systems require only a small amount of storage space and need not leave necessary software to save and manage the electronic- message . Removal or modification of the message is easily accomplished.

DESCRIPTION OF DRAWING(S) - The figure shows the block diagram of the message distribution server system.

Server (132)

pp; 15 DwgNo 1/7

Title Terms: ELECTRONIC; MESSAGE; TRANSMISSION; METHOD; ELECTRONIC; MAIL;
MESSAGE; ENCRYPTION; KEY ; TRANSMIT; ENCRYPTION; MESSAGE; RECIPIENT;
BASED; REQUEST; MESSAGE; RECIPIENT

Derwent Class: T01; W01
International Patent Class (Main): G06F-015/16
File Segment: EPI

19/5/6 (Item 6 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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014650400 **Image available**
WPI Acc No: 2002-471104/200250
XRPX Acc No: N02-371946

Resource manager failure recovery method for data processing network, involves sharing units of work relating to data storage structure having failed connection with respective queuing subsystem

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)

Inventor: KETTLEY P; SIDDALL P; WARNES J H

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20020059316	A1	20020516	US 2000220685	A	20000725	200250 B
			US 2001912279	A	20010724	

Priority Applications (No Type Date): US 2000220685 P 20000725; US 2001912279 A 20010724

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 20020059316	A1	14	G06F-012/00	Provisional application US 2000220685

Abstract (Basic): US 20020059316 A1

NOVELTY - A data storage structure of a message queuing system has failed connection with a respective data storage structure of a shared queue, and a notification of connection failure is sent to other subsystem. The unit of work descriptors are analyzed to identify the units of work relating to respective data storage structure that is done by the connection failed queuing system, to share the identified unit of work.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are included for the following:

- (1) Distributed data processing system; and
- (2) Computer program product for connection failure recovery between queuing subsystem and shared queue.

USE - For recovering connection failures affecting a resource manager such as message queuing subsystem within data processing network for cash transaction applications.

ADVANTAGE - Provides sufficient information in the shared storage within the connecting facility. If one of the message queuing subsystem in one computer fails, another message queuing subsystem in another computer is able to back-out or commit the units of work in progress on the failed computer at the time of failure, hence the operation efficiency is improved.

DESCRIPTION OF DRAWING(S) - The figure shows a message queuing system for the recovery of connection failure.

pp; 14 DwgNo 2/5

Title Terms: RESOURCE; MANAGE; FAIL; RECOVER; METHOD; DATA; PROCESS; NETWORK; SHARE; UNIT; WORK; RELATED; DATA; STORAGE; STRUCTURE; FAIL; CONNECT; RESPECTIVE; QUEUE; SUBSYSTEM

Derwent Class: T01; T05; W01

International Patent Class (Main): G06F-012/00

File Segment: EPI

19/5/7 (Item 7 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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013859029

WPI Acc No: 2001-343242/200136

XPX Acc No: N01-248584

Counter-measure method for public key algorithm uses modulo calculations to prevent detection of data from current consumed by microprocessor

Patent Assignee: GEMPLUS (GEMP-N); GEMPLUS SCA (GEMP-N)

Inventor: CORON J; PAILLIER P; CORON J S; PAILLER P

Number of Countries: 095 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
WO 200128153	A1	20010419	WO 2000FR2880	A	20001013	200136 B
FR 2799851	A1	20010420	FR 9912991	A	19991014	200136
AU 200110315	A	20010423	AU 200110315	A	20001013	200147

EP 1224765 A1 20020724 EP 2000971463 A 20001013 200256
WO 2000FR2880 A 20001013

Priority Applications (No Type Date): FR 9912991 A 19991014

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes

WO 200128153 A1 F 27 H04L-009/30

Designated States (National): AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA
CH CN CR CU CZ DE DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP
KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT
RO RU SD SE SG SI SK SL TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW

Designated States (Regional): AT BE CH CY DE DK EA ES FI FR GB GH GM GR
IE IT KE LS LU MC MW MZ NL OA PT SD SE SL SZ TZ UG ZW

FR 2799851 A1 G06F-012/14

AU 200110315 A H04L-009/30 Based on patent WO 200128153

EP 1224765 A1 F H04L-009/30 Based on patent WO 200128153

Designated States (Regional): AL AT BE CH CY DE DK ES FI FR GB GR IE IT
LI LT LU LV MC MK NL PT RO SE SI

Abstract (Basic): WO 200128153 A1

NOVELTY - The system uses a series of modulo calculations to
conceal the data relating to secret information.

DETAILED DESCRIPTION - The counter-measure procedure uses the
Chinese rest theorem (CRT), taking a random calculation for each new
execution of the decryption algorithm. The process includes
calculations of modulo pasteriskr and qasteriskr, where r and t are
random numbers, taking as the input the message, c, and a decryption
value, d, and a security parameter, s. Three random numbers are
selected (r, t and u) between 0 and 2s. Values p'=pasteriskr and
q'=qasteriskr are calculated. c is replaced by c+uasteriskn, and
values cp, cq, dp, dq, mp' and mq' are calculated from the
equations: cp=c modulo p' and cq=c modulo q'; dp=d' modulo p-1 and dq=d'
modulo q-1; mp'=cpdp modulo p' and mq'='cqdq modulo q'. A value m is
then determined: m=((mq'-mp)asterisk(p-1 mod q) mod q')asteriskp+mp. m
is then replaced by m mod n.

USE - Counter measure for RAS-type public key cryptographic
algorithm.

ADVANTAGE - Prevents revelation of information relating to secret
data by consumption of current of microprocessor executing
instructions.

pp; 27 DwgNo 0/0

Title Terms: COUNTER; MEASURE; METHOD; PUBLIC; KEY; ALGORITHM; MODULO;
CALCULATE; PREVENT; DETECT; DATA; CURRENT; CONSUME; MICROPROCESSOR

Derwent Class: W01

International Patent Class (Main): G06F-012/14; H04L-009/30

International Patent Class (Additional): G06K-019/073

File Segment: EPI

19/5/8 (Item 8 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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013057065 **Image available**

WPI Acc No: 2000-228933/200020

XRPX Acc No: N00-172141

Cash transaction processing system in bank, has queue manager which
forwards message immediately by indication from change payment
processing program

Patent Assignee: NTT DATA TSUSHIN KK (NITE)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 2000047986	A	20000218	JP 98211018	A	1998072	200020 B

Priority Applications (No Type Date): JP 98211018 A 19980727

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes

Abstract (Basic): JP 2000047986 A

NOVELTY - The queue manager (75) forwards a message immediately by the indication from the change payment processing program (71), without waiting for completion of the **committing** process (244).

USE - In bank.

ADVANTAGE - The message forwarding to other process from one process and the message arrival queuing time in the other process is reduced, since the **queue manager** forwards **message** immediately without waiting for completion of the **committing** process.

DESCRIPTION OF DRAWING(S) - The figure shows the functional block diagram of the components of cash transaction processing system.

Change payment processing program (71)

Queue manager (75)

Committing process (244)

pp; 14 DwgNo 4/14

Title Terms: CASH; TRANSACTION; PROCESS; SYSTEM; BANK; QUEUE; MANAGE; FORWARD; MESSAGE; IMMEDIATE; INDICATE; CHANGE; PAY; PROCESS; PROGRAM

Derwent Class: T01

International Patent Class (Main): G06F-015/00

International Patent Class (Additional): G06F-009/46 ; G06F-012/00 ;

G06F-019/00

File Segment: EPI

19/5/9 (Item 9 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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012866165 **Image available**

WPI Acc No: 2000-037998/200003

Related WPI Acc No: 2001-354191; 2001-416477; 2002-254611; 2002-642466

XRPX Acc No: N00-028641

End to end response time measuring method for computer programs in network application

Patent Assignee: CANDLE DISTRIBUTED SOLUTIONS INC (CAND-N)

Inventor: AMMERMAN R P; KLEIN P F

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5991705	A	19991123	US 97899195	A	19970723	200003 B

Priority Applications (No Type Date): US 97899195 A 19970723

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes

US 5991705 A 8 G04F-010/00

Abstract (Basic): US 5991705 A

NOVELTY - When a message is received at inbound **message queue** (114) starting time is assigned for the message. The message is then processed by application (112). When a subsequent message is received in response to the **first** message, a stop time is assigned for the initial message. Based on the difference between start time and stop time of the initial message, end-to-end response time is calculated.

DETAILED DESCRIPTION - The subsequent message which is sent in response to the initial message is selected from a group of messages such as create message, mouse button 1 down message, enter **key** message and button activation message. When another message is received in response to the initial message, the stop time is updated. After updating the stop time, the end-to-end response time is calculated again. An INDEPENDENT CLAIM is also included for end-to-end response time measurement apparatus.

USE - For measuring end-to-end response time of computer programs in network application.

ADVANTAGE - As start time and stop time are assigned to each message which arrives at **message queue**, the end-to-end response time is calculated reliably.

DESCRIPTION OF DRAWING(S) - The figure shows block diagram of various software components explaining the end-to-end response time measurement.

Application (112)

Inbound **message queue** (114)

pp; 8 DwgNo 2/3

Title Terms: END; END; RESPOND; TIME; MEASURE; METHOD; COMPUTER; PROGRAM; NETWORK; APPLY

Derwent Class: S04; T01

International Patent Class (Main): G04F-010/00

International Patent Class (Additional): **G06F-013/00**

File Segment: EPI

19/5/10 (Item 10 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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012864977

WPI Acc No: 2000-036810/200003

XRPX Acc No: N00-027587

Matrix commutator assembly

Patent Assignee: UNIV KURSK TECH (UYKU-R)

Inventor: KOLOSKOV V A; TITOV V S; ZOTOV I V

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
RU 2116664	C1	19980727	RU 96108431	A	19960424	200003 B

Priority Applications (No Type Date): RU 96108431 A 19960424

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
RU 2116664	C1		G06F-007/00	

Abstract (Basic): RU 2116664 C1

NOVELTY - Device has **first**, second and third units for processing **message queues**, multiplexer, unit for analysis of **message queues**, register, decoder, synchronization unit, flip-flop, **first** AND gate. Goal of invention is achieved by introduced message transmission unit, second to eighth AND gates, **first** to fourth OR gates. Device provides connection of any of three input channels to any of three output channels. Messages from different input channels are received simultaneously.

USE - Commutation of special-purpose multiprocessor systems, microcontroller networks and units of parallel information exchange in telemetry systems.

ADVANTAGE - Increased functional capabilities, possibility to design commutation structures for adaptive message routing, facilitated scalability for fixed message length. 7 dwg, 1 tbl1

pp; 0 DwgNo 0/0

Title Terms: MATRIX; COMMUTATE; ASSEMBLE

Derwent Class: T01

International Patent Class (Main): **G06F-007/00**

International Patent Class (Additional): G06F-015/163

File Segment: EPI

19/5/11 (Item 11 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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012280258 **Image available**

WPI Acc No: 1999-086364/199908

XRPX Acc No: N99-062758

Transaction processing method for message queuing system - involves using asynchronous message queue to maintain conformity of data between systems

Patent Assignee: HITACHI LTD (HITA)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 10320251	A	19981204	JP 97128281	A	19970519	199908 B

Priority Applications (No Type Date): JP 97128281 A 19970519

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
JP 10320251	A		5 G06F-012/00	

Abstract (Basic): JP 10320251 A

The method involves sending a message from a client through a communication channel of low capability and reliability. The conformity of data between systems is maintained using an asynchronous **message queue**.

A **committing** apparatus decides the result of a transaction based on notices from all sub **committing** apparatuses. The client determines the result of transaction by accessing a sequence queue.

USE - For transaction processing system using communication channel of low capability and reliability.

ADVANTAGE - Enables client to definitely know result of transaction. Enables continuation of other processes when transaction queue is generated.

Dwg.1/4

Title Terms: TRANSACTION; PROCESS; METHOD; MESSAGE; QUEUE; SYSTEM;

ASYNCHRONOUS; MESSAGE; QUEUE; MAINTAIN; CONFORM; DATA; SYSTEM

Derwent Class: T01; W01

International Patent Class (Main): G06F-012/00

International Patent Class (Additional): G06F-009/46 ; G06F-013/00 ;

G06F-015/00

File Segment: EPI

19/5/12 (Item 12 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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012216616

WPI Acc No: 1999-022722/199902

XRPX Acc No: N99-017387

Linking SAP system to MQ series server - using transaction MQ queue holding messages indicating processing of transaction by server

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
RD 415085	A	19981110	RD 98415085	A	19981020	199902 B

Priority Applications (No Type Date): RD 98415085 A 19981020

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
RD 415085	A		1 G06F-000/00	

Abstract (Basic): RD 415085 A

The method involves using a transaction **MQ** queue to hold two **MQ** messages which denote the state of the transaction. The first is an 'In progress' message, which includes the way that the transaction ID and the process ID of the handler. The second message is a 'Failed' message, which includes the transaction ID and the process ID. Both messages are placed on the queue in an atomic way, and subsequently the failure message is acquired under syncpoint, so that it is invisible to other processes browsing the queue.

When a process server is already handling the IDoc passed to it, any other process can query the queue to see an 'in progress' message for the first process, and tell the SAP that the transaction is already being processed. After the first process has received the incoming IDoc, it informs SAP, which will ask the server to **commit** the transaction. The server **commits** the unit of work, saving the IDoc

data and removing the failed message. SAP then calls the server to tidy up the queue.

ADVANTAGE - Ensures only one process handling of IDoc data.

Dwg.0/0

Title Terms: LINK; SAP; SYSTEM; SERIES; SERVE; TRANSACTION; QUEUE; HOLD;

MESSAGE; INDICATE; PROCESS; TRANSACTION; SERVE

Derwent Class: T01

International Patent Class (Main): G06F-000/00

File Segment: EPI

19/5/13 (Item 13 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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011980307 **Image available**

WPI Acc No: 1998-397217/199834

XRFX Acc No: N98-308960

Multi-media data processing relay - comprises controller which connects peripherals including telephone, fax, display, printer and mass storage device

Patent Assignee: SHIAU R (SHIA-I)

Inventor: SHIAU R

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
TW 325548	A	19980121	TW 97106171	A	19970509	199834 B

Priority Applications (No Type Date): TW 97106171 A 19970509

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
TW 325548	A	31	G06F-003/00	

Abstract (Basic): TW 325548 A

The relay comprises: (1) a telephone; (2) a fax; (3) a input/output device including phone line socket with at **least** one line, one network socket and at **least** one parallel port, which can individually used to connect phone line, Internet and PC to receive or send every kind of multimedia data from phone, fax, Internet or PC; (4) a display; (5) a control panel including multiple numerical and function **keys**, and each function **key** has drawn simple and easy graph symbol or English abbreviation representing the function **key**; (6) a printer for text or graphics; (7) a power supply; (8) a multimedia store which includes a system ROM, a RAM, and a hard disk; (9) a controller, which consists of a data access unit (DAA) and a central processing unit (CPU), in which the DAA can consistently **manage** multimedia **message** data access and automatically identify message data type, and the CPU is used to consistently control operation of each unit and apparatus.

The controller can identify type of every kind of multimedia message data received from the I/O device and store it to the data storage unit. It then generates an index datum shown on the display, which the user only need check to know all index data of every kind of received multimedia message. Through function **keys** on the control panel, the user can preview some message data content on the display, print, send data to the PC from the I/O device's parallel port, send a fax, or reserve or delete data in the mass storage.

USE - For consolidating, processing, storing, displaying or printing multimedia data such as phone, fax, e-mail or electronic files.

ADVANTAGE - Uses 'Harbour port' technique so that any type of incoming data can be classified and stored in a user friendly manner.

Dwg.1/4

Title Terms: DATA; PROCESS; RELAY; COMPRISE; CONTROL; CONNECT; PERIPHERAL;

TELEPHONE; FACSIMILE; DISPLAY; PRINT; MASS; STORAGE; DEVICE

Derwent Class: T01; T04; W01; W02

International Patent Class (Main): G06F-003/00

File Segment: EPI

19/5/14 (Item 14 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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010260674 **Image available**

WPI Acc No: 1995-161929/199521

XRPX Acc No: N95-126981

Inter-program communication in transaction-oriented network - uses asynchronous message queuing between queue managers, such that send and receive operations remain uncommitted before resolution

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)

Inventor: BLACK G; CLARKE P; DREW R M; JOHNSON P; KINGSTON W; MELI R

Number of Countries: 022 Number of Patents: 008

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
WO 9510805	A1	19950420	WO 93GB2086	A	19931008	199521 B
EP 673523	A1	19950927	EP 93922031	A	19931008	199543
			WO 93GB2086	A	19931008	
JP 8504991	W	19960528	WO 93GB2086	A	19931008	199646
			JP 95511425	A	19931008	
CN 1109233	A	19950927	CN 94116773	A	19940929	199734
EP 673523	B1	19990210	EP 93922031	A	19931008	199911
			WO 93GB2086	A	19931008	
DE 69323499	E	19990325	DE 623499	A	19931008	199918
			EP 93922031	A	19931008	
			WO 93GB2086	A	19931008	
CA 2148459	C	20000111	CA 2148459	A	19931008	200023
			WO 93GB2086	A	19931008	
KR 223290	B1	19991015	WO 93GB2086	A	19931008	200108
			KR 95702328	A	19950608	

Priority Applications (No Type Date): WO 93GB2086 A 19931008

Cited Patents: 02Jnl.Ref; EP 457108

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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WO 9510805	A1	E	31	G06F-009/46	
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Designated States (National): CA JP KR US

Designated States (Regional): AT BE CH DE DK ES FR GB GR IE IT LU MC NL PT SE

EP 673523	A1	E	31	G06F-009/46	Based on patent WO 9510805
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Designated States (Regional): AT BE CH DE ES FR GB IT LI NL SE

JP 8504991	W		29	G06F-015/00	Based on patent WO 9510805
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CN 1109233	A			H04L-029/02	
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EP 673523	B1	E		G06F-009/46	Based on patent WO 9510805
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Designated States (Regional): AT BE CH DE ES FR GB IT LI NL SE

DE 69323499	E			G06F-009/46	Based on patent EP 673523
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Based on patent WO 9510805

CA 2148459	C	E		G06F-009/46	Based on patent WO 9510805
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KR 223290	B1			G06F-015/18	
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Abstract (Basic): WO 9510805 A

Within the network, a sender program sends messages from a first node and a receiver program received messages at a second node. Messages are transmitted by the sender within a first sync-point-manager-controlled unit of work, and received by the receiver within a second sync-point-manager-controlled unit of work. The sending and receiving operations are **uncommitted** until resolution of both units of work, which are logically linked.

For successful message receipt, the receiver **commits** the second unit and issues a positive confirmation for the sender to **commit** the first unit. For an unsuccessful receipt, the receiver rolls back the second unit and issues a negative confirmation for the sender to back out the first unit.

ADVANTAGE - Ensures that no messages are lost and none are delivered more than once. Avoids additional message flow that is feature of two-phase **commit** procedures, avoiding need for resource managers to synchronise with each other.

Dwg.3/5

Title Terms: INTER; PROGRAM; COMMUNICATE; TRANSACTION; ORIENT; NETWORK;
ASYNCHRONOUS; MESSAGE; QUEUE; QUEUE; SEND; RECEIVE; OPERATE; REMAINING;
UNCOMMITTED ; RESOLUTION
Derwent Class: T01
International Patent Class (Main): **G06F-009/46 ; G06F-015/00 ;**
G06F-015/18 ; H04L-029/02
International Patent Class (Additional): **G06F-011/14 ; G06F-012/00 ;**
G06F-013/00
File Segment: EPI

19/5/15 (Item 15 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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008426925 **Image available**
WPI Acc No: 1990-313926/199042
Related WPI Acc No: 1983-748316; 1985-074407; 1989-348161; 1990-211604
XRPX Acc No: N90-240801

**High speed multiplier in single chip microcomputer - uses modified
Harvard architecture to provide Booth's algorithm decoders static adder
levels and ripple-carry adder**

Patent Assignee: TEXAS INSTR INC (TEXI)
Inventor: CAUDEL E R; MAGAR S S
Number of Countries: 003 Number of Patents: 003
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 392133	A	19901017	EP 90100186	A	19821223	199042 B
EP 392133	B1	19971008	EP 82306902	A	19821223	199745
			EP 90100186	A	19821223	
DE 3280477	G	19971113	DE 3280477	A	19821223	199751
			EP 90100186	A	19821223	

Priority Applications (No Type Date): US 82350852 A 19820222; US 82347859 A
19820211; US 82347860 A 19820211

Cited Patents: 3.Jnl.Ref; US 3757306

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
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EP 392133	A			
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Designated States (Regional): DE FR GB

EP 392133	B1 E	28	G06F-007/52	Div ex application EP 82306902 Div ex patent EP 86307
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Designated States (Regional): DE FR GB

DE 3280477	G		G06F-007/52	Based on patent EP 392133
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Abstract (Basic): EP 392133 A

A **first** sixteen-bit operand is applied to a register (T-REGISTER) and the second operand is input on a bus (D-BUS). A set of right Booth's decoders (Mb) each receives two bits from the T-register outputs and the set produces eight sets of outputs (Mc), each set having five functions.

Eight static adder levels are provided (**Mq** -1 to **Ma**-8) each level includes a control section receiving decoder outputs (Mc). Each level except the **lowest** has sixteen parallel binary adder stages. Each stage of each level receives one of the sixteen bits from the D-bus. Lines (Mf) connect partial products and carries from one level to the next higher level. No carry is coupled along adder stages within a level. A thirty-one stage ripple-carry adder (Md) receives partial products from the static adder levels and provides, with sign-extension a thirty-two bit product in the product register (P). (28pp

Dwg.No.4b/4)

Title Terms: HIGH; SPEED; MULTIPLIER; SINGLE; CHIP; MICROCOMPUTER; MODIFIED
; ARCHITECTURE; BOOTH; ALGORITHM; DECODE; STATIC; ADDER; LEVEL; RIPPLE;
CARRY; ADDER

Derwent Class: T01

International Patent Class (Main): **G06F-007/52**

International Patent Class (Additional): **G06F-007/52**

File Segment: EPI

19/5/16 (Item 16 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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008346559 **Image available**
WPI Acc No: 1990-233560/199031
XRPX Acc No: N90-181131

Fast contact conversational video system - has prestored interest message expanded and inverted in command line when interest message code provided to interact conversational contacts

Patent Assignee: REUTERS LTD (REUT-N)
Inventor: ORDISH C J
Number of Countries: 002 Number of Patents: 003
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
GB 2227625	A	19900801	GB 8923937	A	19891024	199031 B
US 5034916	A	19910723	US 88261578	A	19881024	199132
GB 2227625	B	19930127	GB 8923937	A	19891024	199304

Priority Applications (No Type Date): US 88261578 A 19881024

Patent Details:
Patent No Kind Lan Pg Main IPC Filing Notes
GB 2227625 B H04M-011/06

Abstract (Basic): GB 2227625 A

The system (30) provides fast contact in a conventional video system by use of a r (200) in connection with a windowed display of a financial data page and a unique subscriber identification code with or without an interest message code. When an interest message code is provided, the prestored interest message is expanded in the command line. If a double click is employed with the mouse (200) screen pointer, contact is automatically initiated with the called party and the interest message transmitted. However, if only a single click is detected, the interest message is inserted in the command line without being automatically transmitted.

The subscriber may then double click his mouse screen pointer (200) at the location of the code in the display (76) or may press the TRANSMIT key on the keyboard (72) to send the interest message to the called party. The system (30) may also use CONTACT LISTS to initiate conversational contacts.

ADVANTAGE - Increased contact speed. (48pp Dwg.No.1/17

Title Terms: FAST; CONTACT; CONVERSATION; VIDEO; SYSTEM; INTEREST; MESSAGE; EXPAND; INVERT; COMMAND; LINE; INTEREST; MESSAGE; CODE; INTERACT; CONVERSATION; CONTACT

Derwent Class: W01; W02

International Patent Class (Main): H04M-011/06

International Patent Class (Additional): G06F-003/14

File Segment: EPI

19/5/17 (Item 17 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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008345624 **Image available**
WPI Acc No: 1990-232625/199031
XRPX Acc No: N90-180426

Single-FIFO high speed combining switch for data transmission - has two multiplexers which receive inputs from data processors and directs message to output port for transmission to memory module

Patent Assignee: IBM CORP (IBMC); INT BUSINESS MACHINES CORP (IBMC)
Inventor: HSU Y
Number of Countries: 005 Number of Patents: 006

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
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EP 379709	A	19900801	EP 89123610	A	19891221	199031	B
JP 2234543	A	19900917	JP 90215133	A	19900126	199043	
US 5046000	A	19910903	US 89303699	A	19890127	199138	
EP 379709	A3	19920408	EP 89123610	A	19891221	199328	
EP 379709	B1	19950823	EP 89123610	A	19891221	199538	
DE 68923951	E	19950928	DE 623951	A	19891221	199544	
			EP 89123610	A	19891221		

Priority Applications (No Type Date): US 89303699 A 19890127

Cited Patents: NoSR.Pub; 1.Jnl.Ref; US 4750154

Patent Details:

Patent No. Kind Lan Pg Main IPC Filing Notes

EP 379709 A

Designated States (Regional): DE FR GB

EP 379709 B1 E 10 G06F-007/22

Designated States (Regional): DE FR GB

DE 68923951 E G06F-007/22 Based on patent EP 379709

Abstract (Basic): EP 379709 A

The combining switch (10) includes a two input multiplexer (12) which receives I and J inputs from data processors and directs one of the incoming messages, if there are no contentions or congestions at a switch output port (14) and a Queue FIFO (16) is empty, directly to the output port (14) for transmission to one of a number of memory modules. If the output port is busy and the Queue is empty the incoming message is routed to the Queue FIFO for storage. If the Queue FIFO is not empty the incoming message is compared using comparator (20) to all existing messages stored in the Queue to determine if the message is destined for a memory address which already has a **queued message**.

If no match is determined by the comparator, the incoming message is routed to the Queue for storage. If the comparator determines that the memory address and operation type of the incoming message matches a message already stored in the Queue, both the incoming **message** and the **queued message** are applied to a message combining ALU (26). The ALU generates a combined message stored at the same Queue location as the **queued message** which generated a comparison match with the incoming message.

ADVANTAGE - Has improved packing density, and reduced power consumption. (7pp Dwg.No.2/2

Title Terms: SINGLE; FIFO; HIGH; SPEED; COMBINATION; SWITCH; DATA; TRANSMISSION; TWO; MULTIPLEX; RECEIVE; INPUT; DATA; PROCESSOR; DIRECT; MESSAGE; OUTPUT; PORT; TRANSMISSION; MEMORY; MODULE

Derwent Class: T01; W01

International Patent Class (Main): G06F-007/22

International Patent Class (Additional): G06F-005/06; G06F-013/16;

H04L-012/54

File Segment: EPI

19/5/18 (Item 18 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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008300786 **Image available**

WPI Acc No: 1990-187787/199025

XRPX Acc No: N90-146049

Conversation analysing video communication system - has message switching interface routing data and controlled subscriber key -stations working in real-time

Patent Assignee: REUTERS LTD (REUT-N)

Inventor: ORDISH C J; RICHARDS J M

Number of Countries: 002 Number of Patents: 003

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
GB 2226217	A	19900620	GB 8923936	A	19891024	199025	B
GB 2226217	B	19930127	GB 8923936	A	19891024	199304	
US 5195031	A	19930316	US 88261984	A	19881024	199313	

Priority Applications (No Type Date): US 88261984 A 19881024

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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US 5195031	A		308	G06F-015/21	
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GB 2226217	B			H04M-011/06	
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Abstract (Basic): GB 2226217 A

The system has at **least** a portion of a number of subscriber terminals comprising subscriber **key** stations. Each of the subscriber **key** stations comprise a video display for providing a textual display of data input to the network. The network includes a **message** switching **interface** network for routing video conversational textual data throughout the network, and at **least** one conversation analysing **key** station terminal controller interface routing data input by the one subscriber **key** station to another designated subscriber **key** station through the **message** switching **interface** network and for receiving data input to the **message** switching **interface** network by the designated other subscriber **key** station for controlling communication of the conversation between them.

The conversation analysing **key** station terminal controller comprises appts. for analysing the conversation substantially in real time for providing messages in conjunction with the conversation based on the real time conversation analysis.

USE - Subscriber to subscriber video data communication in conversational mode for e.g. commodity dealing.

Dwg.1/34

Title Terms: CONVERSATION; ANALYSE; VIDEO; COMMUNICATE; SYSTEM; MESSAGE; SWITCH; INTERFACE; ROUTE; DATA; CONTROL; SUBSCRIBER; **KEY** ; STATION; WORK ; REAL-TIME

Derwent Class: T05; W01; W02

International Patent Class (Main): G06F-015/21 ; H04M-011/06

File Segment: EPI

19/5/19 (Item 19 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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007650259 **Image available**

WPI Acc No: 1988-284191/198840

XRPX Acc No: N88-215820

Coding and image-processing modulo-M multiplier - has multi-input adder controlling one switch for input inversion and another for inversion of result

Patent Assignee: PHYS MECH INST (PHYS-R)

Inventor: VARICHENKO L V

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
SU 1383339	A	19880323	SU 4090600	A	19860715	198840 B

Priority Applications (No Type Date): SU 4090600 A 19860715

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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SU 1383339	A		10		
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Abstract (Basic): SU 1383339 A

The circuitry contg. the **first** -operand direct (1) and inverse (2) inputs, second-operand direct inputs (3), partial products forming (5) and summing (6) circuits and the corrector (7), has the switches (4,8), a multi-input single-digit adder (9) and a group of NOT-gates (10). The **values** of the operands a and b can be taken from registers. The product of the numbers a and b in modulo $MQ = a.b \text{ (modulo } M)$ is the remainder of division of the usual product a.b by the **value** of M. For $M = 1-2$, $-a = a\text{-bar}$, i.e. a negative number in a ring is obtd. as a result of inversion of the corresp. position number. The adder (9) controls the switches. The direct or inverse **value** of the multiplier is fed to the partial products forming circuit which receives the multiplicand from the inputs (3) and forms k partial product words

which are summed and corrected. The product $M=2$ (to the power of n) - 1 is switched to the outputs (11). USE/ADVANTAGE - In computer engineering and information-measuring systems in digital processing of signals, esp. images and in coders using the theory of finite rings and Galois fields, operating speed is increased. Bul.11/23.3.88. (10pp Dwg.No.1/10)

Title Terms: CODE; IMAGE; PROCESS; MODULO; MULTIPLIER; MULTI; INPUT; ADDER; CONTROL; ONE; SWITCH; INPUT; INVERT; INVERT; RESULT

Derwent Class: T01

International Patent Class (Additional): G06F-007/49

File Segment: EPI

19/5/20 (Item 20 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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007595275 **Image available**

WPI Acc No: 1988-229207/198833

XRPX Acc No: N88-174419

Network inter-node communication method for data processing - using node unique key mapping to second node unique queue key used for formulating actual address

Patent Assignee: IBM CORP (IBMC); INT BUSINESS MACHINES CORP (IBMC)

Inventor: JOHNSON D W; LOUCKS L K; SHAHEENGOU A A; SHAHEEN-GOUDA A A

Number of Countries: 004 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 278316	A	19880817	EP 88101090	A	19880126	198833 B
US 5133053	A	19920721	US 8714888	A	19870213	199232
EP 278316	B1	19941207	EP 88101090	A	19880126	199502
DE 3852324	G	19950119	DE 3852324	A	19880126	199508
			EP 88101090	A	19880126	

Priority Applications (No Type Date): US 8714888 A 19870213

Cited Patents: 2.Jnl.Ref; A3...9039; EP 201063; No.SR.Pub

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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EP 278316	A	E	19		
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Designated States (Regional): DE FR GB

US 5133053	A		14	G06F-009/00	
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EP 278316	B1	E	21	G06F-009/46	
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Designated States (Regional): DE FR GB

DE 3852324	G			G06F-009/46	Based on patent EP 278316
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Abstract (Basic): EP 278316 A

The processors (40) may operate either as stand-alone systems or as part of a local or wide area network (30) in association with various storage media (48) and peripheral input/output devices, performing processes (44) under the control of an operating system (46) including a kernel (50). A queue profile record (52) is created by execution of a library routine called by an application's installation program, supplying a queue name with a local **key** and the actual **key** and node for the queue which is thereby designated.

USE/ADVANTAGE - In queue-based communication among multiprocessing and multitasking nodes in distributed services network. Advanced Interacting Executive operating system's interprocess communication message facilities are extended with capability of sending and receiving among queues at different nodes.

1/6

Title Terms: NETWORK; INTER; NODE; COMMUNICATE; METHOD; DATA; PROCESS; NODE ; UNIQUE; **KEY** ; MAP; SECOND; NODE; UNIQUE; QUEUE; **KEY** ; FORMULATION;

ACTUAL; ADDRESS

Derwent Class: T01

International Patent Class (Main): G06F-009/46

International Patent Class (Additional): G06F-013/00 ; G06F-015/16

File Segment: EPI

19/5/21 (Item 21 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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003327635

WPI Acc No: 1982-H5649E/198225

Multiplier decoder using parallel multiplication register - has multiplier signal register, ALU, decoder registers for ALU data and data signal source

Patent Assignee: SPERRY CORP (SPER)

Inventor: WONG W T C

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 4334284	A	19820608				198225 B

Priority Applications (No Type Date): US 79108339 A 19791231

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 4334284	A		21		

Abstract (Basic): US 4334284 A

The processor architecture permits multiple bit shifting over strings of binary 1's and strings of binary 0's in a single machine cycle. During a multiply operation, an **MQ** register (arranged in parallel) which stored the multiplier, shifts the multiplier out for decoding at a rate comparable to the rate at which the partial product is shifted. This is made possible by using a parallel **MQ** register so that two bits may be shifted per clock cycle.

This architecture permits **extremely** fast multiplication by using a multiple bit shift architecture while minimising hardware requirements. The decoder effectively looks for strings of zeroes or ones in the multiplier and causes the partial product of data stored in two separate registers to be shifted by multiple bits by a combinational shifter.

Title Terms: MULTIPLIER; DECODE; PARALLEL; MULTIPLICATION; REGISTER; MULTIPLIER; SIGNAL; REGISTER; ALU; DECODE; REGISTER; ALU; DATA; DATA; SIGNAL; SOURCE

Index Terms/Additional Words: ARITHMETIC; LOGIC; UNIT

Derwent Class: T01

International Patent Class (Additional): G06F-007/52

File Segment: EPI

19/5/22 (Item 22 from file: 347)
DIALOG(R)File 347:JAPIO
(c) 2003 JPO & JAPIO. All rts. reserv.

06490533 **Image available**
TRANSACTIONAL MESSAGE PROCESSING METHOD

PUB. NO.: 2000-076114 [JP 2000076114 A]
PUBLISHED: March 14, 2000 (20000314)
INVENTOR(s): CHATANI KENICHI
IWASAKI MOTOAKI
KITAGAWA MAKOTO
MASUISHI TETSUYA
APPLICANT(s): HITACHI LTD
APPL. NO.: 10-246745 [JP 98246745]
FILED: September 01, 1998 (19980901)
INTL CLASS: G06F-012/00 ; G06F-017/30

ABSTRACT

PROBLEM TO BE SOLVED: To provide a message processing method for **committing** a transaction by a 1-phase **commitment** in a transaction processing including a message processing and a data base processing.

SOLUTION: A transactional message processing is realized by the data base processing. A queue management program 112 stores a **message** in a **queue** table 108 and the state of a queue in a queue state table 107 and processes the message by using a data base management program 113. The transaction is managed by a transaction management program 114.

COPYRIGHT: (C)2000, JPO

19/5/23 (Item 23 from file: 347)
DIALOG(R)File 347:JAPIO
(c) 2003 JPO & JAPIO. All rts. reserv.

03908419 **Image available**
END BATCH **MANAGEMENT SYSTEM FOR MESSAGE**

PUB. NO.: 04-273519 [JP 4273519 A]
PUBLISHED: September 29, 1992 (19920929)
INVENTOR(s): MASHITA HITOSHI
YAMAGATA MASAYUKI
APPLICANT(s): NEC SOFTWARE LTD [491061] (A Japanese Company or Corporation)
, JP (Japan)
APPL. NO.: 03-033838 [JP 9133838]
FILED: February 28, 1991 (19910228)
INTL CLASS: [5] **G06F-003/14**
JAPIO CLASS: 45.3 (INFORMATION PROCESSING -- Input Output Units)
JOURNAL: Section: P, Section No. 1484, Vol. 17, No. 66, Pg. 1,
February 09, 1993 (19930209)

ABSTRACT

PURPOSE: To operate the correction of a message without affecting a related program, and to obtain a means valid to the standardization of the program by **managing** the **message** en bloc by an outside medium.

CONSTITUTION: This system is equipped with a message master file 1 which stores an alphanumeric KANA (Japanese syllabary) message and a Japanese message corresponding to each message code constituted of both the classification of the message and a message number, and updates the content from a message updating terminal 3, and a **message managing** means 2 which reads at **least** one of the alphanumeric KANA message and the Japanese message from the message master file 1 by using the message code as a **key**, and displays the message at a message display terminal 4.

19/5/24 (Item 24 from file: 347)
DIALOG(R)File 347:JAPIO
(c) 2003 JPO & JAPIO. All rts. reserv.

01011629 **Image available**
STATISTICAL INFORMATION GATHERING SYSTEM IN ON-LINE DATA BASE SYSTEM

PUB. NO.: 57-161929 [JP 57161929 A]
PUBLISHED: October 05, 1982 (19821005)
INVENTOR(s): OGUSHI YUKITOSHI
APPLICANT(s): FUJITSU LTD [000522] (A Japanese Company or Corporation), JP
(Japan)
APPL. NO.: 56-045616 [JP 8145616]
FILED: March 30, 1981 (19810330)
INTL CLASS: [3] **G06F-007/22** ; G06F-015/40
JAPIO CLASS: 45.1 (INFORMATION PROCESSING -- Arithmetic Sequence Units);
45.2 (INFORMATION PROCESSING -- Memory Units); 45.4
(INFORMATION PROCESSING -- Computer Applications)
JOURNAL: Section: P, Section No. 166, Vol. 07, No. 2, Pg. 44, January
07, 1983 (19830107)

ABSTRACT

PURPOSE: To grasp accurately the inclination of a queue to reduce the load of the information processing system, by gathering cummulatively the queue at each time and integrating it in respect to time and obtaining an average

value of the queue at a prescribed time.

CONSTITUTION: A supplied message is transmitted to a queue processing part 13 and is sent to a queue storage part 71. A clock 31 sets a sampling period. **First**, integration is executed. An integral **value** **a1** is obtained by a **message queue** 711 and a **message queue** node 712. This **value** **a1** is stored in a message transmitting part 14 and is stored in astorage part 12. Hereafter, **values** **a2** and **a3** are processed similarly. For **values** **a1**, **a2**, and **a3**, a cumulative **value** **A** is calculated in an information gathering part 32 and is stored in a logging file 4. Similarly, the second and the third cumulative **values** **B** and **C** are filed. On a basis of cumulative **values** **A**, **B**, and **C**, an integral **value**, an average **value** **L3**, a maximum **value** **L2**, and a minimum **value** **L1** in all of sampling time are calculated by a PDA5 and are stored in an information report part 51, and respective numeric **values** are printed out.

Set	Items	Description
S1	42	AU=(ELKO D? OR ELKO, D?)
S2	9	AU=(DIEVENDORFF R? OR DIEVENDORFF, R?)
S3	22	AU=(FLAHERTY D? OR FLAHERTY, D?)
S4	77	AU=(NICK J? OR NICK, J?)
S5	22	AU=(SURMAN D? OR SURMAN, D?)
S6	6	AU=(WARNES J? OR WARNES, J?)
S7	42	AU=(WESTCOTT D? OR WESTCOTT, D?)
S8	0	S1 AND S2 AND S3 AND S4 AND S5 AND S6 AND S7
S9	5	(S1 OR S2 OR S3 OR S4 OR S5 OR S6) AND MESSAGE()QUEUE
S10	10	(S1 OR S2 OR S3 OR S4 OR S5 OR S6) AND IC=(G06F-007? OR G0-6F-017?)
S11	15	S9 OR S10
S12	15	IDPAT (sorted in duplicate/non-duplicate order)
S13	14	IDPAT (primary/non-duplicate records only)

File 344:Chinese Patents Abs Aug 1985-2003/Jan
(c) 2003 European Patent Office

File 347:JAPIO Oct 1976-2002/Nov(Updated 030306)
(c) 2003 JPO & JAPIO

File 348:EUROPEAN PATENTS 1978-2003/Mar W03
(c) 2003 European Patent Office

File 349:PCT FULLTEXT 1979-2002/UB=20030327,UT=20030320
(c) 2003 WIPO/Univentio

File 350:Derwent WPIX 1963-2003/UD,UM &UP=200321
(c) 2003 Thomson Derwent

13/5/1 (Item 1 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2003 Thomson Derwent. All rts. reserv.

014968494 **Image available**
WPI Acc No: 2003-029008/200302
XRPX Acc No: N03-022846

Inter-partition data processing method in computer system, involves allocating resources to partitioned operating systems based on created resource balancing directives

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC); IBM UK LTD (IBMC)
Inventor: KUBALA J P; NICK J M ; TEMPLE J L; YOCOM P B; KUBALA J; NICK J ; TEMPLE J; YOCOM P

Number of Countries: 100 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20020129085	A1	20020912	US 2001801993	A	20010308	200302 B
WO 200273397	A2	20020919	WO 2002GB456	A	20020201	200302

Priority Applications (No Type Date): US 2001801993 A 20010308

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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US 20020129085	A1		29	G06F-015/16	
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WO 200273397	A2	E		G06F-009/00	
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Designated States (National): AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA
CH CN CO CR CU CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN
IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ
OM PH PL PT RO RU SD SE SG SI SK SL TJ TM TN TR TT TZ UA UG UZ VN YU ZA
ZM ZW

Designated States (Regional): AT BE CH CY DE DK EA ES FI FR GB GH GM GR
IE IT KE LS LU MC MW MZ NL OA PT SD SE SL SZ TR TZ UG ZM ZW

Abstract (Basic): US 20020129085 A1

NOVELTY - Resource balancing directives for the partitioned resource are created in a partition manager, based on the information obtained from a partitioned operating system (614). Resources are allocated to another partition (615) based on the resource balancing directives.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are included for the following:

- (1) Computer system resources allocating program; and
- (2) Computer system resources allocating system.

USE - Used in uniprocessor and multiprocessor computer systems.

ADVANTAGE - Supports multiple heterogeneous operating system partitions to pass messages between their memory locations at memory speed without sharing memory locations. Facilitates movement of data from one memory space of one partition directly to another memory space of another partition.

DESCRIPTION OF DRAWING(S) - The figure shows an illustrative view of the partitioned data processing system.

Partitioned operating systems (614, 615)

pp; 29 DwgNo 6/16

Title Terms: INTER; PARTITION; DATA; PROCESS; METHOD; COMPUTER; SYSTEM;
ALLOCATE; RESOURCE; PARTITION; OPERATE; SYSTEM; BASED; RESOURCE; BALANCE;
DIRECT

Derwent Class: T01

International Patent Class (Main): G06F-009/00; G06F-015/16

International Patent Class (Additional): G06F-009/24; G06F-015/177;

G06F-017/00

File Segment: EPI

13/5/2 (Item 2 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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014797348 **Image available**
WPI Acc No: 2002-618054/200266

XRPX Acc No: N02-489228

Message retrieval managing method in data processing apparatus, involves monitoring availability of message in queue with reference to assigned key used for identifying committed messages having specified attribute value

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)

Inventor: HOPEWELL P; KETTLEY P; **NICK J M** ; SIDDALL P; **WARNES J H**

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20020087507	A1	20020704	US 2000219889	A	20000721	200266 B
			US 2001909538	A	20010720	

Priority Applications (No Type Date): US 2000219889 P 20000721; US 2001909538 A 20010720

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 20020087507	A1	13	G06F-007/00	Provisional application US 2000219889

Abstract (Basic): US 20020087507 A1

NOVELTY - An index key having attribute value of message is assigned to the message in response to commitment operation of putting the message on the queue. The availability of message is monitored in response to message retrieval request and with reference to assigned key used for identifying committed messages having specified attribute value.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are included for the following:

- (1) Program product storing program code instructions;
- (2) Data processing apparatus; and
- (3) Resource manager component.

USE - For managing message retrieval in data processing apparatus (claimed).

ADVANTAGE - Provides an efficient queue indexing solution to avoid retrieval of uncommitted messages, complexities of lock allocation and maintenance in a shared queue environment and provides notification to waiting requesters in the shared queue environment.

DESCRIPTION OF DRAWING(S) - The figure shows a schematic representation of queue manager program having shared access to a coupling facility.

pp; 13 DwgNo 1/4

Title Terms: MESSAGE; RETRIEVAL; MANAGE; METHOD; DATA; PROCESS; APPARATUS; MONITOR; AVAILABLE; MESSAGE; QUEUE; REFERENCE; ASSIGN; KEY; IDENTIFY; COMMIT; MESSAGE; SPECIFIED; ATTRIBUTE; VALUE

Derwent Class: T01

International Patent Class (Main): **G06F-007/00**

File Segment: EPI

13/5/3 (Item 3 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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014189091 **Image available**

WPI Acc No: 2002-009788/200201

XRPX Acc No: N02-008156

Fully associative non-linear item browsing method for data processing, involves segmenting collective items into various segments and simultaneously browsing various segments using several browsers in parallel

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)

Inventor: **ELKO D A** ; **NICK J M** ; **SURMAN D H** ; WILKINSON W W

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 6317744	B1	20011113	US 99379103	A	19990823	200201 B

Priority Applications (No Type Date): US 99379103 A 19990823

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes
US 6317744 B1 113 G06F-017/30

Abstract (Basic): US 6317744 B1

NOVELTY - Browsing of data of an unsegmented fully associative non-linear collection of items is initiated. The collection is segmented and the segments containing the data are browsed cooperatively by several browsers in parallel.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are also included for the following:

- (a) Fully associative non-linear item browsing system;
- (b) Fully associative non-linear item browsing computer program storage device

USE - For browsing fully associative non-linear items for data processing within a shared facility of the computing environment.

ADVANTAGE - By segmenting the collection of data and simultaneously browsing the data by several browsers, efficient browsing of very large collections of items is achieved.

DESCRIPTION OF DRAWING(S) - The figure shows an example of a computing environment for browsing fully associative collection of items.

pp; 113 DwgNo 1/51

Title Terms: ASSOCIATE; NON; LINEAR; ITEM; METHOD; DATA; PROCESS; SEGMENT;
COLLECT; ITEM; VARIOUS; SEGMENT; SIMULTANEOUS; VARIOUS; SEGMENT; PARALLEL
Derwent Class: T01
International Patent Class (Main): G06F-017/30
File Segment: EPI

13/5/4 (Item 4 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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013911965 **Image available**
WPI Acc No: 2001-396178/200142
XRPX Acc No: N01-291780

Apparatus for conducting a high performance locking facility in a loosely coupled environment with plural processing units coupled to a high power locking facility

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)
Inventor: BOONIE M A; NICK J M ; SUTTON P G; WILKINSON W W; YEH P C
Number of Countries: 001 Number of Patents: 001
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 6189007	B1	20010213	US 98143324	A	19980828	200142 B

Priority Applications (No Type Date): US 98143324 A 19980828

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes
US 6189007 B1 8 G06F-017/30

Abstract (Basic): US 6189007 B1

NOVELTY - Data processing systems (110,112,114,116) each have one or more central processor units (CPU) (190) accessing the locking facility, which can provide locks to a variety of different components that require it ranging from data to programs to applications. The system provides for receiving and/or processing of multiple requests for locking operations simultaneously and a lock table will be provided to dynamically create, alter and update entries reflecting lock status. When a request is received, the table is searched using a high speed searching means to locate a lock entry and create an appropriate response.

DETAILED DESCRIPTION - AN INDEPENDENT CLAIM is included for a method for receiving and processing lock operations.

USE - Performing high performance locking facility in a loosely coupled environment.

DESCRIPTION OF DRAWING(S) - The drawing is a block diagram of the system environment

Data processing systems (110,112,114,116)
CPU (190)
pp; 8 DwgNo 1/5
Title Terms: APPARATUS; CONDUCTING; HIGH; PERFORMANCE; LOCK; FACILITY;
LOOSE; COUPLE; ENVIRONMENT; PLURAL; PROCESS; UNIT; COUPLE; HIGH; POWER;
LOCK; FACILITY
Derwent Class: T01
International Patent Class (Main): G06F-017/30
File Segment: EPI

13/5/5 (Item 5 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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013881763 **Image available**
WPI Acc No: 2001-365975/200138
XRPX Acc No: N01-266891

**Backup data maintenance method in database management system, involves
retrieving mirrored backup data during unavailability of database for
read-write operation**

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)
Inventor: KERN R F; MICKA W F; NICK J M ; PERRY L R; PETERSEN D B; SLONE H
G; SPEAR G A; YUDENFRIEND H M

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 6199074	B1	20010306	US 97948529	A	19971009	200138 B
			US 99286389	A	19990405	

Priority Applications (No Type Date): US 99286389 A 19990405; US 97948529 A
19971009

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 6199074	B1	15	G06F-017/30	CIP of application US 97948529	

Abstract (Basic): US 6199074 B1

NOVELTY - Primary and secondary controllers are coupled to multiple primary and secondary storage devices. On detecting unavailability of database for read-write operation, call operations performed by respective controllers are suspended. The mirrored backup data is retrieved from the secondary database and transmitted to the primary controller for further read-write operation.

DETAILED DESCRIPTION - An INDEPENDENT CLAIM is also included for database management system.

USE - In database management system.

ADVANTAGE - Ensures consistency between primary and mirrored backup copies of the database. Reduces data loss and recovers lost data quickly using mirrored backup copies of database.

DESCRIPTION OF DRAWING(S) - The figure shows the flow chart illustrating operational sequence for ensuring consistency between primary and mirrored backup databases.

pp; 15 DwgNo 5/5

Title Terms: DATA; MAINTAIN; METHOD; DATABASE; MANAGEMENT; SYSTEM;
RETRIEVAL; MIRROR; DATA; DATABASE; READ; WRITING; OPERATE

Derwent Class: T01

International Patent Class (Main): G06F-017/30

File Segment: EPI

13/5/6 (Item 6 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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013162896 **Image available**
WPI Acc No: 2000-334769/200029
XRPX Acc No: N00-252460

**Data structure assignment result preview for information processing
system, involves returning dependent parameter values of data structure**

defined with independent parameter values received from the requester

Patent Assignee: IBM CORP (IBM) ; INT BUSINESS MACHINES CORP (IBM)

Inventor: DAHLEN D J; ELKO D A

Number of Countries: 002 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 2000105722	A	20000411	JP 99110624	A	19990419	200029 B
US 6237000	B1	20010522	US 9871555	A	19980501	200130

Priority Applications (No Type Date): US 9871555 A 19980501

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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JP 2000105722	A		39	G06F-012/00	
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US 6237000	B1			G06F-017/30	
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Abstract (Basic): JP 2000105722 A

NOVELTY - A set of independent parameter values defining a data structure are received from requester. A set of dependent parameter values of the data structure defined with independent parameter values, are determined and returned to the requester.

DETAILED DESCRIPTION - An INDEPENDENT CLAIM is also included for result preview apparatus.

USE - For previewing assignment result of data structure such as the cache structure, list structure in information processing system.

DESCRIPTION OF DRAWING(S) - The figure shows the block diagram of data processing system.

pp; 39 DwgNo 1/22

Title Terms: DATA; STRUCTURE; ASSIGN; RESULT; PREVIEW; INFORMATION; PROCESS ; SYSTEM; RETURN; DEPEND; PARAMETER; VALUE; DATA; STRUCTURE; DEFINE;

INDEPENDENT; PARAMETER; VALUE; RECEIVE

Derwent Class: T01

International Patent Class (Main): G06F-012/00; G06F-017/30

International Patent Class (Additional): G06F-012/08

File Segment: EPI

13/5/7 (Item 7 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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013034250 **Image available**

WPI Acc No: 2000-206101/200018

XRPX Acc No: N00-153247

Object run time services system for running objects on computer, extracts method invocations from message and issues it to object so that client can asynchronously invoke object methods

Patent Assignee: MICROSOFT CORP (MICR-N); MICROSOFT CORP (MICT)

Inventor: AL-GHOSEIN M; CHOPRA G; DIEVENDORFF R ; HELLAND P J

Number of Countries: 021 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
WO 200010080	A1	20000224	WO 99US18749	A	19990817	200018 B
EP 1025493	A1	20000809	EP 99942278	A	19990817	200039
			WO 99US18749	A	19990817	
US 6425017	B1	20020723	US 98135378	A	19980817	200254
JP 2002522843	W	20020723	WO 99US18749	A	19990817	200263
			JP 2000565459	A	19990817	

Priority Applications (No Type Date): US 98135378 A 19980817

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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WO 200010080	A1	E	49	G06F-009/44	
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Designated States (National): JP

Designated States (Regional): AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

EP 1025493	A1	E		G06F-009/44	Based on patent WO 200010080
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Designated States (Regional): AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE

US 6425017 B1 G06F-009/54
JP 2002522843 W 58 G06F-009/44 Based on patent WO 200010080

Abstract (Basic): WO 200010080 A1

NOVELTY - A recorder (150) is operated to intercept several method invocations issued in a transaction by a client, to invoke methods of object. The method invocations are recorded as a message and a **message queue** is then formed. A player (154) extracts the invocations from the message and issues it to the object. The client invokes the methods of the object asynchronously.

DETAILED DESCRIPTION - An INDEPENDENT CLAIM is also included for a method for a client to access object method.

USE - For distributed component based computer software applications.

ADVANTAGE - The object framework or environment provides automatic marshaling support for queued message invocations. Allows client to issue and receive method invocations on queued basis using normal call semantics of object model without use of message queuing API. The application developer need not program the client and object to use message queuing API and avoids the needs to learn such API.

DESCRIPTION OF DRAWING(S) - The figure shows block diagram of execution environment and run-time architecture for queued method invocations.

Recorder (150)

Player (154)

pp; 49 DwgNo 2/10

Title Terms: OBJECT; RUN; TIME; SERVICE; SYSTEM; RUN; OBJECT; COMPUTER; EXTRACT; METHOD; MESSAGE; ISSUE; OBJECT; SO; CLIENT; CAN; ASYNCHRONOUS; INVOKE; OBJECT; METHOD

Derwent Class: T01

International Patent Class (Main): G06F-009/44; G06F-009/54

International Patent Class (Additional): G06F-009/46

File Segment: EPI

13/5/8 (Item 8 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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012229197 **Image available**

WPI Acc No: 1999-035304/199903

XRPX Acc No: N99-026369

Batch delivery system with automatic overfill compensation - performs number of trial batch runs to derive information used to calculate overfill estimation function subsequently used to run production batches

Patent Assignee: MICRO MOTION INC (MICR-N)

Inventor: **FLAHERTY D T**

Number of Countries: 031 Number of Patents: 010

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
WO 9854553	A1	19981203	WO 98US9251	A	19980506	199903 B
AU 9872906	A	19981230	AU 9872906	A	19980506	199918
US 5975747	A	19991102	US 97864639	A	19970529	199953
EP 985136	A1	20000315	EP 98920300	A	19980506	200018
			WO 98US9251	A	19980506	
BR 9810238	A	20000905	BR 9810238	A	19980506	200048
			WO 98US9251	A	19980506	
CN 1258349	A	20000628	CN 98805563	A	19980506	200050
MX 9910946	A1	20000401	MX 9910946	A	19991126	200124
JP 2001506761	W	20010522	WO 98US9251	A	19980506	200134
			JP 99500692	A	19980506	
KR 2001013144	A	20010226	KR 99711126	A	19991129	200154
JP 3296568	B2	20020702	WO 98US9251	A	19980506	200246
			JP 99500692	A	19980506	

Priority Applications (No Type Date): US 97864639 A 19970529

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes

WO 9854553 A1 E 25 G01F-025/00
 Designated States (National): AU BR CA CN ID JP KR MX PL RU SG
 Designated States (Regional): AT BE CH CY DE DK ES FI FR GB GR IE IT LU
 MC NL PT SE
 AU 9872906 A Based on patent WO 9854553
 US 5975747 A G06F-017/00
 EP 985136 A1 E G01F-025/00 Based on patent WO 9854553
 Designated States (Regional): CH DE FR GB LI
 BR 9810238 A G01F-025/00 Based on patent WO 9854553
 CN 1258349 A G01F-025/00
 MX 9910946 A1 G01F-025/00
 JP 2001506761 W 43 G01F-013/00 Based on patent WO 9854553
 KR 2001013144 A G01F-025/00
 JP 3296568 B2 15 G01F-013/00 Previous Publ. patent JP 200106761
 Based on patent WO 9854553

Abstract (Basic): WO 9854553 A

The batch delivery system runs a number of trial batches in which material is delivered from a source (101) to a destination. The flow rate of the material is determined during the running of each batch, and the trial data, including the determined flow rate and the amount of material overfill for each batch, is accumulated and recorded. The recorded data is used for determining an estimated material overfill amount for any flow rate. Subsequently, material is delivered in production batches with each batch having associated with it data specifying the size of the batch to be delivered. The flow rate is measured and the estimated material overfill amount for the measured flow rate is determined for at least one subsequent batch. The estimated material overfill amount is then subtracted from the target amount to derive a material flow termination signal amount. The material flow is terminated in response to the termination signal amount to deliver the target amount of material to the destination.

ADVANTAGE - Can detect possible malfunction of flow control valve or pump.

Dwg.1/11

Title Terms: BATCH; DELIVER; SYSTEM; AUTOMATIC; OVERFILL; COMPENSATE;
 PERFORMANCE; NUMBER; TRIAL; BATCH; RUN; DERIVATIVE; INFORMATION;
 CALCULATE; OVERFILL; ESTIMATE; FUNCTION; SUBSEQUENT; RUN; PRODUCE; BATCH
 Derwent Class: Q39; S02
 International Patent Class (Main): G01F-013/00; G01F-025/00; **G06F-017/00**
 International Patent Class (Additional): B67D-003/00; B67D-005/30;
 B67D-005/34
 File Segment: EPI; EngPI

13/5/9 (Item 9 from file: 350)
 DIALOG(R)File 350:Derwent WPIX
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011822732 **Image available**
 WPI Acc No: 1998-239642/199821

Log data management method for coupled data processing system - involves providing real-time merge of data records in particular sequence by performing serialisation for log stream at coupling facility

Patent Assignee: INT BUSINESS MACHINES CORP (IBM)
 Inventor: GEINER R V; **NICK J M** ; PHILLIPS M; **WARNES J H** ; ZIMMER D J
 Number of Countries: 001 Number of Patents: 001
 Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5737600	A	19980407	US 94304677	A	19940912	199821 B
			US 96632683	A	19960415	

Priority Applications (No Type Date): US 94304677 A 19940912; US 96632683 A 19960415

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 5737600	A		61	G06F-017/30	Cont of application US 94304677

Abstract (Basic): US 5737600 A

The method for managing log data in a data processing system involves processor writing data records to a log stream. The log stream includes a portion of a coupling facility storage structure residing within a coupling facility. The coupling facility is sharable by the processors and includes a processor and a communications facility.

The writing involves providing a real-time merge of the data records in a particular sequence. This involves performing serialisation for the log stream at the coupling facility. The processor are relieved of providing the serialisation for the log stream.

USE - For coupled data processing system with multiple users, either in single system or multi-system environment.

ADVANTAGE - Provides real time physical log merge of data written into log stream without inter-processor communication required to process records in logical sequence. Logical block id is assigned to each log record written so that it can be directly accessed at later time.

Dwg.2/20

Title Terms: LOG; DATA; MANAGEMENT; METHOD; COUPLE; DATA; PROCESS; SYSTEM; REAL; TIME; MERGE; DATA; RECORD; SEQUENCE; PERFORMANCE; LOG; STREAM; COUPLE; FACILITY

Derwent Class: T01

International Patent Class (Main): G06F-017/30

File Segment: EPI

13/5/10 (Item 10 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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010456974 **Image available**

WPI Acc No: 1995-358293/199546

XRFX Acc No: N95-266296

Data handling control method for SES computer complex - involves including CPCs and SES in sysplex with cache controls and directories receiving data items from CPCs for storage in SES

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)

Inventor: ELKO D A ; FREY J A; MOORE B B; NICK J M ; SMITH K F; SWANSON M D

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5457793	A	19951010	US 92860807	A	19920330	199546 B

Priority Applications (No Type Date): US 92860807 A 19920330

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 5457793	A	39	G06F-015/167	

Abstract (Basic): US 5457793 A

The method involves structuring a sysplex to include many CPCs, in which each of the CPCs contains one or more control programs controlling internal CPC operations. Sysplex DASDs commonly connected to the CPCs for storing CPC shared data items permanently in the sysplex are included along with SES attached to the CPCs. One of the CPCs is controlled to execute a program issuing an allocation command to the SES with operand parameters specifying space in SES. The space is needed to set up cache controls, a cache directory, and a data area for receiving data items from attached CPCs to be stored in the SES cache.

The SES cache not previously existing in the SES is generated in response to the allocation command when specified space exists in SES based on the specified operand parameters from which the PG,72 SES cache is formed. The generating step provides in each cache directory entry a data name field and a storage class field. The data name field receives a name for a data item to be written in a corresponding data area in the cache and the storage class field for receiving an

identifier of a storage class for the directory entry and the data item.

ADVANTAGE - Provides isolation of data items in storage class where replacement not currently allowed, assuring continued availability to attached CPCs of data items in SES cache. Allows CPC to control storage performance of SES cache, including reallocation for directory entries and data items among numerous storage classes. Controls access hit ratios among numerous storage classes.

Dwg.6a/18

Title Terms: DATA; HANDLE; CONTROL; METHOD; COMPUTER; COMPLEX; CACHE;
CONTROL; DIRECTORY; RECEIVE; DATA; ITEM; STORAGE
Index Terms/Additional Words: DATA; HANDLE; CONTROL; METHOD; COMPUTER;
COMPLEX; CACHE; CONTROL; DIRECTORY; RECE
Derwent Class: T01
International Patent Class (Main): G06F-015/167
International Patent Class (Additional): G06F-017/30
File Segment: EPI

13/5/11 (Item 11 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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010442230 **Image available**

WPI Acc No: 1995-343549/199544

XRFX Acc No: N95-256674

**Concurrent management of facility shared by number of processing systems
- associating authority value with objects in shared structured
electronic storage facility, while object includes control and status
information**

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)

Inventor: ELKO D A ; FREY J A; HELFFRICH A A; NICK J M ; SWANSON M D

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5450590	A	19950912	US 9321285	A	19930222	199544 B
			US 95408446	A	19950322	

Priority Applications (No Type Date): US 9321285 A 19930222; US 95408446 A 19950322

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 5450590	A	12	G06F-007/04	Cont of application US 9321285	

Abstract (Basic): US 5450590 A

The method involves accessing by any data processing system stored global values reflecting the status of facility objects including a global authority value. That is followed by issuing a command to the facility by a user by any data processing system, the command to be executed by the facility on a facility object, and including an operand specifying a comparative global authority value.

The method also entails specifying a further operand in the command comprising a new global authority value, then inhibiting execution of the command by the facility if the comparative global authority value is not equal to an existing global authority value stored for the facility object.

USE/ADVANTAGE - In data processing systems through coupling facility. Provision for authorisation as part of execution of certain commands to facility to conditionally execute command if authorisation allows execution.

Dwg.1/7

Title Terms: CONCURRENT; MANAGEMENT; FACILITY; SHARE; NUMBER; PROCESS;
SYSTEM; ASSOCIATE; AUTHORISE; VALUE; OBJECT; SHARE; STRUCTURE; ELECTRONIC
; STORAGE; FACILITY; OBJECT; CONTROL; STATUS; INFORMATION
Derwent Class: T01
International Patent Class (Main): G06F-007/04
International Patent Class (Additional): G06F-012/14
File Segment: EPI

13/5/12 (Item 12 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00644807

System and method for storing persistent and non-persistent queued data
Einrichtung und Verfahren zum Speichern dauerhafter und nicht dauerhafter
Warteschlangendaten

Systeme et procede pour le stockage en queue de donnees persistentes et
non-persistentes

PATENT ASSIGNEE:

International Business Machines Corporation, (200120), Old Orchard Road,
Armonk, N.Y. 10504, (US), (applicant designated states: DE;FR;GB)

INVENTOR:

Dievendorff, Richard , 1211 Janis Way, San Jose, California 95125, (US)
Mohan, Chandrasekaran, 727 Portswood Drive, San Jose, CA 95120, (US)

LEGAL REPRESENTATIVE:

Moss, Robert Douglas (34141), IBM United Kingdom Limited Intellectual
Property Department Hursley Park, Winchester Hampshire SO21 2JN, (GB)

PATENT (CC, No, Kind, Date): EP 623877 A2 941109 (Basic)

EP 623877 A3 950920

EP 623877 B1 990113

APPLICATION (CC, No, Date): EP 94302239 940329;

PRIORITY (CC, No, Date): GB 9306662 930330

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G06F-011/14;

ABSTRACT EP 623877 A2

Disclosed is a method for the storage of data which is required to
survive a system failure (persistent data) and data which is not required
to survive a system failure (non-persistent data) on a single queue.
Individual pages of memory are allocated to the storage of non-persistent
data only or persistent data only. Details of changes made to persistent
data only are recorded in a log. In the event of a system failure, the
queue can have the persistent data only recovered by checking individual
pages, without the necessity of scanning individual entries within those
pages. (see image in original document)

ABSTRACT WORD COUNT: 103

LEGAL STATUS (Type, Pub Date, Kind, Text):

Oppn None: 20000105 B1 No opposition filed: 19991014

Application: 941109 A2 Published application (Alwith Search Report
;A2without Search Report)

Examination: 950315 A2 Date of filing of request for examination:
950117

Search Report: 950920 A3 Separate publication of the European or
International search report

Examination: 970716 A2 Date of despatch of first examination report:
970530

Change: 980506 A2 Title of invention (French) (change)

Grant: 990113 B1 Granted patent

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9902	586
CLAIMS B	(German)	9902	582
CLAIMS B	(French)	9902	696
SPEC B	(English)	9902	4469
Total word count - document A			0
Total word count - document B			6333
Total word count - documents A + B			6333

13/5/13 (Item 13 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2003 European Patent Office. All rts. reserv.

00637151

Fault-tolerant transaction-oriented data processing.

Fehlertolerante transaktionsorientierte Datenverarbeitung.

Traitement des donnees transactionnel tolerant des fautes.

PATENT ASSIGNEE:

International Business Machines Corporation, (200120), Old Orchard Road,
Armonk, N.Y. 10504, (US), (applicant designated states: DE;FR;GB)

INVENTOR:

Dievendorff, Richard , 1211 Janis Way, San Jose, California 95125, (US)
Mohan, Chandrasekaran, 727 Portswood Drive, San Jose, California 95120,
(US)

LEGAL REPRESENTATIVE:

Moss, Robert Douglas (34141), IBM United Kingdom Limited Intellectual
Property Department Hursley Park, Winchester Hampshire SO21 2JN, (GB)

PATENT (CC, No, Kind, Date): EP 618534 A2 941005 (Basic)

EP 618534 A3 950906

APPLICATION (CC, No, Date): EP 94302238 940329;

PRIORITY (CC, No, Date): GB 9306649 930330

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G06F-011/14;

ABSTRACT EP 618534 A2

In transaction processing systems, it is known for resource-updating operations within a transaction to be backed out at the request of an application program following detection of error conditions during processing of the transaction. If the error condition is very likely to recur, it may be undesirable for the operations request to be presented to the application exactly as before. A transaction-oriented data processing system and a method of transaction-oriented data processing are provided in which operation requests or data packets may be marked to be excluded from the effects of application-requested backouts. (see image in original document)

ABSTRACT WORD COUNT: 100

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 941005 A2 Published application (Alwith Search Report
;A2without Search Report)

Examination: 950315 A2 Date of filing of request for examination:
950117

Search Report: 950906 A3 Separate publication of the European or
International search report

Withdrawal: 970723 A2 Date on which the European patent application
was withdrawn: 970521

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF2	496
SPEC A	(English)	EPABF2	10507
Total word count - document A			11003
Total word count - document B			0
Total word count - documents A + B			11003

13/5/14 (Item 14 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00263551

Recovery enhancement in a transaction-oriented data processing system.

Ruckgewinnungserhöhung in transaktionsorientierten Datenverarbeitungssystemen.

Augmentation de restauration dans les systemes de traitement de donnees transactionnels.

PATENT ASSIGNEE:

International Business Machines Corporation, (200120), Old Orchard Road,
Armonk, N.Y. 10504, (US), (applicant designated states: DE;FR;GB)

INVENTOR:

Dievendorff, Richard , 443, South Forestdale Avenue, Covina California
91723, (US)

Jenner, Earle Henderson, 6963 Bret Harte Drive, San Jose California 95120
, (US)

Palmer, John Davis, 1443 Medallion Drive, San Jose California 95120, (US)
LEGAL REPRESENTATIVE:

Blakemore, Frederick Norman et al (28381), IBM United Kingdom Limited
Intellectual Property Department Hursley Park, Winchester Hampshire
SO21 2JN, (GB)

PATENT (CC, No, Kind, Date): EP 280773 A2 880907 (Basic)
EP 280773 A3 891220

APPLICATION (CC, No, Date): EP 87117301 871124;

PRIORITY (CC, No, Date): US 1719 870109

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G06F-011/14;

CITED REFERENCES (EP A):

IBM SYSTEMS JOURNAL, vol. 3, no. 2, 1984, pages 178-188, Armonk, NY, US;

R.A. CRUS: "Data recovery in IBM database 2"

COMPUTING SURVEYS, vol. 15, no. 4, December 1983, pages 287,303-312, ACM,
New York, US; T. HAERDER et al.: "Principles of transaction-oriented
database recovery";

ABSTRACT EP 280773 A2

The number of log records required to be written and read in supporting
the reconstruction of a volatile queue of objects is minimised and
conveniently relocated by selectively relogging on attaining an activity
threshold post start or the most recent checkpoint. Only those objects
which remain in the queue for a long period of time will be rewritten. If
high activity reflects high queue usage, then the objects will qualify
for relogging in a shorter period of clock time.

ABSTRACT WORD COUNT: 83

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 880907 A2 Published application (Alwith Search Report
;A2without Search Report)

Examination: 890125 A2 Date of filing of request for examination:
881126

Search Report: 891220 A3 Separate publication of the European or
International search report

Examination: 920506 A2 Date of despatch of first examination report:
920323

Change: 930210 A2 Representative (change)

Withdrawal: 931124 A2 Date on which the European patent application
was deemed to be withdrawn: 930527

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	314
SPEC A	(English)	EPABF1	5073
Total word count - document A			5387
Total word count - document B			0
Total word count - documents A + B			5387

Set	Items	Description
S1	1387487	MESSAGE OR MESSAGING
S2	63568	S1(2N) (MANAG? OR INTERFACE? OR QUEUE? OR MIDDLEWARE?) OR MQ OR MQI
S3	4657443	COMMIT? OR UNCOMMIT?
S4	4567968	DEFINED() (ORDER? OR RANK? OR ORDER?) OR LIST()ENTRY? OR LI- ST()STRUCTURE? OR KEY? ?
S5	2680	S2(5N) (VALUE? OR HIGHEST? OR LOWEST? OR EXTREME? OR FIRST? OR LAST? OR GREATEST? OR LEAST? OR MOST?)
S6	74	S3(S)S5
S7	97	S4(S)S5
S8	470	S2(3N) (SHARE? OR SHARING?)
S9	3	(S6 OR S7) (S)S8
S10	3	S8 AND (S6 OR S7)
S11	52	S8(S) (S3 OR S4)
S12	2	S8(S)S3(S)S4
S13	30	S8(10N) (S3 OR S4)
S14	5	S5(S)S3(S)S4
S15	3	S5(3N)S3 AND LIST? ?
S16	43	S9 OR S10 OR S12 OR S13 OR S14 OR S15
S17	19	RD (unique items)
S18	15	S17 NOT PY>2000
S19	13	S18 NOT PD>20001002
File 275:	Gale Group Computer DB(TM)	1983-2003/Mar 31 (c) 2003 The Gale Group
File 47:	Gale Group Magazine DB(TM)	1959-2003/Mar 28 (c) 2003 The Gale group
File 75:	TGG Management Contents(R)	86-2003/Mar W4 (c) 2003 The Gale Group
File 636:	Gale Group Newsletter DB(TM)	1987-2003/Mar 31 (c) 2003 The Gale Group
File 16:	Gale Group PROMT(R)	1990-2003/Mar 31 (c) 2003 The Gale Group
File 624:	McGraw-Hill Publications	1985-2003/Apr 01 (c) 2003 McGraw-Hill Co. Inc
File 613:	PR Newswire	1999-2003/Apr 01 (c) 2003 PR Newswire Association Inc
File 813:	PR Newswire	1987-1999/Apr 30 (c) 1999 PR Newswire Association Inc
File 553:	Wilson Bus. Abs. FullText	1982-2003/Feb (c) 2003 The HW Wilson Co
File 621:	Gale Group New Prod. Annou. (R)	1985-2003/Mar 31 (c) 2003 The Gale Group
File 674:	Computer News Fulltext	1989-2003/Mar W4 (c) 2003 IDG Communications
File 88:	Gale Group Business A.R.T.S.	1976-2003/Mar 31 (c) 2003 The Gale Group
File 369:	New Scientist	1994-2003/Mar W3 (c) 2003 Reed Business Information Ltd.
File 160:	Gale Group PROMT(R)	1972-1989 (c) 1999 The Gale Group
File 635:	Business Dateline(R)	1985-2003/Apr 01 (c) 2003 ProQuest Info&Learning
File 15:	ABI/Inform(R)	1971-2003/Apr 01 (c) 2003 ProQuest Info&Learning
File 9:	Business & Industry(R)	Jul/1994-2003/Mar 31 (c) 2003 Resp. DB Svcs.
File 13:	BAMP	2003/Mar W4 (c) 2003 Resp. DB Svcs.
File 810:	Business Wire	1986-1999/Feb 28 (c) 1999 Business Wire
File 610:	Business Wire	1999-2003/Apr 01 (c) 2003 Business Wire.
File 647:	CMP Computer Fulltext	1988-2003/Mar W2 (c) 2003 CMP Media, LLC
File 98:	General Sci Abs/Full-Text	1984-2003/Feb (c) 2003 The HW Wilson Co.
File 148:	Gale Group Trade & Industry DB	1976-2003/Mar 31

19/3,K/1 (Item 1 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

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02336156 SUPPLIER NUMBER: 55730234 (USE FORMAT 7 OR 9 FOR FULL TEXT)

USi, Microsoft ink Exchange pact. (Company Business and Marketing)

Taft, Darryl K.

Computer Reseller News, 76

Sept 13, 1999

ISSN: 0893-8377 LANGUAGE: English

RECORD TYPE: Fulltext

WORD COUNT: 305 LINE COUNT: 00029

... future, Magliato said.

USi is targeting companies with 500 to 5,000 seats that have committed to Exchange. "Our goal is to attain a market share in managed messaging of 50 percent within 12 months," Magliato said.

USi offers services in several other areas...

19/3,K/2 (Item 1 from file: 636)

DIALOG(R)File 636:Gale Group Newsletter DB(TM)

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03806092 Supplier Number: 48253371 (USE FORMAT 7 FOR FULLTEXT)

WEEKLY IBM NEWS SUMMARY

Report on IBM, v15, n4, pN/A

Jan 28, 1998

Language: English Record Type: Fulltext

Document Type: Newsletter; Trade

Word Count: 1449

... critical IMS data sharing in a Parallel Sysplex environment.

Support includes new functions that analyze Shared Message Queues, a key feature of IMS Version 6.1.

- Candle Automation Solutions, including AF/OPERATOR and OMEGACENTER Gateway...

19/3,K/3 (Item 2 from file: 636)

DIALOG(R)File 636:Gale Group Newsletter DB(TM)

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02733989 Supplier Number: 45547451 (USE FORMAT 7 FOR FULLTEXT)

AUTOMATED HANDLING OF MESSAGE ATTACHMENTS CLOSE AT HAND

Electronic Messaging News, v7, n10, pN/A

May 17, 1995

Language: English Record Type: Fulltext

Document Type: Newsletter; Trade

Word Count: 1052

(USE FORMAT 7 FOR FULLTEXT)

TEXT:

...Messaging Association (EMA) (EMN, Sept. 21, 1994, p. 1). Completion of specification testing topped the list of accomplishments by the EMA at last week's committee meetings in New Orleans. Below...

... 400 messages. (Jim McDermott, Chair, 713/656-3897; Barbara Bender, Vice Chair, 813/878 -3254.)

Messaging Management Committee

The Messaging Management Committee at last week's meeting distributed draft copies of a new standard designed to let end-users...

19/3,K/4 (Item 1 from file: 16)

DIALOG(R)File 16:Gale Group PROMT(R)

(c) 2003 The Gale Group. All rts. reserv.

06755844 Supplier Number: 56902610 (USE FORMAT 7 FOR FULLTEXT)

Ventriloquist Provides Crisis Management Solution for Y2K.

Business Wire, p1050
Oct 26, 1999
Language: English Record Type: Fulltext
Document Type: Newswire; Trade
Word Count: 823

... clock turns over revealing no Y2K problems, the CEO calls ITG and records a reassuring **message** telling **management**, employees, **key shareholders**, or analysts to enjoy the evening because all systems are running normally. "Even if you..."

19/3,K/5 (Item 2 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)
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06144576 Supplier Number: 53919969 (USE FORMAT 7 FOR FULLTEXT)
USWeb/CKS E-Services Division Announces Availability of Outsourced Communications and Knowledge Management Services.
Business Wire, p0133
Feb 22, 1999
Language: English Record Type: Fulltext
Document Type: Newswire; Trade
Word Count: 1034

... customers, allow employees to easily share expertise, and build online communities that reinforce brand loyalty. **Key** features include real-time video conferencing, application **sharing** and meeting **management**, instant **messaging** threaded discussions, alert systems and business communication archival.

-- Presentations Central enables businesses to deliver dynamic...

19/3,K/6 (Item 3 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)
(c) 2003 The Gale Group. All rts. reserv.

05432883 Supplier Number: 48238719 (USE FORMAT 7 FOR FULLTEXT)
Candle Corp. Announces Immediate Support for IBM IMS/ESA Version 6.1; Support and Exploitation Includes OMEGAMON II, Automation and Candle Command Center Solutions.
Business Wire, p01210200
Jan 21, 1998
Language: English Record Type: Fulltext
Document Type: Newswire; Trade
Word Count: 436

... critical IMS data sharing in a Parallel Sysplex environment. Support includes new functions that analyze **Shared Message Queues**, a **key** feature of IMS Version 6.1. -- Candle Automation Solutions, including AF/OPERATOR and OMEGACENTER Gateway...

19/3,K/7 (Item 4 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)
(c) 2003 The Gale Group. All rts. reserv.

04578228 Supplier Number: 46730627 (USE FORMAT 7 FOR FULLTEXT)
CANDLE CORPORATION APPOINTS TONY D'ERRICO TO HEAD NEW PROFESSIONAL SERVICES ORGANIZATION
News Release, pN/A
Sept 23, 1996
Language: English Record Type: Fulltext
Document Type: Magazine/Journal; Trade
Word Count: 872

(USE FORMAT 7 FOR FULLTEXT)

TEXT:

...efforts of a new global staff of middleware and systems management specialists. "We are fully **committed** to providing comprehensive solutions to our customers as they face the challenges of a borderless...

...specific business needs. He is the ideal person to grow our services organization into a **key** strategic asset for our customers and us." "We will help customers' businesses by helping them...

...is to make sure that all Candle customers derive significant value from their IT investments." **Key** New Hires Dan Dzikewich joins Candle as an MQSeries application development specialist. Dzikewich brings many...

...MQSeries for MVS, the first MQSeries product. In addition to co-authoring Messaging & Queuing Using the **MQI**, Harris was **most** recently providing independent consulting and design services to large financial institutions reviewing IT middleware systems...

19/3,K/8 (Item 5 from file: 16)

DIALOG(R)File 16:Gale Group PROMT(R)
(c) 2003 The Gale Group. All rts. reserv.

04480376 Supplier Number: 46577437 (USE FORMAT 7 FOR FULLTEXT)
ACROSS DATA SYSTEMS AND CANDLE CORPORATION TO FORM STRATEGIC ALLIANCE
News Release, pN/A
July 29, 1996
Language: English Record Type: Fulltext
Document Type: Magazine/Journal; Trade
Word Count: 703

(USE FORMAT 7 FOR FULLTEXT)

TEXT:

Agreement Signifies Strong **Commitment** to MQSeries-based Solutions and Services NEW YORK, July 29, 1996 -- Across Data Systems, Inc...

...and distribution. Extending Candle's MQ and Messaging Solutions Business This: agreement extends Candle's **MQ** business on three fronts. **First**, the **MQ** /Secure product that Candle will acquire now joins the Candle Command Center monitoring solutions for...

...customers with complementary middleware technology and solutions, and our \$2.7 million investment shows our **commitment** to make this strategic alliance a long-term relationship." About Candle Corporation Candle Corporation is...

...Data Systems, Inc. develops and markets business software and provides consulting and ancillary services. The **key** growth vehicle of Across is the Level 8 subsidiary, which provides products and services that...

19/3,K/9 (Item 1 from file: 15)

DIALOG(R)File 15:ABI/Inform(R)
(c) 2003 ProQuest Info&Learning. All rts. reserv.

02431035 115923193
Strategic change leadership
Graetz, Fiona
Management Decision v38n8 PP: 550 2000
ISSN: 0025-1747 JRNL CODE: MGD
WORD COUNT: 6253

...TEXT: with the backing of the managing director. However, although there was no single figurehead, the **key** team players were **committed** members of the senior executive, not small bit actors. This aligns with the view that...

... and Kochan, 1992). However, it remained to be seen whether the less

direct involvement of **key** senior executives, in particular the managing director, would be detrimental to the long-term success...

...and power base. Unless the management development programme succeeded in unlocking old behaviours, attitudes and **values** among its middle and senior **management**, the **message** from its "guiding coalition" would be seen as inconsistent and thus discredited.
Communicating the message...

19/3,K/10 (Item 2 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)
(c) 2003 ProQuest Info&Learning. All rts. reserv.

01960849 47034343

What you know about harassment can hurt you, too

Milite, George A

HR Focus v76n12 PP: 9-10 Dec 1999

ISSN: 1059-6038 JRNL CODE: PER

WORD COUNT: 1703

...TEXT: program to eradicate sexual harassment has five components, says Orlov. First, "you have to have **commitment** from top **management**. The **message** has to come from the **highest** levels that the company will not tolerate sexual harassment, or situations that could be construed...policy should cover any and all possibilities. Darlene Orlov has put together a "Top 10 **List**," which appears in her new book, What Every Manager Needs to Know about Sexual Harassment...

19/3,K/11 (Item 1 from file: 9)
DIALOG(R)File 9:Business & Industry(R)
(c) 2003 Resp. DB Svcs. All rts. reserv.

02573227 (USE FORMAT 7 OR 9 FOR FULLTEXT)

USi, Microsoft ink Exchange pact

(Internet reseller USinternetworking Inc (USi) and software giant Microsoft Corp have teamed up to offer a managed messaging service using Microsoft Exchange)

Computer Reseller News, p 76

September 13, 1999

DOCUMENT TYPE: Journal ISSN: 0893-8377 (United States)

LANGUAGE: English RECORD TYPE: Fulltext

WORD COUNT: 278

(USE FORMAT 7 OR 9 FOR FULLTEXT)

TEXT:

...future, Magliato said.

USi is targeting companies with 500 to 5,000 seats that have **committed** to Exchange. "Our goal is to attain a market **share** in **managed messaging** of 50 percent within 12 months," Magliato said.

USi offers services in several other areas...

19/3,K/12 (Item 1 from file: 647)
DIALOG(R)File 647:CMP Computer Fulltext
(c) 2003 CMP Media, LLC. All rts. reserv.

01199893 CMP ACCESSION NUMBER: CRN19990913S0051

USi, Microsoft ink Exchange pact

Darryl K. Taft

COMPUTER RESELLER NEWS, 1999, n 859, PG76

PUBLICATION DATE: 990913

JOURNAL CODE: CRN LANGUAGE: English

RECORD TYPE: Fulltext

SECTION HEADING: E-Business
WORD COUNT: 281

... future, Magliato said.

USi is targeting companies with 500 to 5,000 seats that have committed to Exchange. "Our goal is to attain a market share in managed messaging of 50 percent within 12 months," Magliato said.

USi offers services in several other areas...

19/3,K/13 (Item 1 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB
(c)2003 The Gale Group. All rts. reserv.

08979481 SUPPLIER NUMBER: 18696495 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Distributed applications bend RTOS rules. (real-time operating system) (Special Report on Embedded Systems) (Technology Information)

Ready, Jim

Electronic Engineering Times, n919, p69(2)

Sep 16, 1996

ISSN: 0192-1541 LANGUAGE: English RECORD TYPE: Fulltext; Abstract

WORD COUNT: 2619 LINE COUNT: 00207

...ABSTRACT: with intertasking of communication services. The software needs to be able to pass pointers in **message queues** and abstract away **shared** memory dependency to allow true transparent message passing. **Key** application-development issues include hardware and software independence and asynchronous versus synchronous behavior.